

CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 2 (Non-graded))

Question # 2 of 5 (Start time: 10:28:44 PM, 16 November 2023)

The decimal representation of 0x17 is _____.

Select the correct option

☒	23
☐	32
☐	21
☐	17

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 2 (Non-graded))

Question # 3 of 5 (Start time: 10:30:25 PM, 16 November 2023)

numl: db 75, 50, 77, 82

Which of the following accesses the third element i.e. 77?

Select the correct option

☒	[numl+2]
☐	numl[2]
☐	[numl+3]
☐	[numl+4]

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 2 (Non-graded))

Question # 4 of 5 (Start time: 10:32:20 PM, 16 November 2023)

Which of following jump instruction is an unconditional jump?

Select the correct option

- | | |
|----------------------------------|------|
| ☒ | jmp |
| ☐ | jnz |
| ☐ | jns |
| ☐ | jcxz |

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 2 (Non-graded))

Question # 5 of 5 (Start time: 10:33:09 PM, 16 November 2023)

Which of following is an incorrect instruction?

Select the correct option

☐	mov ax, [bx]
☐	mov [ax], bx
☒	mov [ax], [bx]
☐	mov ax, 20

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 2 (Non-graded))

Question # 1 of 5 (Start time: 10:35:56 PM, 16 November 2023)

_____ is used to hold the address of current instruction to be executed

Select the correct option

☐	IR
☐	Flag
☒	PC
☐	Stack

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 2 (Non-graded))

Question # 3 of 5 (Start time: 10:38:44 PM, 16 November 2023)

numl: db 75, 50, 77, 82

Which of the following accesses the third element i.e. 77?

Select the correct option

☐	numl[2]
☒	[numl+2]
☐	[numl+4]
☐	[numl+3]

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 2 (Non-graded))

Question # 4 of 5 (Start time: 10:40:59 PM, 16 November 2023)

What is correct syntax for move instruction?

Select the correct option

- | | |
|----------------------------------|--------------------|
| ☒ | mov operand, value |
| ☐ | mov value, operand |
| ☐ | mov operand value |
| ☐ | move value operand |

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 2 (Non-graded))

Question # 4 of 5 (Start time: 10:40:59 PM, 16 November 2023)

What is correct syntax for move instruction?

Select the correct option

- | | |
|----------------------------------|--------------------|
| ☒ | mov operand, value |
| ☐ | mov value, operand |
| ☐ | mov operand value |
| ☐ | move value operand |

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 2 (Non-graded))

Question # 5 of 5 (Start time: 10:42:22 PM, 16 November 2023)

"mov [bx], ax" moves the two bytes contents of the AX register to the address contained in BX register in the current _____.

Select the correct option

☐	Code Segment
☐	Stack Segment
☐	Extra Segment
☒	Data Segment

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 3 (Non-graded))

Question # 1 of 5 (Start time: 10:24:21 PM, 18 November 2023)

Jump will be taken by 'JNZ' instruction if _____

Select the correct option

☒	Jump if the zero flag is not set
☐	Jump if the sign flag is not set
☐	Jump if the zero flag is set
☐	Jump if the sign flag is set

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 3 (Non-graded))

Question # 2 of 5 (Start time: 10:25:33 PM, 18 November 2023)

What symbol is used to define a label in the assembly language programming?

Select the correct option

☐	Tab
☒	: (colon)
☐	; (semi-colon)
☐	Space

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 3 (Non-graded))

Question # 3 of 5 (Start time: 10:26:36 PM, 18 November 2023)

----- jump instruction is independent of flags.

Select the correct option

☒	JCXZ
☐	JNE
☐	JNS
☐	JO

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 3 (Non-graded))

Question # 4 of 5 (Start time: 10:28:34 PM, 18 November 2023)

_____ is an illegal instruction.

Select the correct option

- | | |
|----------------------------------|----------------|
| ☐ | MOV AX,65 |
| ☒ | MOV AX,[BX+BP] |
| ☐ | Mov BX, 10 |
| ☐ | MOV AX,BX |

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 3 (Non-graded))

Question # 2 of 5 (Start time: 10:33:30 PM, 18 November 2023)

JCXZ jump is taken, if _____ is zero.

Select the correct option

☒	CX
☐	BX
☐	DX
☐	AX

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 3 (Non-graded))

Question # 4 of 5 (Start time: 10:35:43 PM, 18 November 2023)

_____ or _____ are taken if the last arithmetic operation has produced a number in its destination that has odd parity.

Select the correct option

☐	JCX, JNS
☐	JS, JNS
☐	JNP, JPO
☒	JP, JPE

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 3 (Non-graded))

Question # 1 of 5 (Start time: 10:39:09 PM, 18 November 2023)

How many bits in the memory will be reserved by the following instruction?

```
num: dw 1,2,3
```

Select the correct option

☒	48
☐	24
☐	3
☐	6

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 3 (Not graded))

Question # 3 of 5 (Start time: 10:41:18 PM, 18 November 2023)

Which of following jump is not dependent on any flag?

Select the correct option

- | | |
|----------------------------------|-----|
| ☐ | jna |
| ☐ | jnz |
| ☒ | jmp |
| ☐ | jnc |

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 3 (Not graded))

Question # 4 of 5 (Start time: 10:42:31 PM, 18 November 2023)

1 db is equivalent to _____ dw

Select the correct option

☐	0
☐	2
☐	1
☒	0.5

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 4 (Non-Graded))

Question # 1 of 5 (Start time: 10:45:45 PM, 21 November 2023)

If we multiply two 4-bit numbers, the result will be in _____ bits.

Select the correct option

☐	16
☒	8
☐	4
☐	32

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 4 (Non-Graded))

Question # 2 of 5 (**Start time: 10:47:12 PM, 21 November 2023**)

_____ is used to hold address of the next instruction to be executed.

Select the correct option

☐	Memory address registe
☒	Program counter
☐	Memory data register
☐	Instruction register

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 4 (Non-Graded))

Question # 3 of 5 (Start time: 10:48:11 PM, 21 November 2023)

1 nibble is equivalent to

Select the correct option

- | | |
|----------------------------------|-----------|
| ☐ | 0.5 bit |
| ☐ | 2 bytes |
| ☐ | 1 byte |
| ☒ | 0.5 bytes |

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What will be the final value in the ax register after execution of the following code?

```
mov ax,10
```

```
inc ax
```

```
mov ax,1
```

```
add ax,20
```

Select the correct option

☐	31
☐	20
☒	21
☐	32

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 4 (Non-Graded))

Question # 5 of 5 (**Start time: 10:50:54 PM, 21 November 2023**)

Intel 8088 stack works on _____ sized elements.

Select the correct option

☐	bit
☒	word
☐	nibble
☐	byte

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 4 (Non-Graded))

Question # 3 of 5 (Start time: 11:04:38 PM, 21 November 2023)

In near jump the jump can be anywhere within a _____.

Select the correct option

☒	segment
☐	block
☐	code file
☐	program

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz
No. 4 (Non-Graded))

Question # 4 of 5 (Start time: 11:05:59 PM, 21 November 2023)

What would you do, when you want the counter to be increased by 2? which of following options matches the best with scenario?

Select the correct option

☐	use the inc instruction twice
☐	Use the add instruction and add 2 in the counter register
☒	You can use inc instruction twice or use add instruction to add 2 in the counter register
☐	add 2 in the cx registers

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 1 (Graded))

Question # 1 of 10 (**Start time: 06:32:44 PM, 25 November 2023**)

If we multiply two 4-bit numbers, the result will be in _____ bits.

Select the correct option

☐	16
☐	32
☒	8
☐	4

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 1 (Graded))

Question # 2 of 10 (Start time: 06:34:22 PM, 25 November 2023)

What symbol is used to define a label in the assembly language programming?

Select the correct option

☐	Space
☒	: (colon)
☐	; (semi-colon)
☐	Tab

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 1 (Graded))

Question # 3 of 10 (Start time: 06:35:22 PM, 25 November 2023)

Intel 8088 stack works on _____ sized elements.

Select the correct option

☐	nibble
☒	word
☐	byte
☐	bit

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 1 (Graded))

Question # 4 of 10 (Start time: 06:36:26 PM, 25 November 2023)

Which one is correct extension of list file and assembly file?

Select the correct option

- | | |
|----------------------------------|-----------------------------|
| ☒ | .lst and .asm respectively |
| ☐ | .lit and .asm respectively |
| ☐ | .lt and .am respectively |
| ☐ | .lst and .assm respectively |

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 1 (Graded))

Quiz Stc

Question # 5 of 10 (Start time: 06:37:54 PM, 25 November 2023)

Suppose there is a folder named as "AssmSoft" in the root directory of drive C and you want to mount it in DOSBox as drive M. Which of the following is a correct command to do this?

Select the correct option

- | | |
|----------------------------------|-------------------------|
| ☒ | mount M: C://AssmSoft |
| ☐ | mount M: C://AssmSoft |
| ☐ | mount C://AssmSoft M: |
| ☐ | mount C://AssmSoft M: / |

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 1 (Graded))

Question # 6 of 10 (Start time: 06:39:51 PM, 25 November 2023)

"mov [bp], al" moves the one byte contents of the AL register to the address contained in the BP register in the current

Select the correct option

☒	Stack Segment
☐	Code Segment
☐	Extra Segment
☐	Data Segment

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 1 (Graded))

Question # 7 of 10 (Start time: 06:41:43 PM, 25 November 2023)

Which of following is a correct instruction to debug the assembly language program?

Select the correct option

☒	afd FileName.asm
☐	afd FileName.com
☐	open FileName.asm
☐	debug FileName.asm

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 1 (Graded))

Question # 8 of 10 (Start time: 06:43:32 PM, 25 November 2023)

The decimal representation of 0x17 is _____.

Select the correct option

☐	32
☒	23
☐	21
☐	17

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 1 (Graded))

Question # 9 of 10 (Start time: 06:44:59 PM, 25 November 2023)

Which software is used to emulate the Disk Operating System?

Select the correct option

☐	PowerShell
☐	NASM
☒	DOSBOX
☐	AFD

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 1 (Graded))

Question # 10 of 10 (Start time: 06:45:52 PM, 25 November 2023)

Which of following is an incorrect instruction?

Select the correct option

☐	mov ax, [bx]
☐	mov [ax], [bx]
☒	mov ax, 20
☐	mov [ax], bx

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 1 (Graded))

Question # 3 of 10 (Start time: 06:51:46 PM, 25 November 2023)

What is correct syntax for move instruction?

Select the correct option

☐	mov value, operand
☐	mov operand value
☒	mov operand, value
☐	move value operand

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 1 (Graded))

Question # 4 of 10 (Start time: 06:53:13 PM, 25 November 2023)

SP is decremented by _____ when an element is pushed on the stack.

Select the correct option

☐	3
☐	1
☒	2
☐	4

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 1 (Graded))

Question # 5 of 10 (Start time: 06:54:44 PM, 25 November 2023)

RET pops the _____ at the top of the stack into the instruction pointer and increments SP by _____.

Select the correct option

☒	word, two
☐	byte, one
☐	word, one
☐	byte, two

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 1 (Graded))

Question # 8 of 10 (Start time: 06:56:53 PM, 25 November 2023)

The value of _____ is recovered from the stack when RET instruction is executed.

Select the correct option

☐	DS
☐	PC
☒	IP
☐	SP

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 1 (Graded))

Question # 1 of 10 (**Start time: 07:00:38 PM, 25 November 2023**)

"mov [bx], ax" moves the two bytes contents of the AX register to the address contained in BX register in the current -----.

Select the correct option

☐	Code Segment
☒	Data Segment
☐	Extra Segment
☐	Stack Segment

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 1 (Graded))

Question # 4 of 10 (Start time: 07:04:53 PM, 25 November 2023)

_____ is the correct syntax for NOT operation?

Select the correct option

☐	not byte [var], ax
☐	not bx
☐	not ax, bx
☒	not word [var], ax

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 1 (Graded))

Question # 7 of 10 (Start time: 07:08:03 PM, 25 November 2023)

Jump will be taken by 'JNZ' instruction if -----

Select the correct option

- | | |
|----------------------------------|----------------------------------|
| ☐ | Jump if the zero flag is set |
| ☐ | Jump if the sign flag is not set |
| ☒ | Jump if the zero flag is not set |
| ☐ | Jump if the sign flag is set |

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 1 (Graded))

Question # 8 of 10 (Start time: 07:09:24 PM, 25 November 2023)

_____ is an illegal instruction.

Select the correct option

☐	MOV AX,65
☐	Mov BX, 10
☒	MOV AX,[BX+BP]
☐	MOV AX,BX

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 1 (Graded))

Question # 9 of 10 (Start time: 07:10:44 PM, 25 November 2023)

Which of following is a correct instruction to debug the assembly language program?

Select the correct option

- | | |
|----------------------------------|--------------------|
| ☒ | afd FileName.com |
| ☐ | open FileName.asm |
| ☐ | debug FileName.asm |
| ☐ | afd FileName.asm |

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 1 (Graded))

Question # 10 of 10 (Start time: 07:12:49 PM, 25 November 2023)

The intel 8085 processor can access maximum _____of memory.

Select the correct option

☐	2 MB
☐	1 MB
☐	85 KB
☒	64 KB

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 5 (Non-Graded))

Question # 1 of 5 (**Start time: 08:40:33 PM, 28 November 2023**)

0x represents

Select the correct option

☐	Decimal Numbers
☐	Binary Number
☒	Hexadecimal Numbers
☐	0 Based Numbers

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 5 (Non-Graded))

Question # 2 of 5 (Start time: 08:42:08 PM, 28 November 2023)

The value of _____ is recovered from the stack when RET instruction is executed.

Select the correct option

☒	PC
☐	SP
☐	DS
☐	IP

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 5 (Non-Graded))

Question # 3 of 5 (Start time: 08:43:08 PM, 28 November 2023)

Near Calls are also called _____.

Select the correct option

☐	Small call
☐	Close call
☒	intra segment
☐	inter segment

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 5 (Non-Graded))

Question # 4 of 5 (Start time: 08:44:03 PM, 28 November 2023)

RET pops the _____ at the top of the stack into the instruction pointer and increments SP by _____.

Select the correct option

☒	word, two
☐	word, one
☐	byte, two
☐	byte, one

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 5 (Non-Graded))

Question # 5 of 5 (Start time: 08:44:51 PM, 28 November 2023)

Shift Arithmetic Left (SAL) can also be called as

Select the correct option

- | | |
|----------------------------------|---------------------|
| ☐ | Rotate right |
| ☒ | Shift Logical Left |
| ☐ | Rotate Left |
| ☐ | Shift Logical Right |

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 5 (Non-Graded))

Question # 4 of 5 (Start time: 08:47:50 PM, 28 November 2023)

When all the general registers are used then a subroutine can receive maximum _____ parameters.

Select the correct option

☐	6
☐	4
☐	5
☒	7

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 6 (Non-Graded))

Question # 1 of 5 (Start time: 08:50:01 PM, 01 December 2023)

mov byte [swap], 0

What would happen if we dont use the keyword byte or word before the operand?

Select the correct option

- | | |
|----------------------------------|--|
| ☐ | Its useless to use these keywords |
| ☐ | Its good to use, otherwise size mismatch error may occur |
| ☐ | Nothing will happen, program will run smoothly all the time |
| ☒ | Nothing will happen but its good practice to use theses keywords |

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 6 (Non-Graded))

Question # 5 of 5 (Start time: 08:58:05 PM, 01 December 2023)

If operand is 16bits in DIV instruction then dividend will be ____

Select the correct option

- | | |
|----------------------------------|---------|
| ☐ | 64-bits |
| ☒ | 8-bits |
| ☐ | 16-bits |
| ☐ | 32-bits |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 6 (Non-Graded))

Question # 1 of 5 (**Start time: 09:10:06 PM, 01 December 2023**)

MOVS instruction is used to _____

Select the correct option

☐	Move register to register
☐	Move a memory location to register
☐	Move register to a memory location
☒	Move memory to memory

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 6 (Non-Graded))

Question # 2 of 5 (Start time: 09:11:55 PM, 01 December 2023)

The execution of the instruction "mov word [ES : 160], 0x1230" will print a character on the screen at:

Select the correct option

☒	First column of second row
☐	First column of third row
☐	Second column of second row
☐	Second column of first row

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 6 (Non-Graded))

Question # 3 of 5 (Start time: 09:14:55 PM, 01 December 2023)

Our computers screen is like a 2-D array having _____ rows and _____ columns.

Select the correct option

- | | |
|----------------------------------|--------|
| ☐ | 25, 40 |
| ☐ | 80, 25 |
| ☐ | 40, 25 |
| ☒ | 25, 80 |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 6 (Non-Graded))

Question # 5 of 5 (Start time: 09:18:38 PM, 01 December 2023)

If operand is 16bits in DIV instruction then dividend will be ____

Select the correct option

- | | |
|----------------------------------|---------|
| ☐ | 16-bits |
| ☐ | 64-bits |
| ☒ | 8-bits |
| ☐ | 32-bits |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 6 (Non-Graded))

Question # 2 of 5 (Start time: 09:24:53 PM, 01 December 2023)

The execution of the instruction "mov word [ES : 160], 0x1230" will print a character on the screen at:

Select the correct option

☐	Second column of second row
☒	First column of third row
☐	Second column of first row
☐	First column of second row

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 6 (Non-Graded))

Question # 3 of 5 (Start time: 09:26:34 PM, 01 December 2023)

To access the video memory _____register is used.

Select the correct option

☐	BH
☐	DI
☒	ES
☐	AX

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 6 (Non-Graded))

Question # 4 of 5 (Start time: 09:28:54 PM, 01 December 2023)

`mov byte [swap], 0`

What would happen if we dont use the keyword byte or word before the operand?

Select the correct option

- | | |
|----------------------------------|--|
| ☐ | Nothing will happen, program will run smoothly all the time |
| ☐ | Nothing will happen but its good practice to use theses keywords |
| ☒ | Its good to use, otherwise size mismatch error may occur |
| ☐ | Its useless to use these keywords |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 6 (Non-Graded))

Question # 5 of 5 (Start time: 09:30:16 PM, 01 December 2023)

Which of following jump would perform better to sort the signed numbers?

Select the correct option

☐	jz
☐	jcx
☒	jge
☐	jle

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 7 (Non-Graded))

Question # 1 of 5 (Start time: 11:20:47 PM, 05 December 2023)

Which of the following is base address for displaying any character in the Screen?

Select the correct option

- | | |
|----------------------------------|--------|
| ☐ | 0b800 |
| ☐ | 0b800x |
| ☐ | 0xb80 |
| ☒ | 0xb800 |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 7 (Non-Graded))

Question # 2 of 5 (Start time: 11:22:44 PM, 05 December 2023)

The last location of word, corresponding to the screen will be _____

Select the correct option

- | | |
|----------------------------------|------|
| ☐ | 1998 |
| ☒ | 3998 |
| ☐ | 2000 |
| ☐ | 4000 |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 7 (Non Graded))

Question # 3 of 5 (Start time: 11:25:03 PM, 05 December 2023)

In string instructions, the mode is called auto-increment mode when:

Select the correct option

☐	CX is set
☐	ZF is cleared
☒	DF is cleared
☐	DF is set

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 7 (Non-Graded))

Question # 4 of 5 (Start time: 11:26:11 PM, 05 December 2023)

REPE repeat the following string instruction while the _____ is set.

Select the correct option

☐	Carry flag
☒	Zero flag
☐	Direction flag
☐	Parity flag

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 7 (Non-Graded))

Question # 5 of 5 (Start time: 11:27:41 PM, 05 December 2023)

Push instruction, will store the value of operand used, in the _____

Select the correct option

☒	SP
☐	BP
☐	AX
☐	Flag

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 7 (Non-Graded))

Question # 2 of 5 (Start time: 11:31:10 PM, 05 December 2023)

MOV AX, 0XB800 MOV ES, AX ; This instruction points ES to_____.

Select the correct option

☐	audio base
☒	video base
☐	numeric base
☐	graphics base

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 7 (Non-Graded))

Question # 3 of 5 (Start time: 11:32:07 PM, 05 December 2023)

The last location of word, corresponding to the screen will be _____

Select the correct option

☐	3998
☐	2000
☐	1998
☒	4000

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 7 (Non-Graded))

Question # 1 of 5 (Start time: 11:34:48 PM, 05 December 2023)

REP with _____ will utilizes the full processor power to do the scrolling in minimum time

Select the correct option

☐	REPNE
☐	SCAS
☐	LES
☒	MOVS

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 7 (Not Graded))

Question # 3 of 5 (Start time: 11:36:54 PM, 05 December 2023)

In the assembly language program, when using div instruction, dividend is stored in ____

Select the correct option

☐	DI
☒	AX
☐	Operand placed with div instruction
☐	BX

For any query feel free to ask princealvi9222@gmail.com

CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 7 (Non-Graded))

Question # 4 of 5 (Start time: 11:37:57 PM, 05 December 2023)

In the assembly language program, when using div instruction, dividend is stored in ----

Select the correct option

- | | |
|----------------------------------|-------------------------------------|
| ☐ | DI |
| ☐ | Operand placed with div instruction |
| ☒ | AX |
| ☐ | BX |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 1 of 10 (Start time: 08:40:09 PM, 08 December 2023)

The last location of word, corresponding to the screen will be -----

Select the correct option

☐	2000
☐	1998
☐	3998
☒	4000

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 2 of 10 (Start time: 08:42:03 PM, 08 December 2023)

MOVS instruction is used to _____

Select the correct option

- | | |
|----------------------------------|------------------------------------|
| ☐ | Move register to a memory location |
| ☐ | Move a memory location to register |
| ☒ | Move memory to memory |
| ☐ | Move register to register |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 5 of 10 (Start time: 08:47:08 PM, 08 December 2023)

Far calls are also called _____ calls.

Select the correct option

☐	distant
☐	obscure
☐	intra segment
☒	inter segment

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 6 of 10 (Start time: 08:48:11 PM, 08 December 2023)

REPE repeat the following string instruction while the _____is set.

Select the correct option

☒	Zero flag
☐	Direction flag
☐	Parity flag
☐	Carry flag

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 7 of 10 (Start time: 08:49:38 PM, 08 December 2023)

SP is decremented by _____ when an element is pushed on the stack.

Select the correct option

☐	4
☐	3
☒	2
☐	1

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 8 of 10 (Start time: 08:51:15 PM, 08 December 2023)

The value of _____ is recovered from the stack when RET instruction is executed.

Select the correct option

☒	SP
☐	IP
☐	PC
☐	DS

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 10 of 10 (Start time: 08:54:47 PM, 08 December 2023)

The last location of word, corresponding to the screen will be -----

Select the correct option

☐	4000
☐	2000
☒	3998
☐	1998

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 1 of 10 (Start time: 09:00:21 PM, 08 December 2023)

Near Calls are also called _____.

Select the correct option

- | | |
|----------------------------------|---------------|
| ☒ | intra segment |
| ☐ | Close call |
| ☐ | Small call |
| ☐ | inter segment |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 3 of 10 (Start time: 09:00:53 PM, 08 December 2023)

Push instruction, will store the value of operand used, in the _____

Select the correct option

☒	SP
☐	AX
☐	Flag
☐	BP

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 4 of 10 (Start time: 09:05:07 PM, 08 December 2023)

When all the general registers are used then a subroutine can receive maximum _____ parameters.

Select the correct option

☒	6
☐	4
☐	7
☐	5

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 5 of 10 (Start time: 09:07:04 PM, 08 December 2023)

The_____ bit tells that this is a system descriptor while the 1110 following it tells that it is a 386 interrupt gate.

Select the correct option

☐	T
☐	O
☒	S
☐	P

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 6 of 10 (Start time: 09:09:28 PM, 08 December 2023)

To access the video memory _____register is used.

Select the correct option

☐	AX
☒	BH
☐	DI
☐	ES

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 7 of 10 (Start time: 09:10:38 PM, 08 December 2023)

INT instruction takes a _____ byte(s) argument varying from 0-255.

Select the correct option

☐	4
☐	2
☒	1
☐	3

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 8 of 10 (**Start time: 09:12:52 PM, 08 December 2023**)

```
mov byte [swap], 0
```

What does above code do?

Select the correct option

- | | |
|----------------------------------|---|
| ☒ | It simply stores the 0 value to location named as swap |
| ☐ | It stores the byte value of 0 to location named as swap |
| ☐ | It simply stores the 0 value to location named as byte |
| ☐ | It stores the byte value of 0 to location named as byte |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 9 of 10 (Start time: 09:14:33 PM, 08 December 2023)

Which of the following is base address for displaying any character in the Screen?

Select the correct option

☐	0b800
☐	0b800x
☐	0xb80
☒	0xb800

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 10 of 10 (Start time: 09:16:02 PM, 08 December 2023)

The execution of the instruction "mov word [ES : 160], 0x1230" will print a character on the screen at:

Select the correct option

- | | |
|----------------------------------|-----------------------------|
| ☐ | Second column of second row |
| ☐ | Second column of first row |
| ☐ | First column of third row |
| ☒ | First column of second row |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 2 of 10 (Start time: 09:21:21 PM, 08 December 2023)

Each entry of the interrupt vector table is of _____ bytes.

Select the correct option

☐	3
☐	1
☒	4
☐	2

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 3 of 10 (Start time: 09:22:55 PM, 08 December 2023)

```
mov byte [swap], 0
```

What would happen if we dont use the keyword byte or word before the operand?

Select the correct option

- | | |
|----------------------------------|--|
| ☐ | Nothing will happen, program will run smoothly all the time |
| ☐ | Nothing will happen but its good practice to use theses keywords |
| ☒ | Its good to use, otherwise size mismatch error may occur |
| ☐ | Its useless to use these keywords |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Grade C))

Question # 4 of 10 (Start time: 09:24:24 PM, 08 December 2023)

The value of _____ is recovered from the stack when RET instruction is executed.

Select the correct option

☐	DS
☒	PC
☐	IP
☐	SP

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 5 of 10 (Start time: 09:25:43 PM, 08 December 2023)

```
mov byte [swap], 0
```

What does above code do?

Select the correct option

- | | |
|----------------------------------|---|
| ☐ | It simply stores the 0 value to location named as swap |
| ☒ | It stores the byte value of 0 to location named as byte |
| ☐ | It stores the byte value of 0 to location named as swap |
| ☐ | It simply stores the 0 value to location named as byte |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 6 of 10 (Start time: 09:27:13 PM, 08 December 2023)

Stack behaves as

Select the correct option

- | | |
|----------------------------------|--------------------|
| ☐ | First in First Out |
| ☒ | Last In First Out |
| ☐ | First in Last out |
| ☐ | Last in Last out |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 8 of 10 (Start time: 09:28:41 PM, 08 December 2023)

Push instruction, will store the value of operand used, in the _____

Select the correct option

☒	SP
☐	AX
☐	BP
☐	Flag

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 2 (Graded))

Question # 9 of 10 (Start time: 09:29:39 PM, 08 December 2023)

In string instructions, the mode is called auto-increment mode when:

Select the correct option

☒	CX is set
☐	ZF is cleared
☐	DF is cleared
☐	DF is set

CS610P – Computer Networks (Practical) (Lab No. 8 Quiz)

Question # 1 of 10 (Start time: 10:15:19 PM, 08 December 2023)

In "Internet Protocol Version 4 (TCP/IPv4) Properties" window, the _____ radio button will be selected to enter the correct IP address.

Select the correct option

☐	Use the same IP address
☐	Use the internal IP address
☐	Use the embedded IP address
☒	Use the following IP address

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS610P – Computer Networks (Practical) (Lab No. 8 Quiz)

Question # 2 of 10 (Start time: 10:17:01 PM, 08 December 2023)

In Windows 7 or Windows 10 operating system, you will go to _____ settings to assign the IP address to the computer.

Select the correct option

☒	Change adapter
☐	Change LAN
☐	Change devices
☐	Change IP

CS610P – Computer Networks (Practical) (Lab No. 8 Quiz)

Question # 3 of 10 (Start time: 10:18:26 PM, 08 December 2023)

In Windows 7 or Windows 10 operating system, which of the following option will be used to assign or change the computer's IP address?

Select the correct option

☐	Devices and Printers
☒	Network and Sharing
☐	Device Manger
☐	Programs and Features

For any query feel free to ask princealvi9222@gmail.com

CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

Question # 4 of 10 (Start time: 10:20:16 PM, 08 December 2023)

In "Internet Protocol Version 4 (TCP/IPv4) Properties" window while providing IP address, you also need to provide _____ and Default gateway that corresponds with your network setup.

Select the correct option

☒	Subnet mask
☐	Localnet
☐	Supernet
☐	Defaultnet

CS610P - Computer Networks (Practical) (Lab No. 8 Quiz)

Question # 5 of 10 (Start time: 10:22:40 PM, 08 December 2023)

In Windows 7 or Windows 10 operating system, for assigning IP address to the computer, you will select _____ after right-click on your local adapter.

Select the correct option

☒	Properties
☐	Bridge connection
☐	Diagnose
☐	Status

For any query feel free to ask princealvi9222@gmail.com

CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS610P - Computer Networks (Practical) (Lab No. 8 Quiz)

Quiz

Question # 6 of 10 (Start time: 10:23:56 PM, 08 December 2023)

In "Internet Protocol Version 4 (TCP/IPv4) Properties" window, after entering correct IP address, you also need to provide the preferred and alternate _____ server addresses.

Select the correct option

☒	DNS
☐	Database
☐	Web
☐	Proxy

CS610P - Computer Networks (Practical) (Lab No. 8 Quiz)

Question # 8 of 10 (Start time: 10:27:37 PM, 08 December 2023)

Which of the following is correct address of Google DNS server?

Select the correct option

☐	8.8.7.8
☐	8.7.8.8
☒	8.8.8.8
☐	8.8.8.7

For any query feel free to ask princealvi9222@gmail.com

CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS610P - Computer Networks (Practical) (Lab No. 8 Quiz)

Question # 9 of 10 (**Start time: 10:28:37 PM, 08 December 2023**)

Windows will run _____ to verify the connection is good.

Select the correct option

☐	Network Modeller
☐	Network Sniffer
☒	Network Diagnostics
☐	Network Designer

For any query feel free to ask princealvi9222@gmail.com

CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS610P - Computer Networks (Practical) (Lab No. 8 Quiz)

Question # 10 of 10 (Start time: 10:29:46 PM, 08 December 2023)

In windows operating system, if you find any problem in configuration of IP address then run the _____ wizard.

Select the correct option

☐	Network solution
☐	Network handling
☒	Network troubleshooting
☐	Network fixing

CS610P - Computer Networks (Practical) (Lab No. 8 Quiz)

Quiz Start Time: 10:29:46 PM, 08 December 2023

Question # 4 of 10 (Start time: 10:35:31 PM, 08 December 2023)

In "Internet Protocol Version 4 (TCP/IPv4) Properties" window while providing IP address, you also need to provide _____ and Default gateway that corresponds with your network setup.

Select the correct option

☐	Localnet
☐	Defaultnet
☐	Supernet
☒	Subnet mask

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS610P - Computer Networks (Practical) (Lab No. 8 Quiz)

Question # 5 of 10 (Start time: 10:37:04 PM, 08 December 2023)

In "Internet Protocol Version 4 (TCP/IPv4) Properties" window, _____ option will be checked to find any problems with the addresses you entered.

Select the correct option

☐	Correct settings upon exit
☒	Validate settings upon exit
☐	Verify settings upon exit
☐	Find settings upon exit

CS610P - Computer Networks (Practical) (Lab No. 8 Quiz)

Quiz

Question # 6 of 10 (Start time: 10:37:57 PM, 08 December 2023)

In "Internet Protocol Version 4 (TCP/IPv4) Properties" window, after entering correct IP address, you also need to provide the preferred and alternate _____ server addresses.

Select the correct option

☒	DNS
☐	Web
☐	Database
☐	Proxy

For any query feel free to ask princealvi9222@gmail.com

CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS610P - Computer Networks (Practical) (Lab No. 8 Quiz)

Question # 7 of 10 (Start time: 10:39:16 PM, 08 December 2023)

To configure IP address in windows operating system, go to the "Local Area Connection Properties" window and highlight -----

Select the correct option

☒	Internet Protocol Version 4 (TCP/IPv4)
☐	Internet Protocol Version 2 (TCP/IPv2)
☐	Internet Protocol Version1 (TCP/IPv1)
☐	Internet Protocol Version 5 (TCP/IPv5)

CS610P - Computer Networks (Practical) (Lab No. 8 Quiz)

Question # 8 of 10 (Start time: 10:40:47 PM, 08 December 2023)

Windows will run ----- to verify the connection is good.

Select the correct option

☐	Network Designer
☐	Network Modeller
☐	Network Sniffer
☒	Network Diagnostics

For any query feel free to ask princealvi9222@gmail.com

CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practice Lab Quiz No. 8 (Non-Graded))

Question # 1 of 5 (**Start time: 08:47:01 PM, 04 January 2024**)

_____ interrupt is used for Arithmetic overflow.

Select the correct option

☒	INT 7
☐	INT 8
☐	INT 6

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 8 (Non-Graded))

Question # 2 of 5 (Start time: 08:50:09 PM, 04 January 2024)

INT instruction takes a _____ byte(s) argument varying from 0-255.

Select the correct option

☐	2
☐	3
☐	4
☒	1

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 8 (Non-Graded))

Question # 3 of 5 (Start time: 08:51:38 PM, 04 January 2024)

The _____ bit tells that this is a system descriptor while the 1110 following it tells that it is a 386 interrupt gate.

Select the correct option

☐	P
☐	O
☒	S
☐	T

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 8 (Not Graded))

Question # 4 of 5 (Start time: 08:53:15 PM, 04 January 2024)

Stack behaves as

Select the correct option

- | | |
|----------------------------------|--------------------|
| ☐ | First in First Out |
| ☐ | Last in Last out |
| ☐ | First in Last out |
| ☒ | Last In First Out |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 8 (Non-Graded))

Question # 5 of 5 (Start time: 08:54:20 PM, 04 January 2024)

Whenever ret function is called

Select the correct option

☐	Stack is pushed
☐	nothing happens in stack
☒	Stack is pulled
☐	Stack is cleared

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 8 (Non-Graded))

Question # 3 of 5 (Start time: 09:01:10 PM, 04 January 2024)

Each entry of the interrupt vector table is of _____ bytes.

Select the correct option

☐	1
☐	3
☐	2
☒	4

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

Question # 4 of 5 (Start time: 09:02:37 PM, 04 January 2024)

```
mov ax,2  
push ax  
pop bx  
add bx,ax
```

at the end of execution of above code, what will be value in ax?

Select the correct option

☐	0
☐	2
☒	4
☐	1

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 8 (Non-Graded))

Question # 5 of 5 (**Start time: 09:05:03 PM, 04 January 2024**)

_____ interrupt is used for Arithmetic overflow.

Select the correct option

☒	INT 4
☐	INT 8
☐	INT 6
☐	INT 7

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 9 (Non Graded))

Question # 1 of 5 (Start time: 12:09:42 AM, 06 January 2024)

How many I/O Ports are there?

Select the correct option

- | | |
|----------------------------------|-------|
| ☐ | 65524 |
| ☐ | 65520 |
| ☒ | 65536 |
| ☐ | 65548 |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 9 (Non-Graded))

Question # 2 of 5 (Start time: 12:11:40 AM, 06 January 2024)

How is the video device perceived by the computer in terms of memory?

Select the correct option

☐	Two-dimensional space with ASCII codes
☒	Linearly mapped memory
☐	Independent of system memory
☐	Only accessible through I/O ports

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 9 (Non-Graded))

Question # 3 of 5 (Start time: 12:12:50 AM, 06 January 2024)

What effect does setting bit 7 in the attribute byte have on the foreground character (in the context of display memory

Select the correct option

☐	Clears the screen
☐	Initiates blinking
☐	Changes background color
☒	Intensity of foreground color

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 9 (Non-Graded))

Question # 4 of 5 (Start time: 12:14:19 AM, 06 January 2024)

What is the significance of the memory location B8000 in the context of Display Memory?

Select the correct option

☐	Physical memory location for video controller's memory
☒	Default cursor position
☐	Segment register for accessing video memory
☐	BIOS vendor's problem location

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 9 (Non-Graded))

Question # 5 of 5 (**Start time: 12:15:17 AM, 06 January 2024**)

How is the screen location calculated (in the context of display memory formation)?

Select the correct option

☐	Using the MUL instruction
☐	Adding row and column numbers
☐	Dividing row and column numbers
☒	Multiplying row number by 80 and adding column number

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 9 (Non-Graded))

Question # 1 of 5 (Start time: 12:44:16 AM, 06 January 2024)

What does the attribute byte in the word designated for one screen location hold?

Select the correct option

- | | |
|----------------------------------|----------------------------------|
| ☐ | Video controller instructions |
| ☐ | ASCII code for the character |
| ☒ | Foreground and background colors |
| ☐ | Cursor height information |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 9 (Non-Graded))

Question # 3 of 5 (Start time: 12:46:42 AM, 06 January 2024)

While displaying something on screen, why is ES loaded with the segment of video memory?

Select the correct option

☐	To open a window to the video memory
☐	To control cursor position
☐	To manipulate ASCII codes
☒	To access data

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 9 (Non-Graded))

Question # 4 of 5 (Start time: 12:47:42 AM, 06 January 2024)

Service numbers are given in _____ register while sub services in _____

Select the correct option

☒	AX, AI or Ah
☐	AH, AL or BL
☐	AI, Ah or ax
☐	Ax, Bx

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 9 (Non-Graded))

Question # 5 of 5 (Start time: 12:49:41 AM, 06 January 2024)

How is the video device perceived by the computer in terms of memory?

Select the correct option

- | | |
|----------------------------------|--|
| ☒ | Linearly mapped memory |
| ☐ | Independent of system memory |
| ☐ | Two-dimensional space with ASCII codes |
| ☐ | Only accessible through I/O ports |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 10 (Non-Graded))

Question # 1 of 5 (Start time: 07:56:06 PM, 09 January 2024)

Let's assume that location A in memory has 0000 bits. What will be the result when XOR operation is performed between A and A (A XOR A)?

Select the correct option

☐	1
☐	11
☒	0
☐	10

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 10 (Non-Graded))

Question # 2 of 5 (Start time: 07:57:48 PM, 09 January 2024)

If _____ is set, then after every instruction a type 1 interrupt will automatically be generated.

Select the correct option

☒	Trap flag
☐	Overflow flag
☐	Carry flag
☐	Parity flag

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 10 (Non-Graded))

Question # 3 of 5 (Start time: 07:59:22 PM, 09 January 2024)

Which assembly language instruction is used to enable the interrupt requests?

Select the correct option

☐	dit
☐	cli
☐	eit
☒	sti

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 10 (Non-Graded))

Question # 4 of 5 (**Start time: 08:01:03 PM, 09 January 2024**)

Which of following IO port is used to send an EOI signal to PIC?

Select the correct option

☒	0x20
☐	0x23
☐	0x22
☐	0x21

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 10 (Non-Graded))

Question # 5 of 5 (**Start time: 08:02:39 PM, 09 January 2024**)

Using OUT instruction on data port (378), 1 bits result in a _____ signal on the corresponding pin.

Select the correct option

☐	10V
☐	1V
☒	5V
☐	3V

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 10 (Non-Graded))

Question # 2 of 5 (Start time: 08:15:19 PM, 09 January 2024)

DB-25 is a _____ port connector.

Select the correct option

☐	serial
☒	parallel
☐	partially serial
☐	half duplex

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 10 (Non-Graded))

Question # 3 of 5 (Start time: 08:17:01 PM, 09 January 2024)

Let's assume that location A in memory has 0000 bits. What will be the result when XOR operation is performed between A and A (A XOR A)?

Select the correct option

☒	0
☐	1
☐	11
☐	10

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 10 (Non-Graded))

Question # 4 of 5 (Start time: 08:18:46 PM, 09 January 2024)

Which of following pins of parallel port are used to carry the data from processor to the device connected through parallel port?

Select the correct option

☐	pin 10
☐	pin 2 to pin 9
☐	pin 4
☒	pin 18 to pin 25

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 11 (Non-Graded))

Question # 1 of 5 (Start time: 12:05:08 AM, 12 January 2024)

SP register points to_____.

Select the correct option

☒	Top of stack
☐	Bottom of stack
☐	Current item of stack
☐	Highest limit of stack

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 11 (Non-Graded))

Question # 2 of 5 (Start time: 12:07:28 AM, 12 January 2024)

_____ is the highest priority interrupt in interrupt controller.

Select the correct option

☒	IRQ 0
☐	IRQ 1
☐	IRQ 3
☐	IRQ 2

IRET returns on the basis of _____ and _____

- a. CS, IP
- b. DS, IP
- c. CS, SS
- d. IP, SP

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 11 (Non-Graded))

Question # 4 of 5 (Start time: 12:10:13 AM, 12 January 2024)

Which register will be on the top of the stack after execution of the following assembly code?

push ax
push cx
push bx
pull ax
pull dx

Select the correct option

☒	CX
☐	BX
☐	AX
☐	DX

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 11 (Non-Graded))

Question # 5 of 5 (Start time: 12:11:44 AM, 12 January 2024)

Changing and restoring the state of Central Processing Unit (CPU) is referred as:

Select the correct option

☐	Thrashing
☐	Multithreading
☐	Multitasking
☒	Context Switching

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 11 (Non-Graded))

Question # 1 of 5 (Start time: 12:14:18 AM, 12 January 2024)

SP register points to_____.

Select the correct option

☐	Highest limit of stack
☒	Top of stack
☐	Current item of stack
☐	Bottom of stack

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 11 (Non-Graded))

Question # 2 of 5 (Start time: 12:16:13 AM, 12 January 2024)

Changing and restoring the state of Central Processing Unit (CPU) is referred as:

Select the correct option

☐	Thrashing
☐	Multithreading
☒	Context Switching
☐	Multitasking

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 11 (Non-Graded))

Question # 3 of 5 (Start time: 12:17:41 AM, 12 January 2024)

Which IO port is used to scan the code of key pressed on the keyboard?

Select the correct option

☐	0x61
☐	0x63
☐	0x62
☒	0x60

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 11 (Non-Graded))

Question # 4 of 5 (Start time: 12:19:20 AM, 12 January 2024)

How many possible I/O ports are there in the Intel architecture when using the lower 16 bits of the address bus?

Select the correct option

☒	1024
☐	65536
☐	512
☐	256

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 11 (Non-Graded))

Question # 5 of 5 (Start time: 12:21:34 AM, 12 January 2024)

_____ is the highest priority interrupt in interrupt controller.

Select the correct option

☐	IRQ 1
☒	IRQ 0
☐	IRQ 2
☐	IRQ 3

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 1 of 10 (Start time: 09:02:42 PM, 16 January 2024)

If _____ is set, then after every instruction a type 1 interrupt will automatically be generated.

Select the correct option

☒	Trap flag
☐	Overflow flag
☐	Carry flag
☐	Parity flag

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 2 of 10 (Start time: 09:04:09 PM, 16 January 2024)

Which assembly language instruction is used to enable the interrupt requests?

Select the correct option

☐	dit
☒	sti
☐	cli
☐	eit

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 3 of 10 (Start time: 09:05:07 PM, 16 January 2024)

What is the significance of the memory location B8000 in the context of Display Memory?

Select the correct option

- | | |
|----------------------------------|--|
| ☐ | BIOS vendor's problem location |
| ☐ | Default cursor position |
| ☒ | Physical memory location for video controller's memory |
| ☐ | Segment register for accessing video memory |

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 4 of 10 (Start time: 09:06:37 PM, 16 January 2024)

Which of following pins of parallel port are used to carry the data from processor to the device connected through parallel port?

Select the correct option

☐	pin 4
☐	pin 10
☐	pin 2 to pin 9
☒	pin 18 to pin 25

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 5 of 10 (Start time: 09:08:17 PM, 16 January 2024)

In interrupt masking, We use _____ to get control from the program without letting the program know about it.

Select the correct option

☐	IRQ 1
☐	IRQ 3
☒	IRQ 2
☐	IRQ 0

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 6 of 10 (**Start time: 09:09:18 PM, 16 January 2024**)

Service numbers are given in _____ register while sub services in _____

Select the correct option

☐	Al, Ah or ax
☒	AX, Al or Ah
☐	Ax, Bx
☐	AH, AL or BL

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 7 of 10 (Start time: 09:10:19 PM, 16 January 2024)

What is the purpose of sending **0x40** to the VGA card in the context of display memory formation?

Select the correct option

- | | |
|----------------------------------|------------------------|
| ☐ | Set resolution |
| ☐ | Turn pixels on and off |
| ☒ | Change cursor position |
| ☐ | Clear the screen |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 8 of 10 (Start time: 09:12:02 PM, 16 January 2024)

Which of following interrupt is used for BIOS Video Services

Select the correct option

☐	0x21
☐	0x13
☐	0x16
☒	0x10

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 9 of 10 (Start time: 09:12:54 PM, 16 January 2024)

Which register will be on the top of the stack after execution of the following assembly code?

push ax
push cx
push bx
pull ax
pull dx

Select the correct option

☐	DX
☐	BX
☐	CX
☒	AX

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 10 of 10 (Start time: 09:15:25 PM, 16 January 2024)

What is the purpose of the MUL instruction in the context of display memory?

Select the correct option

☐	Controls cursor attributes
☒	Performs unsigned multiplication
☐	Multiplies ASCII codes
☐	Clears the screen

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 1 of 10 (Start time: 09:17:41 PM, 16 January 2024)

To return back from the Interrupt Service Routine to position from where ISR was called, which of the following instruction is used?

Select the correct option

☐	retn
☒	ret
☐	iret
☐	retf

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 2 of 10 (Start time: 09:18:34 PM, 16 January 2024)

_____compares a source byte or word in register AL or AX with the destination string element addressed by ES:DI.

Select the correct option

☒	SCAS
☐	STOSW
☐	STOS
☐	LODS

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 3 of 10 (Start time: 09:19:29 PM, 16 January 2024)

Let's assume that location A in memory has 0000 bits. What will be the result when XOR operation is performed between A and A (A XOR A)?

Select the correct option

☐	10
☒	0
☐	11
☐	1

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 5 of 10 (Start time: 09:21:00 PM, 16 January 2024)

The following instruction will load Si and Ds from_____.
lds si, [bp+4]

Select the correct option

☐	BP+6, BP+4
☒	BP+4, BP+4
☐	BP+6, BP+6
☐	BP+4, BP+6

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 6 of 10 (**Start time: 09:22:59 PM, 16 January 2024**)

While displaying something on screen, why is ES loaded with the segment of video memory?

Select the correct option

- | | |
|----------------------------------|--------------------------------------|
| ☒ | To access data |
| ☐ | To control cursor position |
| ☐ | To open a window to the video memory |
| ☐ | To manipulate ASCII codes |

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 8 of 10 (Start time: 09:25:37 PM, 16 January 2024)

Using OUT instruction on data port (378), 1 bits result in a _____ signal on the corresponding pin.

Select the correct option

☒	5V
☐	1V
☐	3V
☐	10V

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 9 of 10 (Start time: 09:26:28 PM, 16 January 2024)

Which register will be on the top of the stack after execution of the following assembly code?

```
push ax  
push cx  
push bx  
pull ax  
pull dx
```

Select the correct option

☐	BX
☐	AX
☒	DX
☐	CX

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 10 of 10 (Start time: 09:29:09 PM, 16 January 2024)

How is the video device perceived by the computer in terms of memory?

Select the correct option

- | | |
|----------------------------------|--|
| ☒ | Only accessible through I/O ports |
| ☐ | Two-dimensional space with ASCII codes |
| ☐ | Linearly mapped memory |
| ☐ | Independent of system memory |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 1 of 10 (Start time: 09:31:36 PM, 16 January 2024)

What is the purpose of ports 20 and 21 in the Programmable Interrupt Controller (PIC)?

Select the correct option

- | | |
|----------------------------------|---|
| ☐ | Port 20 is for sound card communication, and port 21 is for network card communication. |
| ☐ | Port 20 and port 21 both serve as control ports for the PIC. |
| ☐ | Port 20 is for keyboard input, and port 21 is for mouse input. |
| ☒ | Port 20 is the interrupt mask register, and port 21 is the control port. |

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 3 of 10 (Start time: 09:33:59 PM, 16 January 2024)

_____ instruction transfers a byte or word in AL or AX to the string element addressed by ES:DI.

Select the correct option

☐	LODS
☐	SCAS
☒	STOS
☐	MOVS

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 5 of 10 (**Start time: 09:35:05 PM, 16 January 2024**)

SP register points to_____.

Select the correct option

- | | |
|----------------------------------|------------------------|
| ☐ | Highest limit of stack |
| ☒ | Top of stack |
| ☐ | Bottom of stack |
| ☐ | Current item of stack |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 6 of 10 (**Start time: 09:37:15 PM, 16 January 2024**)

Each of the bits at port _____ corresponds to one of the Interrupt Request (IRQ) lines.

Select the correct option

☐	18
☐	21
☐	20
☒	19

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 7 of 10 (Start time: 09:39:14 PM, 16 January 2024)

Which of following IO port is used to send an EOI signal to PIC?

Select the correct option

- | | |
|----------------------------------|------|
| ☐ | 0x22 |
| ☐ | 0x23 |
| ☒ | 0x20 |
| ☐ | 0x21 |

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 8 of 10 (Start time: 09:39:58 PM, 16 January 2024)

What is the purpose of sending **0x40** to the VGA card in the context of display memory formation?

Select the correct option

- | | |
|----------------------------------|------------------------|
| ☐ | Turn pixels on and off |
| ☐ | Clear the screen |
| ☐ | Set resolution |
| ☒ | Change cursor position |

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 9 of 10 (Start time: 09:41:41 PM, 16 January 2024)

Service numbers are given in _____ register while sub services in _____

Select the correct option

- | | |
|----------------------------------|--------------|
| ☐ | Ax, Bx |
| ☐ | AH, AL or BL |
| ☐ | Al, Ah or ax |
| ☒ | AX, Al or Ah |

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 3)

Question # 10 of 10 (Start time: 09:43:13 PM, 16 January 2024)

In interrupt masking, We use _____ to get control from the program without letting the program know about it.

Select the correct option

☐	IRQ 0
☐	IRQ 3
☒	IRQ 2
☐	IRQ 1

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 12 (Non-Graded))

Question # 1 of 5 (Start time: 09:24:21 PM, 18 January 2024)

Video mode can be set with _____ while exporting with _____.

Select the correct option

☐	Ax and Bx, 0x10
☐	Ah and Al, 0x10
☐	Ah and Al, 0x13
☒	Ax and Bx, 0x13

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 12 (Non-Graded))

Question # 2 of 5 (Start time: 09:25:46 PM, 18 January 2024)

The following instruction will load Si and Ds from_____.
lds si, [bp+4]

Select the correct option

☒	BP+4, BP+4
☐	BP+6, BP+6
☐	BP+4, BP+6
☐	BP+6, BP+4

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 12 (Non-Graded))

Question # 3 of 5 (Start time: 09:27:46 PM, 18 January 2024)

_____compares a source byte or word in register AL or AX with the destination string element addressed by ES:DI.

Select the correct option

☐	STOSW
☒	SCAS
☐	STOS
☐	LODS

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 12 (Non-Graded))

Question # 4 of 5 (Start time: 09:29:07 PM, 18 January 2024)

----- is/are the port number(s) for parallel port.

Select the correct option

☐	375
☒	378
☐	380
☐	383

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 12 (Non-Graded))

Question # 5 of 5 (Start time: 09:30:51 PM, 18 January 2024)

_____ instruction transfers a byte or word in AL or AX to the string element addressed by ES:DI.

Select the correct option

☐	SCAS
☒	STOS
☐	MOVS
☐	LDS

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 12 (Non-Graded))

Question # 1 of 5 (Start time: 10:15:58 PM, 18 January 2024)

Video mode can be set with _____ while exporting with _____.

Select the correct option

- | | |
|----------------------------------|-----------------|
| ☒ | Ax and Bx, 0x13 |
| ☐ | Ah and Al, 0x13 |
| ☐ | Ah and Al, 0x10 |
| ☐ | Ax and Bx, 0x10 |

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 12 (Non-Graded))

Question # 3 of 5 (Start time: 10:18:47 PM, 18 January 2024)

Each of the bits at port _____ corresponds to one of the Interrupt Request (IRQ) lines.

Select the correct option

☐	21
☐	19
☐	18
☒	20

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 12 (Non-Graded))

Question # 4 of 5 (Start time: 10:20:36 PM, 18 January 2024)

_____ instruction transfers a byte or word in AL or AX to the string element addressed by ES:DI.

Select the correct option

☐	MOVS
☐	LODS
☐	SCAS
☒	STOS

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 13 (Non-Graded))

Question # 1 of 5 (Start time: 10:55:17 PM, 20 January 2024)

_____transfers a byte or word from the source location DS:SI to AL or AX and updates SI to point to the next location.

Select the correct option

☐	MOVS
☒	LODS
☐	CMPS
☐	SCAS

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 13 (Non-Graded))

Question # 2 of 5 (Start time: 10:57:36 PM, 20 January 2024)

Which of following interrupts is used for Keyboard BIOS services?

Select the correct option

☐	0x6
☒	0x16
☐	0x10
☐	0x15

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 13 (Non-Graded))

Question # 3 of 5 (Start time: 10:58:42 PM, 20 January 2024)

Which assembly language instruction is used to disable the interrupt requests?

Select the correct option

☒	cli
☐	sti
☐	eit
☐	dit

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 13 (Non Graded))

Question # 4 of 5 (Start time: 11:00:07 PM, 20 January 2024)

_____transfers a byte or word from the source location DS:SI to the destination ES:DI.

Select the correct option

☐	LODS
☐	SCAS
☐	CMPS
☒	MOVS

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 13 (Non-Graded))

Question # 5 of 5 (Start time: 11:02:32 PM, 20 January 2024)

_____ instruction is used to read a character from the keyboard port.

Select the correct option

☐	out al, 0x80
☐	in al, 0x80
☐	out al, 0x60
☒	in al, 0x60

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 13 (Non-Graded))

Question # 1 of 5 (Start time: 11:06:12 PM, 20 January 2024)

Which assembly language instruction is used to disable the interrupt requests?

Select the correct option

☐	dit
☐	eit
☒	cli
☐	sti

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 13 (Non-Graded))

Question # 2 of 5 (Start time: 11:07:04 PM, 20 January 2024)

_____transfers a byte or word from the source location DS:SI to AL or AX and updates SI to point to the next location.

Select the correct option

☐	MOVS
☐	CMPS
☐	SCAS
☒	LODS

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 13 (Non-Graded))

Question # 4 of 5 (Start time: 11:08:58 PM, 20 January 2024)

Which of following interrupts is used for Keyboard BIOS services?

Select the correct option

☐	0x6
☒	0x16
☐	0x10
☐	0x15

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 13 (Non-Graded))

Question # 5 of 5 (**Start time: 11:09:47 PM, 20 January 2024**)

Which of the following interrupts is used for saving and restoring the registers

Select the correct option

☐	INT7
☐	INT0
☒	INT8
☐	INT6

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 4)

Question # 1 of 10 (Start time: 11:22:32 PM, 25 January 2024)

Which assembly language instruction is used to disable the interrupt requests?

Select the correct option

☒	cli
☐	sti
☐	clit
☐	eit

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 4)

Question # 2 of 10 (Start time: 11:23:34 PM, 25 January 2024)

The video services are exported via

Select the correct option

☒	INT10
☐	INT8
☐	INT7
☐	INT9

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 4)

Question # 3 of 10 (Start time: 11:24:55 PM, 25 January 2024)

Which of the following services are at higher level?

Select the correct option

☐	BIOS services
☐	Hardware Services
☐	DOS services
☒	Printer services

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 4)

Question # 4 of 10 (Start time: 11:27:03 PM, 25 January 2024)

BIOS video services are broadly classified into _____ categories.

Select the correct option

☐	1
☒	2
☐	3
☐	4

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 4)

Question # 5 of 10 (Start time: 11:28:28 PM, 25 January 2024)

_____transfers a byte or word from the source location DS:SI to AL or AX and updates SI to point to the next location.

Select the correct option

☐	SCAS
☐	CMPS
☒	MOVS
☐	LDS

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 4)

Question # 6 of 10 (Start time: 11:29:14 PM, 25 January 2024)

_____ instruction is used to read a character from the keyboard port.

Select the correct option

☒	in al, 0x60
☐	in al, 0x80
☐	out al, 0x80
☐	out al, 0x60

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 4)

Question # 7 of 10 (Start time: 11:30:01 PM, 25 January 2024)

Lodsb instruction will load next character and increment SI by -----.

Select the correct option

☒	1
☐	3
☐	4
☐	2

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 4)

Question # 8 of 10 (Start time: 11:31:27 PM, 25 January 2024)

----- instruction is used for memory to memory movement of data.

Select the correct option

- | | |
|----------------------------------|-----------|
| ☒ | rep movsx |
| ☐ | rep movsw |
| ☐ | rep stosw |
| ☐ | rep stos |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 4)

Question # 9 of 10 (Start time: 11:33:04 PM, 25 January 2024)

Which of following interrupts is used for Keyboard BIOS services?

Select the correct option

☐	0x10
☒	0x16
☐	0x15
☐	0x6

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 4)

Question # 10 of 10 (Start time: 11:33:46 PM, 25 January 2024)

EOI stands for_____

Select the correct option

- | | |
|----------------------------------|-------------------|
| ☒ | End of Interrupt |
| ☐ | Exit of Interrupt |
| ☐ | End of ISR |
| ☐ | Exit of ISR |

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 14 (Non-Graded))

Question # 1 of 5 (Start time: 11:35:00 PM, 25 January 2024)

To save the values of registers and flags for later use, They are

Select the correct option

☐	Pulled into stack and then again pulled
☐	Pulled into stack and then pushed
☒	Pushed into stack and then pushed
☐	Pushed into stack and then pulled

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CS401P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS401P - Computer Architecture and Assembly Language Programming (Practical) (Lab Quiz No. 14 (Non-Graded))

Question # 2 of 5 (Start time: 11:36:35 PM, 25 January 2024)

BIOS video services are broadly classified into _____ categories.

Select the correct option

☒	2
☐	3
☐	1
☐	4

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 4)

Question # 6 of 10 (Start time: 11:46:22 PM, 25 January 2024)

_____transfers a byte or word from the source location DS:SI to AL or AX and updates SI to point to the next location.

Select the correct option

☐	CMPS
☐	MOVS
☐	SCAS
☒	LODS

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CS401P – Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 4)

Question # 4 of 10 (Start time: 11:44:16 PM, 25 January 2024)

To save the values of registers and flags for later use, They are

Select the correct option

- | | |
|----------------------------------|---|
| ☒ | Pushed into stack and then pulled |
| ☐ | Pulled into stack and then again pulled |
| ☐ | Pulled into stack and then pushed |
| ☐ | Pushed into stack and then pushed |

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 4)

Question # 6 of 10 (Start time: 11:46:22 PM, 25 January 2024)

_____transfers a byte or word from the source location DS:SI to AL or AX and updates SI to point to the next location.

Select the correct option

☐	CMPS
☐	MOVS
☐	SCAS
☒	LODS

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 4)

Question # 7 of 10 (Start time: 11:48:03 PM, 25 January 2024)

_____transfers a byte or word from the source location DS:SI to the destination ES:DI.

Select the correct option

☐	SCAS
☐	CMPS
☐	LDS
☒	MOVS

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CS401P - Computer Architecture and Assembly Language Programming (Practical) (Quiz No. 4)

Question # 8 of 10 (Start time: 11:50:14 PM, 25 January 2024)

If we have a function declared in C as newfunction(), it will be called from an assembly language program as _____.

Select the correct option

- | | |
|----------------------------------|-------------------|
| ☒ | call _newfunction |
| ☐ | call newfunction |
| ☐ | jmp newfunction |
| ☐ | jmp _newfunction |

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