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کیا آپ نے آج درود شریف پڑھا

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Note:

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اگر کسی سٹوڈنٹ کو کسی بھی قسم کی ہیلپ چاہے ہو تو ہم سے رابطہ کر سکتا ہے جیسا کہ: پاسٹ پیپرز، کورس سلیکشن، پروجیکٹ سلیکشن، ڈیٹ شیٹ،
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Whatsapp: 03066295623

مزید اگر کسی رہنمائی کی ضرورت ہو تو وہ رابطہ کر سکتا ہے۔

Question # 6 of 10 (Start time: 05:19:53 PM, 26 August 2022)

State of flip-flop can be switched by changing its_____.

Select the correct option

<input type="radio"/>	Momentary Signal
<input type="radio"/>	Contemporary Signal
<input checked="" type="radio"/>	Input Signal
<input type="radio"/>	Output Signal

Waqas Ahmed: 0306-6295623

Question # 7 of 10 (Start time: 05:20:09 PM, 26 August 2022)

To implement the counter using S-R flip-flops instead of J-K flip-flops, the _____ transition table is used.

Select the correct option

<input type="radio"/>	Next State
<input type="radio"/>	J-K
<input type="radio"/>	None of above
<input checked="" type="radio"/>	S-R

Waqas Ahmed: 0306-6295623

Question # 8 of 10 (Start time: 05:20:26 PM, 26 August 2022)

The 3-bit up counter can be implemented using _____ flip-flop(s).

Select the correct option



S-R Flip-flops Only



D-Flip-flop Only



S-R Flip-flops or D-Flip-flops



S-R Flip-flops and D-Flip-flops

Waqas Ahmed: 0306-6295623



Question # 9 of 10 (Start time: 05:20:53 PM, 26 August 2022)

A decade counter is -----

Select the correct option



Mod-10 counter

Waqas Ahmed: 0306-6295623



Mod-8 counter



Mod-3 counter



Mod-5 counter

Question # 10 of 10 (Start time: 05:21:03 PM, 26 August 2022)

The power consumed by a flip-flop is defined by _____

Select the correct option



$$P = I_{cc} \times R_{cc}$$



$$P = M_{cc} \times V_{cc}$$



$$P = V_{cc} \times R_{cc}$$



$$P = V_{cc} \times I_{cc}$$

Waqas Ahmed: 0306-6295623

Question # 1 of 10 (Start time: 05:26:26 PM, 26 August 2022)

In gated SR latch, what is the value of the output if EN=1, S=0 and R=1?

Select the correct option

<input checked="" type="radio"/>	0
<input type="radio"/>	Invalid
<input type="radio"/>	Qt
<input type="radio"/>	1

Waqas Ahmed: 0306-6295623

Question # 2 of 10 (Start time: 05:26:39 PM, 26 August 2022)

The _____ inputs can be directly mapped to Karnaugh maps.

Select the correct option

<input type="radio"/>	External
<input type="radio"/>	S-R
<input type="radio"/>	Flip-flop
<input checked="" type="radio"/>	J-K

Waqas Ahmed: 0306-6295623

Question # 3 of 10 (Start time: 05:26:59 PM, 26 August 2022)

When the _____ Hz sampling interval is selected, the signal at the output of the J-K flip-flop has a time period of _____ second

Select the correct option

2,5

0,2

1,1

1,2

Waqas Ahmed: 0306-6295623

Question # 4 of 10 (Start time: 05:27:13 PM, 26 August 2022)

In moore machine the output depends on

Select the correct option

the current state and the output of previous flip flop

Waqas Ahmed: 0306-6295623



only inputs



the current state



the current state and inputs

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Question # 4 of 10 (Start time: 05:27:13 PM, 26 August 2022)

In moore machine the output depends on

Select the correct option

the current state and the output of previous flip flop

Waqas Ahmed: 0306-6295623



only inputs



the current state



the current state and inputs

Question # 5 of 10 (Start time: 05:27:32 PM, 26 August 2022)

RCO stands for _____

Select the correct option



Reconfiguration Clock Output

Waqas Ahmed: 0306-6295623



Ripple Clock Output



Reconfiguration Counter Output



Ripple Counter Output

Question # 6 of 10 (Start time: 05:27:43 PM, 26 August 2022)

In case of cascading Integrated Circuit counters, the enable inputs and RCO of the Integrated Circuit counters allow cascading

Select the correct option

False



Waqas Ahmed: 0306-6295623

True



Question # 7 of 10 (Start time: 05:27:58 PM, 26 August 2022)

A flip-flop is connected to +5 volts and it draws 5 mA of current during its operation, the power dissipation of the flip-flop is

Select the correct option

<input checked="" type="radio"/>	25 mW
<input type="radio"/>	10 mW
<input type="radio"/>	1024 mW
<input type="radio"/>	64 mW

Waqas Ahmed: 0306-6295623

Question # 8 of 10 (Start time: 05:28:16 PM, 26 August 2022)

Design of state diagram is one of many steps used to design

Select the correct option

a clock

any counter

a truncated counter

an UP/DOWN counter

Waqas Ahmed: 0306-6295623

Question # 9 of 10 (Start time: 05:28:25 PM, 26 August 2022)

A decade counter can be implemented by truncating the counting sequence of a MOD-20 counter.

Select the correct option

False



Waqas Ahmed: 0306-6295623

True



Question # 10 of 10 (Start time: 05:28:33 PM, 26 August 2022)

In designing any counter the transition from a current state to the next state is determined by

Select the correct option

<input checked="" type="radio"/>	Current state and inputs
<input type="radio"/>	Only current state
<input type="radio"/>	Only inputs
<input type="radio"/>	current state and outputs

Waqas Ahmed: 0306-6295623

Question # 1 of 10 (Start time: 05:30:25 PM, 26 August 2022)

The minimum time required for the input logic levels to remain stable before the clock transition occurs is known as the _____

Select the correct option

<input checked="" type="radio"/>	Set-up time
<input type="radio"/>	Pulse Stability time (PST)
<input type="radio"/>	Hold time
<input type="radio"/>	Pulse Interval time

Waqas Ahmed: 0306-6295623

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Question # 2 of 10 (Start time: 05:30:41 PM, 26 August 2022)

An Asynchronous Down-counter is implemented (Using J-K flip-flop) by connecting -----

Select the correct option



Q output of all flip-flops to clock input of next flip-flops

Waqas Ahmed: 0306-6295623



Q' output of all flip-flops to K input of next flip-flops



Q' output of all flip-flops to clock input of next flip-flops



Q output of all flip-flops to J input of next flip-flops

Question # 3 of 10 (Start time: 05:31:10 PM, 26 August 2022)

_____ is one of the examples of asynchronous inputs.

Select the correct option

J-K input

Clear Input (CLR)

D input

S-R input

Waqas Ahmed: 0306-6295623

Question # 4 of 10 (Start time: 05:31:20 PM, 26 August 2022)

The glitches due to "Race Condition" can be avoided by using a _____

Select the correct option



Gated flip-flops



Positive-Edge triggered flip-flops



Pulse triggered flip-flops



Negative-Edge triggered flip-flops

Waqas Ahmed: 0306-6295623

Question # 5 of 10 (Start time: 05:31:40 PM, 26 August 2022)

Karnaugh map is used in designing

Select the correct option

<input checked="" type="radio"/>	All of the above
<input type="radio"/>	a counter
<input type="radio"/>	a clock
<input type="radio"/>	an UP/DOWN counter

Waqas Ahmed: 0306-6295623

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Question # 6 of 10 (Start time: 05:31:51 PM, 26 August 2022)

If $S=1$ and $R=1$, for negative edge triggered flip-flop then
 $Q(t+1) = \text{-----}$

Select the correct option

<input type="radio"/>	Input is invalid
<input type="radio"/>	1
<input checked="" type="radio"/>	Invalid
<input type="radio"/>	0

Waqas Ahmed: 0306-6295623

Question # 7 of 10 (Start time: 05:33:17 PM, 26 August 2022)

A positive edge-triggered flip-flop changes its state when -----

Select the correct option



Preset input (PRE) is set

Waqas Ahmed: 0306-6295623



High-to-low transition of clock



Enable input (EN) is set



Low-to-high transition of clock

Question # 8 of 10 (Start time: 05:33:33 PM, 26 August 2022)

The operation of J-K flip-flop is similar to that of the SR flip-flop except that the J-K flip-flop -----

Select the correct option

It does not accept asynchronous inputs

Waqas Ahmed: 0306-6295623

It does not show transition on change in pulse

Sets to clear when both $J = 0$ and $K = 0$

Doesn't have an invalid state



Question # 9 of 10 (Start time: 05:33:55 PM, 26 August 2022)

A mono-stable device only has a single stable state

Select the correct option

True



Waqas Ahmed: 0306-6295623

False



Question # 10 of 10 (Start time: 05:34:03 PM, 26 August 2022)

The terminal count of a 4-bit binary counter in the DOWN mode is _____

Select the correct option

<input checked="" type="radio"/>	0000
<input type="radio"/>	0011
<input type="radio"/>	1100
<input type="radio"/>	1111

Waqas Ahmed: 0306-6295623

Question # 1 of 10 (Start time: 05:36:36 PM, 26 August 2022)

----- is one of the examples of asynchronous inputs.

Select the correct option

D input

Clear Input (CLR)

S-R input

J-K input

Waqas Ahmed: 0306-6295623

Question # 2 of 10 (Start time: 05:36:44 PM, 26 August 2022)

when the transmission line is idle in an asynchronous transmission

Select the correct option



It is set to logic high

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It remains in previous state



It is set to logic low



State of transmission line is not used to start transmission

Question # 3 of 10 (Start time: 05:37:05 PM, 26 August 2022)

When the _____ Hz sampling interval is selected, the signal at the output of the J-K flip-flop has a time period of _____ second

Select the correct option

<input type="radio"/>	0,2
<input type="radio"/>	2,5
<input type="radio"/>	1,1
<input checked="" type="radio"/>	1,2

Waqas Ahmed: 0306-6295623

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Question # 4 of 10 (Start time: 05:37:15 PM, 26 August 2022)

A negative edge-triggered flip-flop changes its state when -----

Select the correct option



High-to-low transition of clock

Waqas Ahmed: 0306-6295623



Low-to-high transition of clock



Preset input (PRE) is set



Enable input (EN) is set

Question # 5 of 10 (Start time: 05:38:08 PM, 26 August 2022)

If the S and R inputs of the gated S-R latch are connected together using a _____ gate then there is only a single input to the latch (A gated D-Latch)

Select the correct option

<input type="radio"/>	OR
<input type="radio"/>	AND
<input checked="" type="radio"/>	NOT
<input type="radio"/>	XOR

Waqas Ahmed: 0306-6295623

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Question # 6 of 10 (Start time: 05:38:40 PM, 26 August 2022)

A-stable multi-vibrator continuously changes from one unstable state to the other without any_____.

Select the correct option

External trigger

Variable

Flip flop

Output

Waqas Ahmed: 0306-6295623

Question # 6 of 10 (Start time: 05:38:40 PM, 26 August 2022)

A-stable multi-vibrator continuously changes from one unstable state to the other without any_____.

Select the correct option

<input checked="" type="radio"/>	External trigger
<input type="radio"/>	Variable
<input type="radio"/>	Flip flop
<input type="radio"/>	Output

Waqas Ahmed: 0306-6295623

Question # 7 of 10 (Start time: 05:40:13 PM, 26 August 2022)

Karnaugh map is used in designing

Select the correct option

- | | |
|----------------------------------|--------------------|
| <input type="radio"/> | a clock |
| <input type="radio"/> | a counter |
| <input checked="" type="radio"/> | All of the above |
| <input type="radio"/> | an UP/DOWN counter |

Waqas Ahmed: 0306-6295623

Question # 9 of 10 (Start time: 05:41:33 PM, 26 August 2022)

In Master-Slave flip-flop the Clock signal is connected to Slave flip-flop using _____ gate

Select the correct option

<input type="radio"/>	OR
<input type="radio"/>	AND
<input checked="" type="radio"/>	NOT
<input type="radio"/>	NAND

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Question # 10 of 10 (Start time: 05:41:52 PM, 26 August 2022)

the terminal count of a modulus-13 binary counter is

Select the correct option

<input type="radio"/>	1100
<input type="radio"/>	0000
<input checked="" type="radio"/>	1101
<input type="radio"/>	1111

Waqas Ahmed: 0306-6295623

Question # 1 of 10 (Start time: 05:47:50 PM, 26 August 2022)

The 74HC163 is a 4-bit Synchronous Counter.it has.....parallel data inputs pins

Select the correct option

8

6

2

4

Waqas Ahmed: 0306-6295623

Question # 2 of 10 (Start time: 05:48:11 PM, 26 August 2022)

In case of cascading Integrated Circuit counters, the enable inputs and RCO of the Integrated Circuit counters allow cascading

Select the correct option

False



Waqas Ahmed: 0306-6295623

True



Question # 3 of 10 (Start time: 05:48:20 PM, 26 August 2022)

In asynchronous digital systems all the circuits change their state with respect to a common clock

Select the correct option

False



Waqas Ahmed: 0306-6295623

True



Question # 4 of 10 (Start time: 05:48:34 PM, 26 August 2022)

Divide-by-160 counter is achieved by using

Select the correct option



DIV 16 and DIV 32



Flip-Flop and DIV 10



DIV 16 and DIV 10



Flip-Flop and DIV 16

Waqas Ahmed: 0306-6295623

Question # 6 of 10 (Start time: 05:50:10 PM, 26 August 2022)

Asynchronous mean that _____

Select the correct option

Each alternative flip-flop after the first one is enabled by the output of the preceding flip-flop

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Each flip-flop after the first one is enabled by the output of the preceding flip-flop



Each flip-flop is enabled by the output of the preceding flip-flop



Each flip-flop except the last one is enabled by the output of the preceding flip-flop

Question # 7 of 10 (Start time: 05:50:37 PM, 26 August 2022)

In Master-Slave flip-flop the Clock signal is connected to Slave flip-flop using _____ gate

Select the correct option

<input checked="" type="radio"/>	NOT
<input type="radio"/>	OR
<input type="radio"/>	AND
<input type="radio"/>	NAND

Waqas Ahmed: 0306-6295623

Question # 8 of 10 (Start time: 05:50:48 PM, 26 August 2022)

A transparent mode means -----

Select the correct option



The changes in the data at the inputs of the latch are seen at the output

Waqas Ahmed: 0306-6295623



Propagation Delay is zero (Output is immediately changed when clock signal is applied)



Input Hold time is zero (no need to maintain input after clock transition)



The changes in the data at the inputs of the latch are not seen at the output

Question # 9 of 10 (Start time: 05:51:11 PM, 26 August 2022)

With a 100 KHz clock frequency, eight bits can be serially entered into a shift register in

Select the correct option

80 mili seconds

8 micro seconds

80 micro seconds

10 micro seconds

Waqas Ahmed: 0306-6295623

Question # 1 of 10 (Start time: 05:54:38 PM, 26 August 2022)

We have a digital circuit. Different parts of circuit operate at different clock frequencies (4MHZ, 2MHZ and 1MHZ), but we have a (4MHZ), to supply the required frequency to each part of circuit, we can get help by using -----

Select the correct option

<input type="radio"/>	D-flipflop
<input type="radio"/>	T-Flip-Flop
<input type="radio"/>	Using S-R Flop-Flop
<input checked="" type="radio"/>	J-K flip-flop

Waqas Ahmed: 0306-6295623

Question # 2 of 10 (Start time: 05:55:06 PM, 26 August 2022)

_____ is one of the examples of asynchronous inputs.

Select the correct option

J-K input

S-R input

Clear Input (CLR)

D input

Waqas Ahmed: 0306-6295623

Question # 3 of 10 (Start time: 05:55:19 PM, 26 August 2022)

A decade counter is _____

Select the correct option



Mod-8 counter



Mod-3 counter



Mod-5 counter



Mod-10 counter

Waqas Ahmed: 0306-6295623

Question # 4 of 10 (Start time: 05:55:28 PM, 26 August 2022)

A mono-stable device only has a single stable state

Select the correct option

True



Waqas Ahmed: 0306-6295623

False



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Question # 5 of 10 (Start time: 05:55:38 PM, 26 August 2022)

----- flip-flops are obsolete now.

Select the correct option

D-Flipflop

T-Flipflop

Master-Slave

Edge-triggered

Waqas Ahmed: 0306-6295623

Question # 7 of 10 (Start time: 05:56:35 PM, 26 August 2022)

For a gated D-Latch if $EN=1$ and $D=1$ then $Q(t+1) = \text{-----}$

Select the correct option

<input type="radio"/>	0
<input checked="" type="radio"/>	1
<input type="radio"/>	$Q(t)$
<input type="radio"/>	Invalid

Waqas Ahmed: 0306-6295623

Question # 8 of 10 (Start time: 05:56:46 PM, 26 August 2022)

_____ occurs when the same clock signal arrives at different times at different clock inputs due to propagation delay.

Select the correct option

<input checked="" type="radio"/>	Clock Skew
<input type="radio"/>	None of given options
<input type="radio"/>	Race condition
<input type="radio"/>	Ripple Effect

Waqas Ahmed: 0306-6295623

Question # 8 of 10 (Start time: 05:56:46 PM, 26 August 2022)

_____ occurs when the same clock signal arrives at different times at different clock inputs due to propagation delay.

Select the correct option

<input checked="" type="radio"/>	Clock Skew
<input type="radio"/>	None of given options
<input type="radio"/>	Race condition
<input type="radio"/>	Ripple Effect

Waqas Ahmed: 0306-6295623

Question # 9 of 10 (Start time: 05:57:02 PM, 26 August 2022)

If a circuit suffers "Clock Skew " problem, the output of circuit can't be guaranteed.

Select the correct option

False



Waqas Ahmed: 0306-6295623

True



Question # 10 of 10 (Start time: 05:57:21 PM, 26 August 2022)

A negative edge-triggered flip-flop changes its state when -----

Select the correct option



Low-to-high transition of clock



High-to-low transition of clock



Preset input (PRE) is set



Enable input (EN) is set

Waqas Ahmed: 0306-6295623


Question # 1 of 10 (Start time: 06:36:03 PM, 26 August 2022)

We have a digital circuit. Different parts of circuit operate at different clock frequencies (4MHZ, 2MHZ and 1MHZ), but we have a (4MHZ), to supply the required frequency to each part of circuit, we can get help by using -----

Select the correct option

<input type="radio"/>	T-Flip-Flop
<input checked="" type="radio"/>	Using S-R Flop-Flop
<input type="radio"/>	J-K flip-flop
<input type="radio"/>	D-flipflop

Waqas Ahmed: 0306-6295623



Question # 2 of 10 (Start time: 06:36:29 PM, 26 August 2022)

A mono-stable device only has a single stable state

Select the correct option

True



Waqas Ahmed: 0306-6295623

False



Question # 3 of 10 (Start time: 06:36:47 PM, 26 August 2022)

The n flip-flops store _____ states.

Select the correct option

<input type="radio"/>	0
<input type="radio"/>	1
<input type="radio"/>	$2^{(n+1)}$
<input checked="" type="radio"/>	2^n

Waqas Ahmed: 0306-6295623

Question # 4 of 10 (Start time: 06:37:10 PM, 26 August 2022)

A negative edge-triggered flip-flop changes its state when -----

Select the correct option



High-to-low transition of clock

Waqas Ahmed: 0306-6295623



Low-to-high transition of clock



Preset input (PRE) is set



Enable input (EN) is set

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Question # 5 of 10 (Start time: 06:37:19 PM, 26 August 2022)

Divide-by-160 counter is achieved by using

Select the correct option



DIV 16 and DIV 32



Flip-Flop and DIV 10



DIV 16 and DIV 10



Flip-Flop and DIV 16

Waqas Ahmed: 0306-6295623

Question # 6 of 10 (Start time: 06:37:31 PM, 26 August 2022)

In gated SR latch, what is the value of the output if EN=1, S=0 and R=1?

Select the correct option

<input type="radio"/>	Invalid
<input checked="" type="radio"/>	0
<input type="radio"/>	Qt
<input type="radio"/>	1

Waqas Ahmed: 0306-6295623

Question # 6 of 10 (Start time: 06:37:31 PM, 26 August 2022)

In gated SR latch, what is the value of the output if EN=1, S=0 and R=1?

Select the correct option

<input type="radio"/>	Invalid
<input checked="" type="radio"/>	0
<input type="radio"/>	Qt
<input type="radio"/>	1

Waqas Ahmed: 0306-6295623

Question # 7 of 10 (Start time: 06:37:54 PM, 26 August 2022)

State of flip-flop can be switched by changing its_____.

Select the correct option

<input checked="" type="radio"/>	Input Signal
<input type="radio"/>	Contemporary Signal
<input type="radio"/>	Momentary Signal
<input type="radio"/>	Output Signal

Waqas Ahmed: 0306-6295623

Question # 8 of 10 (Start time: 06:38:06 PM, 26 August 2022)

When the both inputs of edge-triggered J-K flop-flop are set to logic zero -----

Select the correct option

Q=1 and Q'=0

Q=0 and Q'=1

The output of flip-flop remains unchanged

The flop-flop is triggered

Waqas Ahmed: 0306-6295623

Question # 10 of 10 (Start time: 06:39:20 PM, 26 August 2022)

----- Counters as the name indicates are not triggered simultaneously

Select the correct option

Negative-Edge triggered

Waqas Ahmed: 0306-6295623



Positive-Edge triggered



Synchronous



Asynchronous

Question # 1 of 10 (Start time: 08:02:33 PM, 26 August 2022)

Three cascaded modulus-10 counters have an overall modulus of

Select the correct option

<input type="radio"/>	30
<input type="radio"/>	10000
<input type="radio"/>	100
<input checked="" type="radio"/>	1000

Waqas Ahmed: 0306-6295623

Question # 2 of 10 (Start time: 08:02:42 PM, 26 August 2022)

Flip flops are also called -----

Select the correct option



Bi-stable multivibrators

Waqas Ahmed: 0306-6295623



Bi-stable singlevibrators



Bi-stable transformer



Bi-stable dualvibrators

Question # 3 of 10 (Start time: 08:02:53 PM, 26 August 2022)

The minimum time for which the input signal has to be maintained at the input of flip-flop is called _____ of the flip-flop.

Select the correct option

<input type="radio"/>	Pulse Stability time (PST)
<input type="radio"/>	Pulse Interval time
<input type="radio"/>	Set-up time
<input checked="" type="radio"/>	Hold time

Waqas Ahmed: 0306-6295623

Question # 5 of 10 (Start time: 08:03:47 PM, 26 August 2022)

Each stage of Master-slave flip-flop works at ____ of the clock signal

Select the correct option



One half

Waqas Ahmed: 0306-6295623



Each stage works on complete clock signal



One third



One fourth

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Question # 6 of 10 (Start time: 08:04:11 PM, 26 August 2022)

A decade counter can be implemented by truncating the counting sequence of a MOD-20 counter.

Select the correct option

False



Waqas Ahmed: 0306-6295623

True



Question # 7 of 10 (Start time: 08:04:22 PM, 26 August 2022)

A-stable multi-vibrator continuously changes from one unstable state to the other without any_____.

Select the correct option

<input checked="" type="radio"/>	External trigger
<input type="radio"/>	Flip flop
<input type="radio"/>	Output
<input type="radio"/>	Variable

Waqas Ahmed: 0306-6295623

Question # 8 of 10 (Start time: 08:04:30 PM, 26 August 2022)

The 74HC163 is a 4-bit Synchronous Counter.it has.....parallel data inputs pins

Select the correct option

2

8

4

6

Waqas Ahmed: 0306-6295623

Question # 9 of 10 (Start time: 08:04:49 PM, 26 August 2022)

RCO stands for _____

Select the correct option



Reconfiguration Clock Output

Waqas Ahmed: 0306-6295623



Ripple Clock Output



Ripple Counter Output



Reconfiguration Counter Output

Question # 1 of 10 (Start time: 08:06:57 PM, 26 August 2022)

A Divide-by-20 counter can be achieved by using

Select the correct option

<input type="radio"/>	Flip-Flop and DIV 32
<input type="radio"/>	Div 10 and DIV 16
<input checked="" type="radio"/>	Flip-Flop and DIV 10
<input type="radio"/>	Flip-Flop and DIV 16

Waqas Ahmed: 0306-6295623

Question # 2 of 10 (Start time: 08:07:11 PM, 26 August 2022)

In gated SR latch, what is the value of the output if EN=1, S=0 and R=0?

Select the correct option

<input type="radio"/>	0
<input type="radio"/>	Qt
<input type="radio"/>	Invalid
<input checked="" type="radio"/>	1

Waqas Ahmed: 0306-6295623

Question # 3 of 10 (Start time: 08:07:21 PM, 26 August 2022)

when the transmission line is idle in an asynchronous transmission

Select the correct option

State of transmission line is not used to start transmission

Waqas Ahmed: 0306-6295623

It is set to logic low

It is set to logic high

It remains in previous state

Question # 4 of 10 (Start time: 08:07:34 PM, 26 August 2022)

A synchronous decade counter will have _____ flip-flops

Select the correct option



7



3



4



10

Waqas Ahmed: 0306-6295623

Question # 5 of 10 (Start time: 08:07:41 PM, 26 August 2022)

In case of cascading Integrated Circuit counters, the enable inputs and RCO of the Integrated Circuit counters allow cascading

Select the correct option

False



Waqas Ahmed: 0306-6295623

True



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Question # 6 of 10 (Start time: 08:07:50 PM, 26 August 2022)

A one-shot mono-stable device contains -----

Select the correct option

NAND gate, Resistor, Capacitor and NOT Gate

Waqas Ahmed: 0306-6295623

AND gate, Resistor, Capacitor and NOT Gate

XNOR gate, Resistor, Capacitor and NOT Gate

NOR gate, Resistor, Capacitor and NOT Gate

Question # 7 of 10 (Start time: 08:08:11 PM, 26 August 2022)

_____ is one of the examples of asynchronous inputs.

Select the correct option

S-R input

Clear Input (CLR)

J-K input

D input

Waqas Ahmed: 0306-6295623

Question # 8 of 10 (Start time: 08:08:23 PM, 26 August 2022)

When the _____ Hz sampling interval is selected, the signal at the output of the J-K flip-flop has a time period of _____ second

Select the correct option

<input type="radio"/>	2,5
<input type="radio"/>	1,1
<input type="radio"/>	0,2
<input checked="" type="radio"/>	1,2

Waqas Ahmed: 0306-6295623

Question # 9 of 10 (Start time: 08:08:34 PM, 26 August 2022)

A flip-flop is presently in SET state and must remain SET on the next clock pulse. What must j and K be?

Select the correct option

J = 1, K = 0

Waqas Ahmed: 0306-6295623

J = 0, K = X(Don't care)

J = X(Don't care), K = 0

J = 1, K = X(Don't care)

Question # 6 of 10 (Start time: 02:22:39 PM, 30 August 2022)

When did Yahya Khan hand over power to Zulfiqar Ali Bhutto?

Select the correct option



20th December, 1971



20th October, 1971

Waqas Ahmed: 0306-6295623



20th November, 1971



20th September, 1971

Question # 7 of 10 (Start time: 02:22:52 PM, 30 August 2022)

Who abrogated the 1956 Constitution of Pakistan?

Select the correct option

Yahya Khan

Tikka Khan

Iskandar Mirza

Ayub Khan

Waqas Ahmed: 0306-6295623

Question # 8 of 10 (Start time: 02:23:03 PM, 30 August 2022)

What was the duration of second term of Benazir Bhutto's Government?

Select the correct option



October 1993–November 1996



October 1991–November 1992

Waqas Ahmed: 0306-6295623



October 1995–November 1998



October 1983–November 1986

Question # 9 of 10 (Start time: 02:23:23 PM, 30 August 2022)

Pakistan Peoples Party won _____ number of seats in general elections of 1970.

Select the correct option



92



89



81



98

Waqas Ahmed: 0306-6295623

Question # 1 of 10 (Start time: 02:27:49 PM, 30 August 2022)

Who was the interim Prime Minister during the period of April-May 1993?

Select the correct option



Ghulam Mustafa Jatoi



Dr. Moeen Qureshi

Waqas Ahmed: 0306-6295623



Balakh Sher Mazari



Malik Meraj Khalid

Question # 5 of 10 (Start time: 02:28:57 PM, 30 August 2022)

Where are the deposits of Sulphur found in Pakistan?

Select the correct option



Jhelum, Mianwali, Attock



Kalat, Khairpur, Mardan

Waqas Ahmed: 0306-6295623



Deegari, Sharig, Soer



Thatta, Tharparkar, Manara

Question # 6 of 10 (Start time: 02:29:14 PM, 30 August 2022)

Line of Control (LOC) is a boundary between Pakistan and _____.

Select the correct option

China

Afghanistan

Iran

India

Waqas Ahmed: 0306-6295623

Question # 7 of 10 (Start time: 02:29:28 PM, 30 August 2022)

According to 1973 Constitution, Pakistan had a _____.

Select the correct option

Tricameral Parliament

Multicameral Parliament

Waqas Ahmed: 0306-6295623

Unicameral Parliament

Bicameral Parliament

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Question # 8 of 10 (Start time: 02:29:39 PM, 30 August 2022)

What was the status of the 1973 Constitution during 1977 - 1985?

Select the correct option

Operational after changes

Operational

Waqas Ahmed: 0306-6295623

Suspended

Abrogated

Question # 9 of 10 (Start time: 02:29:51 PM, 30 August 2022)

When Usher was imposed on agriculture products?

Select the correct option



1981



1980

Waqas Ahmed: 0306-6295623



1982



1983



----- Counters as the name indicates are not triggered simultaneously

Select the correct option



Asynchronous

Waqas Ahmed: 0306-6295623



Positive-Edge triggered



Negative-Edge triggered



Synchronous

Question # 3 of 10 (Start time: 05:11:58 PM, 26 August 2022)

A transparent mode means -----

Select the correct option



Propagation Delay is zero (Output is immediately changed when clock signal is applied)

Waqas Ahmed: 0306-6295623



The changes in the data at the inputs of the latch are seen at the output



The changes in the data at the inputs of the latch are not seen at the output



Input Hold time is zero (no need to maintain input after clock transition)

Question # 4 of 10 (Start time: 05:12:24 PM, 26 August 2022)

If data is brought into the J terminal and its complement to the K terminal, a J-K flip-flop operates as a(n) _____.

Select the correct option

<input type="radio"/>	N flip-flop
<input type="radio"/>	S-C flip-flop
<input type="radio"/>	S-R flip-flop
<input checked="" type="radio"/>	D flip-flop

Waqas Ahmed: 0306-6295623

Question # 5 of 10 (Start time: 05:13:21 PM, 26 August 2022)

A stage in the shift register consists of

Select the correct option



a flip flop

Waqas Ahmed: 0306-6295623



a latch



a byte of storage



four bits of storage

Question # 6 of 10 (Start time: 05:13:33 PM, 26 August 2022)

----- is one of the examples of synchronous inputs.

Select the correct option

Clear Input (CLR)



J-K input



Preset input (PRE)



EN input

Waqas Ahmed: 0306-6295623

Question # 7 of 10 (Start time: 05:13:59 PM, 26 August 2022)

With a 100 KHz clock frequency, eight bits can be serially entered into a shift register in

Select the correct option

80 mili seconds

8 micro seconds

10 micro seconds

80 micro seconds

Waqas Ahmed: 0306-6295623

Question # 8 of 10 (Start time: 05:14:22 PM, 26 August 2022)

_____ is one of the examples of asynchronous inputs.

Select the correct option



J-K input



Clear Input (CLR)



S-R input



D input

Waqas Ahmed: 0306-6295623

Question # 8 of 10 (Start time: 05:14:22 PM, 26 August 2022)

----- is one of the examples of asynchronous inputs.

Select the correct option

J-K input

Clear Input (CLR)

S-R input

D input

Waqas Ahmed: 0306-6295623

Question # 9 of 10 (Start time: 05:15:37 PM, 26 August 2022)

Design of state diagram is one of many steps used to design

Select the correct option

an UP/DOWN counter

a truncated counter

any counter

a clock

Waqas Ahmed: 0306-6295623

Question # 10 of 10 (Start time: 05:15:55 PM, 26 August 2022)

The 74HC163 is a 4-bit Synchronous Counter.it has.....data output pins

Select the correct option

2

4

8

6

Waqas Ahmed: 0306-6295623

Question # 1 of 10 (Start time: 05:17:38 PM, 26 August 2022)

For a down counter that counts from (111 to 000), if current state is "101" the next state will be _____

Select the correct option

<input type="radio"/>	111
<input type="radio"/>	110
<input type="radio"/>	010
<input checked="" type="radio"/>	none of given options

Waqas Ahmed: 0306-6295623

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Question # 2 of 10 (Start time: 05:18:02 PM, 26 August 2022)

The minimum time for which the input signal has to be maintained at the input of flip-flop is called _____ of the flip-flop.

Select the correct option

<input type="radio"/>	Pulse Interval time
<input type="radio"/>	Set-up time
<input type="radio"/>	Pulse Stability time (PST)
<input checked="" type="radio"/>	Hold time

Waqas Ahmed: 0306-6295623

Question # 3 of 10 (Start time: 05:18:18 PM, 26 August 2022)

Divide-by-160 counter is achieved by using

Select the correct option



Flip-Flop and DIV 16



DIV 16 and DIV 32



Flip-Flop and DIV 10



DIV 16 and DIV 10

Waqas Ahmed: 0306-6295623

Question # 5 of 10 (Start time: 05:19:38 PM, 26 August 2022)

The term hold always means _____.

Select the correct option

<input type="radio"/>	$Q=0, Q^{\bar{}}=1$
<input type="radio"/>	$Q=1, Q^{\bar{}}=0$
<input type="radio"/>	$Q=0, Q^{\bar{}}=0$
<input checked="" type="radio"/>	No change

Waqas Ahmed: 0306-6295623

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