

CS401 Computer Architecture and Assembly Language

Question # 1 **Instruction Pointer holds the address of the**

- Previous instruction to be executed
- Current instruction
- Next instruction to be executed**
- None of the given

Question # 2 **Register whose each bit specify a different meaning is**

- Accumulator Register
- Pointer Register
- Index register
- Flag register**

Question # 3 **By default CS is associated with**

- SS
- BP
- CX
- IP**

Question # 4 **Memory to Memory operation is allowed**

- True
- False**

Question # 5 **Size Mismatch Error is a syntax error**

- False
- True

Question # 6 **unconditional jump can be**

- near
- short
- far
- all of the given**

Question # 7 Register are storage cell

- Outside the processor
- Both inside and outside the processor
- Inside the processor**
- None of the given

Question # 8 **Register to Register Operation is not allowed**

- True
- False

Q9. The operation of CMP is to subtract source from destination

- True**
- False

Q10. Size Mismatch Error is a syntax error

False

True

Q11. Unconditional jump

Execute in every condition whether true or false

If the condition is true

If the condition is false

None of the given

Q12. Which type of Rotation it is "Every bit moves one position to the right and the bit dropped from the right is inserted at the left. This bit is also copied into the carry flag."

ROL

RCR

RCL

None of the given

Q13. Assembly language is not a low level language.

True

False

Q14. In JA jump is not taken after a CMP if the unsigned destination is larger than the unsigned source.

True

False

Q15. Group of bits processor uses to inform memory which element to read/write is collectively known as

Control bus

Data bus

Address bus

RAM

Q16. Memory to Memory operation is allowed

True

False

Q17. 90 is the op-code of

Do nothing

Add

Subtract

Multiplication

Q18. we cannot add two base register i.e. (bx+bp) or can't use in an instruction

True

False

Q19. Intel follow

Little endian

Big endian

Both little endian and big endian

None of the given

Q20.SHL and SAL are same

True

False

Q21.The first 16-bit processor produced by "Intel" was 8085

True

False

Q22.The first 16-bit processor produced by "Intel" was 8085

True

False

Q23.The extension of assembly language file is

.doc

.com

.lst

.asm

Q24.When a large number is subtracted from a smaller number, a borrow is needed; in this case which flag will be set

ZF

CF

SF

OF

Q25.All the addressing mechanisms in iAPX88 return a number called _____ address.

Effective address

Physical address

Direct address

None of the given

Q26.Which type of shifting is "Inserts a zero from the left and moves every bit one position to the right and copies the rightmost bit in the carry flag."

SHL

SAL

SAR

None of the given

Q27.mov [1234], ax is an example of

Direct addressing

Base register indirect

Base+index

None of the given

Q28.Registers are also called scratch pad ram

True

False

Q29.The basic function of register is to?

Hold the operand

Hold the operator

Hold both the operator and operand

None of the given

Q30. The jump is taken if the last arithmetic operation changed the sign unexpectedly.

JO

JNO

JNZ

JZ

Q31. In JA jump is not taken after a CMP if the unsigned destination is larger than the unsigned source.

True

False

Q32. which type of rotation it is "The carry flag is inserted from the left, every bit moves one position to the right, and the right most bit is dropped in the carry flag. "

RCR

ROL

RCL

ROR

Q33. which type of rotation it is "The carry flag is inserted from the left, every bit moves one position to the right, and the right most bit is dropped in the carry flag. "

RCR

ROL

RCL

ROR

Q34. This jump is taken if the last arithmetic operation produced a number in its destination that has even parity, which jump is taken

JP

JPE

JNP

Both JP and JPE

Q35. In direct addressing the memory address given in the instruction is

When a large number is subtracted from a smaller number, a borrow is needed; in this case which flag will be set

ZF

CF

SF

OF

Q36. SHL and SAL are same

True

False

Q37. we cannot Subtract index register from the base register (bx-si)in assembly language

_vuzs

True

False

Q38. Group of bits processor uses to inform memory which element to read/write is collectively known as

Control bus

Data bus

Address bus

RAM

Q39. The execution of the instruction "mov word [ES: 160], 0x1230" will print a character on the screen at:

First column of second row

Second column of first row

Second column of second row

First column of third row

Q40. Which type of shift operation is "The zero bit is inserted from the right and every bit moves one position to its left with the most significant bit dropping into the carry flag"

shl

sal

sar

both shl and sal

Q41. In XOR operation the output is 1 if

both inputs are same

both inputs are different

I and II

none of the above

Q42. Far jump is position relative.

true

false

Q43. After the execution of STOSWB, the CX will be:

Incremented by 1

Decrement by 1

Incremented by 2

Decrement by 2

Q44. In And the output only one if

Both inputs are 0

One is 0 and other is 1

Both are 1

None of the given

Q45. transfers the word at the current top of stack (pointed to by SP) to the destination operand and then increments SP by two to point to the new top of stack.

push

pop

call

none of given

Q46. When the operand of DIV instruction is of 16-bits then implied dividend will be stored in

AX register

The concatenation of DX and AX

The concatenation of ES and AX

The concatenation of DS and BX

Q47. In near jump we jump anywhere within a segment.

True

false

Q48. In string instructions, the mode is called auto-increment mode when:

CX is set

ZF is cleared

DF is set

DF is cleared

Q49. How many characters standard ASCII has?

512

256

128

64

Q50. Which of the following is the pair of register used to access memory in string instructions?

DI and BP

SI and BP

DI and SI

DS and SI

Q51. RET do not pops the word at the top of the stack (pointed to by register SP) into the instruction pointer but increments SP by two.

true

false

Q52. The operations of placing items on the stack and removing them from there are called push and ret.

true

false

Q53. In a video memory, each screen location corresponds to:

8 byte

4 byte

2 byte

1 byte

Q54. The Operation of Push is if "we push ax" then $SP \leftarrow SP+2$ $[SP] \leftarrow AX$

true

false

Q55. The purpose of MOVS instruction is:

Move register to register

Move register to a memory location

Move a memory location to register

Move memory to memory

Q56. When the operand of DIV instruction is of 16 bits then implied dividend will be of

32 bit

64 bit

16 bit

8 bit

Q57. Parameters clears from the stack by the

caller

callee

caller and callee

none of given

Question No: 1 SP is associated with..... By default

▶ **SS page 26**

▶ DS

▶ CS

▶ ES

Question No: 2 Which bit of the attributes byte represents the red component of foreground color

▶ 5

▶ 4

▶ 3

▶ **2 page73**

Question No: 3 An 8 x 16 font is stored in _____ bytes.

▶ 2

▶ 4

▶ 8

▶ **16 page142**

Question No: 4 In DOS input buffer, the number of characters actually read on return is stored in _____ byte.

▶ **third**

▶ **fourth**

▶ **first**

▶ **second page 144**

Question No: 5 Which of the following gives the more logical view of the storage medium

▶ BIOS

▶ **DOS 153page**

▶ Both

▶ None

Question No: 6 In STOSW instructions, when DF is clear, SI is

▶ Incremented by 1

▶ **Incremented by 2**

▶ Decrement by 1

▶ Decrement by 2

Question No: 7 Which of the following interrupts is Non makeable interrupt

▶ **INT 2 PAGE 97**

▶ INT 3

▶ INT 0

▶ INT 1

Question No: 8 Which of the following IRQs is connected to serial port COM 2?

- ▶ IRQ 0
- ▶ IRQ 1
- ▶ IRQ 2
- ▶ **IRQ 3** PAGE 106

Question No: 9 The time interval between two timer ticks is ?

- ▶ 40ms
- ▶ 45ms
- ▶ 50ms
- ▶ **55ms** PAGE 114

Question No: 10 The physical address of IDT(Interrupt Descriptor Table) is stored in _____

- ▶ GDTR
- ▶ **IDTR** PAGE 174
- ▶ IVT

Question No: 11 In NASM an imported symbol is declared with the while and exported symbol is declared with the

- ▶ Global directive, External directive
- ▶ **External directive, Global directive** PAGE 181
- ▶ Home Directive, Foreign Directive
- ▶ Foreign Directive, Home Directive

Question No: 12 In 68K processors there is a 32bit that holds the address of currently executing instruction

- ▶ Program counter PAGE 184
- ▶ Stack pointer
- ▶ **Register**
- ▶ Stack

Question No: 13 Single step interrupt is

- ▶ Hardware interrupt
- ▶ **Like divide by zero interrupt**
- ▶ Like divide by 1 interrupt
- ▶ Software interrupt

Question No: 14 Which of the following is NOT true about registers:

- ▶ Their operation is very much like memory
- ▶ Intermediate results may also be stored in registers
- ▶ They are also called scratch pad ram
- ▶ **None of given options**

Question No: 15 Types of jump are:

- ▶ Short, near
- ▶ **short, near, far** PAGE 37
- ▶ Near, far
- ▶ short, far

Question No: 16 MS DOS uses ____ display mode.

- ▶ **Character based** page 72
- ▶ Graphics based
- ▶ Numeric based
- ▶ Console based

Question No: 17 Which of the following IRQs is derived by a timer device?

- ▶ **IRQ 0** page 106
- ▶ IRQ 1
- ▶ IRQ 2
- ▶ IRQ 3

Question No: 18 In programmable interrupt controller, which of the following ports is referred

as a *control port*.

- ▶ 19
- ▶ **20** page 107
- ▶ 21
- ▶ 22

Question No: 19 INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____ register.

- ▶ **AL** page 144
- ▶ BL
- ▶ CL
- ▶ BH

Question No: 20 In 9pin DB 9, which pin number is assigned to DSR (Dataset Ready) ?

- ▶ 4
- ▶ 5
- ▶ **6** page 146
- ▶ 7

Question No:21 In 9pin DB 9, which pin number is assigned to TD (Transmitted Data) ?

- ▶ 1
- ▶ 2
- ▶ **3** page 146
- ▶ 4

Question No: 22 In 9pin DB 9, Signal ground is assigned on pin number

- ▶ 4
- ▶ **5** page 146
- ▶ 6
- ▶ 3

Question No: 23 8088 is a.....

- ▶ **16 bit processor** page 6
- ▶ 32 bit processor
- ▶ 64 bit processor
- ▶ 128 bit processor

Question No: 24The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).

- ▶ 1
- ▶ **0** page 168
- ▶ -1
- ▶ -2

Question No: 25 VESA (Video Electronics Standards Association) organizes 16 color bits for every pixel in

- ▶ 5:5:5 formats
- ▶ **5:6:5 format** page 172
- ▶ 6:5:6 formats
- ▶ 5:6:7 formats

Question No: 26 Which flags are NOT used for mathematical operations ?

- ▶ Carry, Interrupt and Trap flag.
- ▶ **Direction, Interrupt and Trap flag.** Page 126
- ▶ Direction, Overflow and Trap flag.
- ▶ Direction, Interrupt and Sign flag.

Question No: 27 Suppose AL contains 5 decimal then after two left shifts produces the value as

- ▶ **5**
- ▶ 10
- ▶ 15

▶ 20

Question No: 28 In graphics mode a location in video memory corresponds to a _____ on the screen.

- ▶ Line
- ▶ Dot page 142
- ▶ Circle
- ▶ Rectangle

Question No: 29 Creation of threads can be

- ▶ Static
- ▶ Dynamic page 133
- ▶ Easy
- ▶ difficult

Question No: 30 The thread registration code initializes the PCB and adds it to the linked list so that they _____ will give it a turn.

- ▶ Assembler
- ▶ Scheduler page 133
- ▶ Linker
- ▶ Debugger

Question No:31 VESA VBE 2.0 is a standard for

- ▶ High resolution Mode page 172
- ▶ Low resolution Mode
- ▶ Medium resolution Mode
- ▶ Very High resolution Mode

Question No: 32 Which of the following IRQs is derived by a key board?

- ▶ IRQ 0
- ▶ IRQ 1 page106
- ▶ IRQ 2
- ▶ IRQ 3

Question No: 33 Which of the following IRQs is used for Floppy disk drive?

- ▶ IRQ 4
- ▶ IRQ 5
- ▶ IRQ 6 page 106
- ▶ IRQ 7

Question No: 34 Which of the following pins of a parallel port connector are grounded?

- ▶ 10-18
- ▶ 18-25 117page
- ▶ 25-32
- ▶ 32-39

Question No: 35The physical address of IDT(Interrupt Descriptor Table) is stored in _____

- ▶ GDTR
- ▶ IDTR 174page
- ▶ IVT
- ▶ IDTT

Question No: 36In NASM an imported symbol is declared with the while and exported symbol is declared with the.....

- ▶ Global directive, External directive
- ▶ External directive, Global directive 181pages
- ▶ Home Directive, Foreign Directive
- ▶ Foreign Directive, Home Directive

Question No: 37 In 68K processors there is a program counter (PC) that holds the address of currently executing instruction

- ▶ 16bit

▶ **32bit** 183page

▶ 64bit

Question No: 38 To reserve 8-bits in memory ___ directive is used.

▶ **db** 17page

▶ dw

▶ dn

▶ dd

For single byte we use db and for two bytes we use dw.

Question No: 39In the “mov ax, 5” 5 is the _____ operand.

▶ **source**

▶ destination Page 18

▶ memory

▶ register

Question No: 40RETF will pop the segment address in the

▶ **CS register page 61**

▶ DS register

▶ SS register

▶ ES register

Question No: 41For the execution of the instruction “DIV BL”, the implied dividend will be stored in

▶ **AX**

▶ BX

▶ CX

▶ DX

Question No: 42When a number is divided by zero”A Division by 0” interrupt is generated. Which instruction is used for this purpose

▶ INT 0

▶ INT 1

▶ INT 2

▶ **This interrupt is generated automatically**

Question No: 43 INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____ register.

▶ **AL**

▶ **BL**

▶ **CL**

▶ **BH**

Question No: 44 BIOS sees the disks as

▶ Logical storage

▶ **Raw storage page 148**

▶ In the form of sectors only

▶ In the form of tracks only

Question No: 45 In 9pin DB 9, which pin number is assigned to CD (Carrier Detect)?

▶ **1**

▶ 2

▶ 3

▶ 4

Question No: 46In 9pin DB 9, Signal ground is assigned on pin number

▶ 4

▶ **5**

▶ 6

▶ 3

Question No: 47In 9pin DB 9, RI (Ring Indicator) is assigned on pin number

- ▶ 6
- ▶ 7
- ▶ 8
- ▶ 9

Question No: 48 Motorola 68K processors have 23bit general purpose registers.

- ▶ 4
- ▶ 8
- ▶ 16
- ▶ 32

Question No: 49 When two devices in the system want to use the same IRQ line then what will happen?

- ▶ An IRQ Collision
- ▶ **An IRQ Conflict** 106page
- ▶ An IRQ Crash
- ▶ An IRQ Blockage

Question No: 50 In the instruction **MOV AX, 5** the number of operands are

- ▶ 1
- ▶ 2
- ▶ 3
- ▶ 4

Question No: 51 Which flags are NOT used for mathematical operations?

- ▶ Carry, Interrupt and Trap flag.
- ▶ **Direction, Interrupt and Trap flag.** 125page
- ▶ Direction, Overflow and Trap flag.
- ▶ Direction, Interrupt and Sign flag.

Question No: 52 Suppose AL contains 5 decimal then after two left shifts produces the value as

- ▶ 5
- ▶ 10
- ▶ 15
- ▶ 20

Question No: 53 VESA VBE 2.0 is a standard for

- ▶ **High resolution Mode** 172PAGE
- ▶ Low resolution Mode
- ▶ Medium resolution Mode
- ▶ Very High resolution Mode

Question No: 54 Which of the following IRQs is derived by a key board?

- ▶ IRQ 0
- ▶ **IRQ 1** 106PAGE
- ▶ IRQ 2
- ▶ IRQ 3

Question No: 55 In the “mov ax, 5” 5 is the _____ operand.

- ▶ **Source** 16page
- ▶ Destination
- ▶ Memory
- ▶ Register

Question No: 56 RETF will pop the segment address in the

- ▶ **CS register**
- ▶ DS register
- ▶ SS register
- ▶ ES register

Question No: 57 For the execution of the instruction “DIV BL”, the implied dividend will be stored in

- ▶ AX
- ▶ BX
- ▶ CX
- ▶ DX

Question No: 58 INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____ register.

- ▶ AL
- ▶ BL
- ▶ CL
- ▶ BH

Question No: 59 In 9pin DB 9, which pin number is assigned to CD (Carrier Detect)?

- ▶ 1 104page
- ▶ 2
- ▶ 3
- ▶ 4

Question No: 60 In 9pin DB 9, Signal ground is assigned on pin number

- ▶ 4
- ▶ 5 104page
- ▶ 6
- ▶ 3

Question No: 61 In 9pin DB 9, RI (Ring Indicator) is assigned on pin number

- ▶ 6
- ▶ 7
- ▶ 8
- ▶ 9 104page

Question No: 62 Motorola 68K processors have..... 23bit general purpose registers.

- ▶ 4
- ▶ 8
- ▶ 16 183page
- ▶ 32

Question No: 63In the instruction MOV AX, 5 the number of operands are

- ▶ 1
- ▶ 2
- ▶ 3
- ▶ 4

Question No: 64 Which of the following is not true about registers:

- Their operation is very much like memory
- Intermediate results may also be stored in registers.
- They are also called scratch pad ram
- None of given options.

Question No: 65 move [bp], al moves the one byte content of the AL register to the address contained in BP register in the current

- ⌘ Stack segment 27page
- Code segment
- Data segment
- Extra segment

Question No: 66 In a rotate through carry right (RCR) instruction applied on a 16 bit word effectively there is

- 16 bits rotation
- 1 bit rotation
- ⌘ 17 bits rotation 45page
- 8 bits rotation

Question No: 67 The 8088 stack works on

Word sized elements 60page

Byte sized elements

Double sized element

Nibble sized element

Question No: 68 An 8 x 16 font is stored in _____ bytes.

2

4

8

16 142page

Question No: 69 INT 10 is used for _____ services.

RAM

Disk

BIOS video 141PAGE

DOS video

Question No: 70 Priority of IRQ 0 interrupt is

medium

high

highest 106PAGE

low

Question No: 71 Threads can have function calls, parameters and _____ variables.

global

local 133PAGE

legal

illegal

Question No: 72 How many prevalent calling conventions do exist

1

2 180PAGE

3

4

Two prevalent calling conventions are the C calling convention and the Pascal calling convention

Question No: 73 In 9pin DB 9 DSR is assigned on pin number

4

5

6 164PAGE

7

Question No: 74 In 9pin DB 9 CTS is assigned on pin number

6

7

8 164PAGE

9

Question No: 75 In 9pin DB 9 CD is assigned on pin number

1 164PAGE

2

3

4

Question No: 76 In 9pin DB 9 RD is assigned on pin number

1

2 164PAGE

3

4

Question No: 77 in device attribute word which of the following bit decides whether it is a

character device or a block device

- Bit 12
- Bit 13
- Bit 14

Bit 15 158PAGE

Question No: 78 Video services are classified into _____ broad categories

2 141PAGE

- 3
- 4
- 5

Question No: 79 In STOSB instruction, when DF is clear, SI is

- Incremented by 1
- Incremented by 2
- Decrement by 1
- Decrement by 2

Question No: 80 The process of sending signals back and forth is called

- Activity
- Hand-shaking
- Interruption
- Time clicking

Question No: 81 Which of the following interrupts is used for Arithmetic overflow

- INT 1
- INT 2
- INT 3

INT 4 98page

Question No: 82 which of the following is a special type of interrupt that returns to the same instruction instead of the next instruction

Divide overflow interrupt 99page

- Debug interrupt
- Arithmetic overflow interrupt
- Change of sign interrupt

Question No: 83 Which of the following IRQs is derived by a timer device?

IRQ 0 106page

- IRQ 1
- IRQ 2
- IRQ 3

Question No: 84 Which of the following IRQs is connected to serial port COM 2?

- IRQ 0
- IRQ 1
- IRQ 2

IRQ 3 106page

Question No: 85 An End of Interrupt (EOI) signal is sent by

- Handler
- Processor
- IRQ

PIC 106page

Question No: 86 The source registers in OUT is

AL or AX 107page

- BL or BX
- CL or CX
- DL or DX

Question No: 87 In programmable interrupt controller which of the following ports is used

for selectively enabling or disabling interrupts

19

20

21 107page

22

Question No: 88 The number of pins in a parallel port connector are?

20

25 117page

30

35

Question No: 89 Suppose a decimal number 35 when its binary is shifted to write two places the new number will become

35

70

140

17

Question No: 90 A 32bit address register can access up to..... of memory so Memory access has increased a lot.

2GB

4GB 168page

6GB

8GB

Question No: 91 The physical address of the stack is obtained by

▶ SS:SI combination

▶ SS:SP combination

▶ ES:BP combination

▶ ES:SP combination

Question No: 92 Value of AH in the write Graphics pixel service is

▶ 0Ch

▶ 0Bh

▶ 1Ch

▶ 2Ch

Question No: 93 Threads can have function calls, parameters and _____ variables.

▶ Global

▶ Local

▶ Legal

▶ Illegal

Question No: 94 Creation of threads can be

▶ Static

▶ Dynamic

▶ Easy

▶ Difficult

Question No: 95 How many prevalent calling conventions do exist

▶ 1

▶ 2

▶ 3

▶ 4

Question No: 96 VESA VBE 2.0 is a standard for

▶ High resolution Mode

▶ Low resolution Mode

▶ Medium resolution Mode

▶ Very High resolution Mode

Question No: 97 The serial port connection is a ----- connector

- ▶ 9pin DB 9
- ▶ 8pin DB 9
- ▶ 3pin DB 9
- ▶ 9pin DB 5

Question No: 98 Which of the following gives the more logical view of the storage medium

- ▶ BIOS
- ▶ DOS
- ▶ Both
- ▶ None

Question No: 99 In STOSB instructions, when DF is clear, SI is

- ▶ Incremented by 1
- ▶ Incremented by 2
- ▶ Decrement by 1
- ▶ Decrement by 2

Question No: 100 After the execution of STOSW the CX will be

- ▶ Decrement by 1
- ▶ Decrement by 2
- ▶ Increment by 1
- ▶ Increment by 2

Question No: 101 IRQ is referred to

- ▶ Eight input signals
- ▶ One output signal
- ▶ One input signals
- ▶ Eight output signals

Question No: 102 Which of the following IRQs is derived by a key board?

- ▶ IRQ 0
- ▶ IRQ 1
- ▶ IRQ 2
- ▶ IRQ 3

Question No: 103 Which of the following IRQs is connected to serial port COM 1?

- ▶ IRQ 4
- ▶ IRQ 5
- ▶ IRQ 6
- ▶ IRQ 7

Question No: 104 The physical address of IDT (Interrupt Descriptor Table) is stored in

- ▶ GDTR
- ▶ IDTR
- ▶ IVT
- ▶ IDTT

Question No: 105 Assembly language is:

- ▶ Low-level programming language
- ▶ High-level programming language
- ▶ Also known as machine language
- ▶ Not considered closer to the computer

Question No: 106 The number of bits required to access 1MB of memory are

- ▶ 16 bits
- ▶ 32 bits
- ▶ Depends on the processor architecture
- ▶ 20 bits

Question No: 107 In STOSB instructions, SI is decremented or incremented by

- ▶ 3
- ▶ 2
- ▶ 1
- ▶ 4

Question No: 108 In programmable interrupt controller, which of the following ports is referred as a control port.

- ▶ 19
- ▶ 20
- ▶ 21
- ▶ 22

Question No: 109 INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____ register.

- ▶ AL
- ▶ BL
- ▶ CL
- ▶ BH

Question No: 110 In device attribute word, which of the following bit decides whether it is a character device or a block device

- ▶ Bit 12
- ▶ Bit 13
- ▶ Bit 14
- ▶ Bit 15

Question No: 111 In 9pin DB 9, which pin number is assigned to CTS (Clear To Send)?

- ▶ 6
- ▶ 7
- ▶ 8
- ▶ 9

Question No: 112 In 9pin DB 9, which pin number is assigned to RD (Received Data)?

- ▶ 1
- ▶ 2
- ▶ 3
- ▶ 4

Question No: 113 VESA (Video Electronics Standards Association) organizes 16 color bits for every pixel in

- ▶ 5:5:5 formats
- ▶ 5:6:5 formats
- ▶ 6:5:6 formats
- ▶ 5:6:7 formats

Question No: 114 Motorola 68K processors have..... 23bit general purpose registers.

- ▶ 4
- ▶ 8
- ▶ 16
- ▶ 32

Question No: 115 Programmable Interrupt Controller (PIC) has

- ▶ One input signals and eight output signals
- ▶ One input signal and one output signal
- ▶ Eight input signals and one output signals
- ▶ Eight input signals and eight output signals

Question No: 116 Video services are classified into..... broad categories.

- ▶ 5
- ▶ 4