

Learning4help by Waqas

Asslam-o-Alaikum! In this file we'll provide you **CS604 quiz 4 2023 Important Finalterm MCQs. Must Prepare Before Finalterm Exams.** Verify Answers yourself too. If you found mistake then inform me.

If you found mistake then let us know.

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Two-dimensional array can be traversed in row major order if it is traversed _____wise.

Select the correct option

Reload Math Equati

- diagonal
- row
- hash
- column

Click to Save Answer & Move to Next Question

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In the following declaration of two dimensional integer array:
`int A[1024][1024];`
Integer array A has _____ number of rows.

Select the correct option

Reload Math Equations

- 1024x 1024
- 256
- 1024
- 512

Click to Save Answer & Move to Next Question

File Explorer | FormatFactory... | Quiz - Opera | Word | Bandicam | 32°C | ENG | Friday 7/21/2023

Question # 9 of 10 (Start time: 06:08:19 PM, 21 July 2023) Total Marks: 1

Page Replacement Algorithm LRU is called:

Select the correct option Reload Math Equations

- Low Resolution Used
- Low Reset Used
- Least Recently Used
- Low Reset UP

Question # 8 of 10 (Start time: 06:07:49 PM, 21 July 2023) Total Marks: 1

As long as there is no _____, the effective access time is equal to the memory access time.

Select the correct option Reload Math Equations

- page faults
- queue faults
- demand faults
- stack faults

Click to Save Answer & Move to Next Question

Question # 6 of 10 (Start time: 06:06:58 PM, 21 July 2023) Total Marks: 1

In _____ the pager brings only those necessary pages into memory that are needed.

Select the correct option

- Demand paging
- Self paging
- Push paging
- Hash paging

Click to Save Answer & Move to Next Question

Question # 5 of 10 (Start time: 06:06:31 PM, 21 July 2023) Total Marks: 1

In most computers memory access time is ranging from _____

Select the correct option

- 10 to 200 microseconds
- 200 to 300 microseconds
- 10 to 200 nanoseconds
- 300 to 600 microseconds

Click to Save Answer & Move to Next Question

Question # 4 of 10 (Start time: 06:05:56 PM, 21 July 2023) Total Marks: 1

Least Recently Used (LRU) page replacement algorithm basically works on the concept that the pages that are heavily used in _____ instructions are likely to be used heavily in next instructions.

Select the correct option

[Reload Math Equations](#)

- hash
- pop
- previous
- push

Question # 2 of 10 (Start time: 06:04:42 PM, 21 July 2023) Total Marks: 1

In _____ Page Replacement algorithm, If we use the recent past as an approximation of the near future, then we will replace the page that has not been used for the longest period of time.

Select the correct option

[Reload Math Equations](#)

- Low Reset UP (LRU)
- Low Resolution Used (LRU)
- Least Recently Used (LRU)
- Low Reset Used (LRU)

[Click to Save Answer & Move to Next Question](#)

Following are the advantages of Virtual memory EXCEPT

Select the correct option

- Running large sized processes
- Concept of memory mapped files
- Efficient process creation
- Low CPU consumption

Taskbar: File Explorer, Quiz - Ope..., Bandicam, CS604 Quiz 4 S..., *Untitled - Not..., *LMS Login.txt..., Internet Down..., 32°C, ENG, Tuesday 7/25/2023, 3:58 PM

Question # 9 of 10 (Start time: 03:57:18 PM, 25 July 2023) Total Marks: 1

Cache is non-volatile memory.

Select the correct option

- False
- True

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Question # 7 of 10 (Start time: 03:56:00 PM, 25 July 2023) Total Marks: 1

Intel 80386 used paged segmentation with _____ level paging.

Select the correct option

- Two
- Three
- Four

Question # 4 of 10 (Start time: 03:54:27 PM, 25 July 2023) Total Marks: 1

_____ is commonly implemented as demand paging.

Select the correct option

- Virtual Memory
- Physical Memory
- Logical Addressing
- Paging

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Question # 2 of 10 (Start time: 03:53:39 PM, 25 July 2023) Total Marks: 1

What do we name to an address that is loaded into the memory-address register of the memory?

Select the correct option

- Logical address
- Binary addresses
- None of the given options
- Physical address

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Question # 1 of 10 (Start time: 03:52:55 PM, 25 July 2023) Total Marks: 1

What do we name to an address that is generated by the CPU?

Select the correct option

- None of the given options
- Physical address
- Binary addresses
- Logical address

Question # 8 of 10 (Start time: 03:46:16 PM, 25 July 2023)

Total Marks: 1

In _____, the library files are linked at load time.

Select the correct option

Static Linking

Dynamic Linking

Question # 7 of 10 (Start time: 03:45:15 PM, 25 July 2023)

Total Marks: 1

How does a logical-address space is represented?

Select the correct option

Through Critical section

Through Segments

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Question # 6 of 10 (Start time: 03:44:40 PM, 25 July 2023) Total Marks: 1

The collection of processes that is waiting on the disk to be brought into the memory for execution forms the -----

Select the correct option

- None of the given options
- Input queue
- Both input and output queue
- Output queue

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Question # 5 of 10 (Start time: 03:42:30 PM, 25 July 2023) Total Marks: 1

In Swapping technique of Memory Management, the total amount transfer time is directly proportional to the -----

Select the correct option

- amount of memory swapped
- all the given options are correct
- amount of space on backing store
- space on main memory

Question # 3 of 10 (Start time: 03:40:49 PM, 25 July 2023)

Total Marks: 1

A program can not execute unless whole or necessary part of it resides in the main memory.

Select the correct option

<input checked="" type="radio"/>	True
<input type="radio"/>	False

When the address used in a program gets converted to an actual physical RAM address, it is called -----

Select the correct option

<input type="radio"/>	Loading
<input type="radio"/>	Execution
<input checked="" type="radio"/>	Address Binding
<input type="radio"/>	Compiling

Physical memory is broken down into fixed-sized blocks, called----- and Logical memory is divided into blocks of the same size, called -----

Select the correct option

- Holes, segments
- Pages, Frames
- Frames, holes
- Frames, pages

Windows taskbar showing search, File Explorer, Quiz - Ope..., Bandicam, and system tray with 32°C, ENG, Tuesday 7/25/2023, 3:24 PM.

Memory protection in paging is achieved by associating ----- with each page. This bit indicates whether the page is in the process address space or not.

Select the correct option

- counting bits
- paging table
- protection bits
- segment table

Windows taskbar showing search, File Explorer, Quiz - Ope..., Bandicam, and system tray with 32°C, ENG, Tuesday 7/25/2023, 3:23 PM.

Question # 6 of 10 (Start time: 03:21:33 PM, 25 July 2023)

Total Marks: 1

The size of pages and frames are same in logical memory and physical memory respectively.

Select the correct option

True

False

Question # 2 of 10 (Start time: 03:19:58 PM, 25 July 2023)

Total Marks: 1

In Overlay technique, we can overload any part of the program with the part of the program required needed recently.

Select the correct option

True

False

Question # 1 of 10 (Start time: 03:19:06 PM, 25 July 2023) Total Marks: 1

Address Binding will be _____ in Multiprogramming with Variable Tasks (MVT)

Select the correct option

- Static
- Dynamic
- Fixed
- Variable

Segmentation is a memory management scheme that supports _____

Select the correct option

- Hardware's view of memory
- None of the given
- Programmer's view of memory
- System's view of memory

Question # 9 of 10 (Start time: 02:56:26 PM, 25 July 2023) Total Marks: 1

Addresses generated relative to part of program, not to start of physical memory are

Select the correct option

- Physical
- Virtual
- Relocatable
- Symbolic

Question # 8 of 10 (Start time: 02:54:42 PM, 25 July 2023) Total Marks: 1

-----points to the page table

Select the correct option

- Translation look-aside buffers
- Page-table length register (PRLR)
- Page-table base register (PTBR)
- Page offset

_____ is caused due to un-used space in fixed size blocks/ pages.

Select the correct option

- MVT
- External fragmentation
- Paging
- Internal fragmentation

Question # 2 of 10 (Start time: 02:48:43 PM, 25 July 2023) Total Marks: 1

The address generated by the CPU, after any indexing or other addressing-mode arithmetic, is called a -----address, and the address it gets translated to by the MMU is called a -----address.

Select the correct option

- Virtual, physical
- Physical, Virtual
- Valid, invalid
- Hexadecimal, Binary

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Question # 1 of 10 (Start time: 02:48:02 PM, 25 July 2023) Total Marks: 1

The system maintains a _____ of all processes whose memory images are on the backing store or in memory and are ready to run.

Select the correct option

- Environment
- Ready Queue
- Queue
- List

File Explorer Quiz - Ope... Bandicam

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In memory management programmer's view of memory representation is called _____

Select the correct option

- Physical address
- Segmentation
- Logical address
- Offset

File Explorer Quiz - Ope... Bandicam

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Intel is basically designed for following Operating Systems except _____.

Select the correct option

- MULTICS
- Windows
- OS/2
- Linux

Each page is a power of _____ bytes long in paging scheme.

Select the correct option

- 5
- 3
- 4
- 2

The main memory is usually divided into partitions, one for _____ and other for _____.

Select the correct option

- Base Register, Limit Register
- Processes, Virtual Memory
- Operating System, User processes
- Operating system, CPU

-----holds the smallest legal physical memory address for a process

Select the correct option

- Limit register
- Base register
- Index register
- Stack pointers register

The size of a page is defined by _____.

Select the correct option

- Page Table
- Physical Memory
- Logical Memory
- CPU

In pages segmentation, the logical address is legal if d is _____ segment length.

Select the correct option

- Greater than
- Equal to
- Greater than or equal to
- Less than

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Question # 1 of 10 (Start time: 01:43:55 PM, 25 July 2023) Total Marks: 1

It is not possible to run a program whose size is greater than the size of the main memory.

Select the correct option

<input checked="" type="radio"/>	False
<input type="radio"/>	True

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CS604 - Operating Systems (Quiz No. 4) Total Questions : 10

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1. Quiz will be based upon Multiple Choice Questions (MCQs).
2. You have to attempt the quiz online. You can start attempting the quiz any time within given date(s) of a particular subject by clicking the link for Quiz in VULMS.
3. Each question has a fixed time of 90 seconds. So you have to save your answer before 90 seconds. But due to unstable internet speeds, it is recommended that you should save your answer within 60 seconds. While attempting a question, keep an eye on the remaining time.
4. Attempting quiz is unidirectional. Once you move forward to the next question, you can not go back to the previous one. Therefore before moving to the next question, make sure that you have selected the best option.
5. **DO NOT** press **Back Button / Backspace Button** while attempting a question, otherwise you will lose that question.
6. **DO NOT** refresh the page unnecessarily, **specially** when following messages appear
Saving...
Question Timeout: Now loading next question...
7. **Javascript MUST be enabled** in your browser; otherwise you will not be able to attempt the quiz.
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Taskbar: File Expl..., Start Qui..., Bandicam, 1:41 PM

Overlays are implemented by the -----

Select the correct option

- Operating system
- Kernel
- Shell
- Programmer

Taskbar and system tray area showing File Explorer, Quiz - Ope..., Bandicam, and system clock (2:31 PM, Monday, 7/24/2023).

----- is the separation of user logical memory from physical memory.

Question # 9 of 10 (Start time: 02:30:23 PM, 24 July 2023) Total Marks: 1

Select the correct option

- None of the given options
- Physical memory
- Virtual Memory
- ROM

Taskbar and system tray area showing File Explorer, Quiz - Ope..., Bandicam, and system clock (2:30 PM, Monday, 7/24/2023).

-----keep in memory only those instructions and data that are needed at any given time.

Select the correct option

- Overlays
- Fragmentation
- Paging
- Swapping

Following schemes allow efficient implementations of page tables EXCEPT

Select the correct option

- Hierarchical / Multilevel Paging
- Hashed Page Table
- Inverted Page Table
- Binary Page Table

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A page table needed for keeping track of pages of the page table is called -----

Select the correct option

- 2-level paging
- Page size
- Page table size
- Page directory

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Secondary Storage memory devices have ----- memory.

Select the correct option

- Temporary
- Volatile
- None of the options is correct
- Permanent and non volatile

File Explorer Quiz - Ope... Bandicam

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Question # 4 of 10 (Start time: 02:25:44 PM, 24 July 2023) Total Marks: 1

The logical address of Intel 80386 is _____

Select the correct option

- 48 bits
- 36 bits
- 128 bits
- 64 bits

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Question # 3 of 10 (Start time: 02:24:50 PM, 24 July 2023) Total Marks: 1

To eliminate external fragmentation in segmentation the scheme used is _____

Select the correct option

- Fixed size paging scheme
- Fixed size partition scheme
- Variable size paging scheme
- Variable size partition scheme

Logical address is defined by which tuple/tuples?

Select the correct option

- Segment number and offset
- Paging
- Physical address
- Indexed number and offset

Question # 1 of 10 (Start time: 02:22:27 PM, 24 July 2023) Total Marks: 1

ST in logical addressing stands for _____

Select the correct option

- String Table
- Smart Trap
- Segment Table
- None of the Given