



CS401 - Computer Architecture and Assembly Language Programming



CS401 - Computer Architecture and Assembly Language Programming

(Mid-Term) 2021 GRAND Quiz 100% Correct Solve (Alhamdulillah)

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https://www.youtube.com/channel/UCjboEX65C_yN7as5di1OuIQ/playlists Sadiq Ahmed

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03063548328 Sonu Ilyas Mughal



CS401:Grand Quiz

Question # 1 of 30 (Start time: 04:14:46 PM, 21 June 2021)

There are _____ forms of the DIV instruction.

Select the correct option

☐	One
☐	Four
☒	Two
☐	Three





CS401:Grand Quiz

Question # 2 of 30 (Start time: 04:16:09 PM, 21 June 2021)

Which of the following is the correct instruction for NOT operation?

Select the correct option

- | | |
|----------------------------------|--------------------|
| ☒ | not bx |
| ☐ | not byte [var], ax |
| ☐ | not ax, bx |
| ☐ | not word [var], ax |

Click





CS401:Grand Quiz

Question # 3 of 30 (Start time: 04:16:31 PM, 21 June 2021)

Which part of B80500 (encoded instruction) is an opcode?

Select the correct option

☐	B80500
☒	B8
☐	0500
☐	B80





CS401:Grand Quiz

Question # 4 of 30 (Start time: 04:17:00 PM, 21 June 2021)

In case of DIV BX instruction, the quotient is stored in _____ register.

Select the correct option

☐	BL
☐	DX
☐	AL
☒	AX





CS401:Grand Quiz

Question # 5 of 30 (Start time: 04:17:28 PM, 21 June 2021)

Which of the following is the interrupt number for debug interrupt?

Select the correct option

☐	INT 1
☐	INT 0
☒	INT 3
☐	INT 2

Click to Sa





CS401:Grand Quiz

Question # 5 of 30 (Start time: 04:17:28 PM, 21 June 2021)

Which of the following is the interrupt number for debug interrupt?

Select the correct option

☐	INT 1
☐	INT 0
☒	INT 3
☐	INT 2

Click





Question # 6 of 30 (Start time: 04:18:33 PM, 21 June 2021)

Code size reduction and improvement in speed were the two reasons for introducing block processing instructions in the _____ processor.

Select the correct option

- | | |
|----------------------------------|---------|
| ☒ | 8088 |
| ☐ | iAPX386 |
| ☐ | 8085 |
| ☐ | 8080 |

Click to Save Answer &





CS401:Grand Quiz

Question # 7 of 30 (Start time: 04:19:01 PM, 21 June 2021)

When SI or DI are used, we name the method _____ addressing.

Select the correct option

☒	indexed
☐	stack
☐	based
☐	segment

Click t





CS401:Grand Quiz

Question # 8 of 30 (Start time: 04:19:30 PM, 21 June 2021)

REPE or REPNE are used with the _____ instruction.

Select the correct option

☒	SCAS
☐	ADD
☐	MOVS
☐	CALL





CS401:Grand Quiz

Question # 9 of 30 (Start time: 04:19:55 PM, 21 June 2021)

_____ cannot appear as an operand in any instruction. It is used for accessing instructions.

Select the correct option

☒	IP
☐	SS
☐	AH
☐	BH





CS401:Grand Quiz

Question # 10 of 30 (Start time: 04:20:24 PM, 21 June 2021)

In ADC instruction, there are _____.

Select the correct option

- | | |
|----------------------------------|---------------------------------------|
| ☐ | two registers only |
| ☐ | two operands and the ZF |
| ☐ | two registers and one memory location |
| ☒ | two operands and the CF |





CS401:Grand Quiz

Question # 11 of 30 (Start time: 04:20:59 PM, 21 June 2021)

What makes db different from dw?

Select the correct option

- | | |
|----------------------------------|---|
| ☒ | dw occupies 16 bits and db occupies 8 bits |
| ☐ | db occupies 1 bit while dw occupies 2 bits |
| ☐ | db occupies 4 bits while dw occupies 8 bits |
| ☐ | dw occupies 8 bits and db occupies 16 bits |





CS401:Grand Quiz

Question # 12 of 30 (Start time: 04:21:23 PM, 21 June 2021)

Whenever an instruction needs a memory source, _____ holds the pointer to it.

Select the correct option

☒	DS:SI
☐	DS:SP
☐	ES:DI
☐	ES:DX





CS401:Grand Quiz

Question # 13 of 30 (Start time: 04:21:56 PM, 21 June 2021)

Which of the following is an incorrect XOR operation?

Select the correct option

- | | |
|----------------------------------|-------------------|
| ☐ | XOR BX, AX |
| ☐ | XOR BYTE [VAR], 5 |
| ☐ | XOR AX, BX |
| ☒ | XOR AX |

CS401:Grand Quiz

Quiz Start Time: 04:14 PM, 21 Jun

Question # 14 of 30 (Start time: 04:22:33 PM, 21 June 2021)

Total M

In _____ operation, the carry flag is inserted from the right causing every bit to move one location to its left. The dropped bit from the left goes into the carry flag.

Select the correct option

- | | |
|----------------------------------|----------------------------------|
| ☒ | Rotate through Carry Left (RCL) |
| ☐ | Rotate Left (ROL) |
| ☐ | Rotate through Carry Right (RCR) |
| ☐ | Rotate Right (ROR) |

Click to Save Answer & Move to Next Ques





CS401:Grand Quiz

Question # 15 of 30 (Start time: 04:22:56 PM, 21 June 2021)

The interrupt call loads new values in CS, IP and _____.

Select the correct option

☒	FLAGS
☐	DS
☐	BP
☐	SS





CS401:Grand Quiz

Question # 16 of 30 (Start time: 04:23:18 PM, 21 June 2021)

Which bit of attribute byte represents the blue component of foreground color?

Select the correct option

☒	0
☐	3
☐	1
☐	2

Click to Save





CS401:Grand Quiz

Question # 17 of 30 (Start time: 04:23:46 PM, 21 June 2021)

Which of the following is the extension of object file?

Select the correct option

☒	.com
☐	.lst
☐	.asm
☐	.exe





CS401:Grand Quiz

Question # 18 of 30 (Start time: 04:24:19 PM, 21 June 2021)

In case of short jump, the offset is stored in _____ .

Select the correct option

- | | |
|----------------------------------|----------|
| ☐ | 2 bytes |
| ☐ | 16 bytes |
| ☐ | 4 bytes |
| ☒ | 1 byte |





CS401:Grand Quiz

Question # 19 of 30 (Start time: 04:24:48 PM, 21 June 2021)

Suppose, the current value of IP is 0x0129, and the relative address is 0x0012. What will be the new value of IP?

Select the correct option

- | | |
|----------------------------------|--------|
| ☐ | 0x0135 |
| ☒ | 0x013B |
| ☐ | 0x0129 |
| ☐ | 0x0012 |

Click to Save





CS401:Grand Quiz

Question # 20 of 30 (Start time: 04:25:18 PM, 21 June 2021)

Which of the following is shifted right in the 4-bit multiplication algorithm?

Select the correct option

☐	Remainder
☐	Multiplicand
☒	Multiplier
☐	Modulus

CS401:Grand Quiz

Quiz Start Time: 04:14 P

Question # 21 of 30 (Start time: 04:25:43 PM, 21 June 2021)

In _____, a zero is inserted from the right, and every bit moves one position to its left. The most significant bit also drops into the carry flag.

Select the correct option

[Reload M](#)

☐	Rotate Right (ROR)
☐	Shift logical Right (SHR)
☒	Shift Arithmetic Left (SAL)
☐	Shift Arithmetic Right (SAR)

[Click to Save Answer & Move to N](#)





Question # 22 of 30 (Start time: 04:26:14 PM, 21 June 2021)

Total

Shift Arithmetic Right (SAR) shifts every bit one place to the right and places a copy of the _____ significant bit at the _____ significant place.

Select the correct option

[Reload Math Editor](#)

- | | |
|----------------------------------|--------------|
| ☐ | most, least |
| ☐ | least, most |
| ☒ | most, most |
| ☐ | least, least |

[Click to Save Answer & Move to Next Question](#)





CS401:Grand Quiz

Question # 23 of 30 (Start time: 04:26:45 PM, 21 June 2021)

How many characters are defined by the standard ASCII?

Select the correct option

☐	32
☒	128
☐	64
☐	132





CS401:Grand Quiz

Question # 24 of 30 (Start time: 04:27:16 PM, 21 June 2021)

There is an auto-increment mode when the _____ is _____.

Select the correct option

☐	ZF, not set
☐	DF, set
☒	DF, not set
☐	CX, not set

Click





CS401:Grand Quiz

Question # 25 of 30 (Start time: 04:27:46 PM, 21 June 2021)

When the operand of DIV instruction is in 16 bits, then the implied dividend will be in _____.

Select the correct option

☐	DS:BX
☒	DX:AX
☐	AX
☐	ES:AX





CS401:Grand Quiz

Question # 26 of 30 (Start time: 04:28:12 PM, 21 June 2021)

MOV AX, 0X4C00

INT 0X21

Which of the following is the purpose of the given instructions?

Select the correct option

☒	Terminate a program
☐	Move ax into 0X4C00
☐	Variable definition
☐	Move number into ax





Question # 27 of 30 (Start time: 04:28:42 PM, 21 June 2021)

REPZ repeats the following instruction while the _____.

Select the correct option

- | | |
|----------------------------------|---------------------------|
| ☐ | Direction Flag is not set |
| ☒ | ZF is not set |
| ☐ | Direction Flag is set |
| ☐ | ZF is set |

Click to Save A





CS401:Grand Quiz

Question # 28 of 30 (Start time: 04:29:28 PM, 21 June 2021)

What would be the output of MOV AX, BL?

Select the correct option

- | | |
|----------------------------------|---|
| ☒ | Assembler will declare it as an illegal expression |
| ☐ | The contents of the BL register will be moved to the AX register |
| ☐ | The contents of the AX register will be moved to the BL register |
| ☐ | The contents of the BL register will be moved to the AX register, and the BL will be set as 0 |





CS401:Grand Quiz

Question # 29 of 30 (Start time: 04:29:48 PM, 21 June 2021)

REPE repeats a string instruction while the _____ flag is set.

Select the correct option

☐	parity
☒	zero
☐	carry
☐	direction





CS401:Grand Quiz

Question # 30 of 30 (Start time: 04:30:21 PM, 21 June 2021)

What the following piece of code does?

Shl word [multiplicand], 1

Rcl word [multiplicand+2], 1

Select the correct option

- | | |
|----------------------------------|--|
| ☐ | Rotate multiplicand and multiplicand+2 |
| ☒ | Extended Shift left |
| ☐ | Multiply multiplicand and multiplicand+2 |
| ☐ | Extended Shift right |





Question # 1 of 30 (Start time: 04:37:20 PM, 21 June 2021)

Shift logical Right (SHR) moves all the bits one position to the _____, and inserts a zero from the _____.

Select the correct option

- | | |
|----------------------------------|--------------|
| ☐ | left, right |
| ☐ | left, left |
| ☒ | right, left |
| ☐ | right, right |

Click to Save Answer





CS401:Grand Quiz

Question # 2 of 30 (Start time: 04:37:50 PM, 21 June 2021)

Which of the following is the most illegal instruction?

Select the correct option

☐	Mov al, [num1]
☐	Mov ax, [num1]
☐	Mov ax, al
☒	Mov al, ax





CS401:Grand Quiz

Question # 3 of 30 (Start time: 04:38:43 PM, 21 June 2021)

The intent of the processor is carried to the memory for read/write operations through the _____.

Select the correct option

☐	address bus
☐	both data bus and address bus
☐	data bus
☒	control bus





CS401:Grand Quiz

Question # 4 of 30 (Start time: 04:39:19 PM, 21 June 2021)

The symbols in extended ASCII table by IBM fits in one _____.

Select the correct option

☒	byte
☐	word
☐	bit
☐	doubleword

Click





CS401:Grand Quiz

Question # 5 of 30 (Start time: 04:39:42 PM, 21 June 2021)

Which of the following instructions allows memory to memory movement of data?

Select the correct option

☐	MOV
☒	MOVS
☐	SCAS
☐	LODS





CS401:Grand Quiz

Q1

Question # 6 of 30 (Start time: 04:40:24 PM, 21 June 2021)

In 8051, there is an _____ stack.

Select the correct option

- | | |
|----------------------------------|----------------|
| ☐ | semi-automatic |
| ☐ | decrementing |
| ☐ | stagnant |
| ☒ | incrementing |

Click to Save





CS401:Grand Quiz

Question # 7 of 30 (Start time: 04:40:52 PM, 21 June 2021)

What is the content of stack pointer?

Select the correct option

- | | |
|----------------------------------|---|
| ☐ | Address of the next instruction |
| ☐ | Address of the lower element of the stack |
| ☒ | Address of the top element of the stack |
| ☐ | Address of the current instruction |





CS401:Grand Quiz

Q1

Question # 8 of 30 (Start time: 04:41:29 PM, 21 June 2021)

The ordering of a program's instructions is ensured by the _____ register.

Select the correct option

☐	Stack Pointer
☐	Flags
☐	Index
☒	Program Counter

Click to Save





CS401:Grand Quiz

Question # 9 of 30 (Start time: 04:42:35 PM, 21 June 2021)

Which of the following is the interrupt number for Non-Maskable Interrupt (NMI)?

Select the correct option

☒	INT 2
☐	INT 0
☐	INT 3
☐	INT 1





CS401:Grand Quiz

Question # 10 of 30 (Start time: 04:42:59 PM, 21 June 2021)

A/An _____ is an area of memory that holds all local variables and parameters.

Select the correct option

- | | |
|----------------------------------|---------------------|
| ☐ | base register |
| ☐ | data segment |
| ☐ | instruction pointer |
| ☒ | stack |

cl





CS401:Grand Quiz

Question # 11 of 30 (Start time: 04:43:27 PM, 21 June 2021)

ASCII table is the contiguous arrangement of the uppercase alphabets (41-5A), the lowercase alphabets (61-7A), and the numbers _____

Select the correct option

☐	31-41
☒	30-39
☐	31-40
☐	29-39





CS401:Grand Quiz

Quiz Start Time: 04:3

Question # 12 of 30 (Start time: 04:43:58 PM, 21 June 2021)

In _____, a zero is inserted from the right and every bit moves one position to its left with the most significant bit dropped into the carry flag.

Select the correct option

- | | |
|----------------------------------|------------------|
| ☐ | SHL |
| ☐ | SAL |
| ☒ | both SHL and SAL |
| ☐ | SAR |

CS401:Grand Quiz

Quiz Start Time: 04:

Question # 13 of 30 (Start time: 04:44:28 PM, 21 June 2021)

In extended multiplication, we store the multiplicand in _____ bits, and the result is stored in _____ bits.

Select the correct option

Relo

- | | |
|----------------------------------|--------|
| ☐ | 64, 64 |
| ☒ | 32, 32 |
| ☐ | 16, 32 |
| ☐ | 32, 64 |

Click to Save Answer & Move





CS401:Grand Quiz

Question # 14 of 30 (Start time: 04:45:01 PM, 21 June 2021)

MOVS is used to _____ a block of memory.

Select the correct option

☐	Delete
☐	Push
☒	Move
☐	Save





CS401:Grand Quiz

Question # 15 of 30 (Start time: 04:45:29 PM, 21 June 2021)

DW is used to reserve _____ bit value in memory.

Select the correct option

☒	16
☐	8
☐	24
☐	32





Question # 16 of 30 (Start time: 04:45:57 PM, 21 June 2021)

Scrolling is the process of moving one or more lines towards the top or bottom of the screen, and the new line that appears on the top or bottom is _____.

Select the correct option



- | | |
|----------------------------------|------------|
| ☐ | multiplied |
| ☐ | added |
| ☒ | cleared |
| ☐ | joined |

Click to Save Answer & Move





CS401:Grand Quiz

Quiz Start

Question # 17 of 30 (Start time: 04:46:40 PM, 21 June 2021)

The execution of the instruction "mov word [ES : 160], 0x1230" will print a character on the screen at the _____ column of the _____ row.

Select the correct option

- | | |
|----------------------------------|----------------|
| ☐ | first, third |
| ☐ | second, second |
| ☒ | first, second |
| ☐ | second, first |

Click to Save Answer





Question # 18 of 30 (Start time: 04:47:10 PM, 21 June 2021)

As a result of base+offset addressing, the address that is generated is called a (an) _____ address.

Select the correct option

- | | |
|----------------------------------|-----------|
| ☐ | elective |
| ☐ | additive |
| ☒ | effective |
| ☐ | net |

Click to Save Ans





CS401:Grand Quiz

Question # 19 of 30 (Start time: 04:47:42 PM, 21 June 2021)

In multiplication algorithm, we take the first digit of the multiplier and multiply it with the _____.

Select the correct option

☐	Reminder
☒	Multiplicand
☐	Divider
☐	Result





CS401:Grand Quiz

Question # 20 of 30 (Start time: 04:48:12 PM, 21 June 2021)

There are _____ forms of MOVS instruction.

Select the correct option

☐	three
☐	five
☐	four
☒	two

Click





CS401:Grand Quiz

Question # 21 of 30 (Start time: 04:48:45 PM, 21 June 2021)

What is the purpose of MOV AX, 0xFFFF?

Select the correct option

- | | |
|----------------------------------|----------------------|
| ☐ | Add 0xFFFF into AX |
| ☐ | Copy 0xFFFE into AX |
| ☐ | Move AX into 0xFFFF |
| ☒ | Store 0xFFFF into AX |





CS401:Grand Quiz

Question # 22 of 30 (Start time: 04:49:16 PM, 21 June 2021)

Program consists of _____ logical parts.

Select the correct option

☒	Three
☐	One
☐	Two
☐	Four





CS401:Grand Quiz

Question # 23 of 30 (**Start time: 04:49:39 PM, 21 June 2021**)

There are _____ byte(s) for each character on the screen.

Select the correct option

- | | |
|----------------------------------|---|
| ☒ | 2 |
| ☐ | 1 |
| ☐ | 4 |
| ☐ | 6 |

Click to Save





CS401:Grand Quiz

Question # 24 of 30 (Start time: 04:50:09 PM, 21 June 2021)

In which of the following addressing, the memory address is fixed and is given in the instruction?

Select the correct option

☐	Indirect
☐	Indexed
☐	Base plus offset
☒	Direct

Click to S





CS401:Grand Quiz

Question # 25 of 30 (**Start time: 04:50:37 PM, 21 June 2021**)

Which of the following keywords is used to define two bytes in memory?

Select the correct option

☐	DN
☐	DD
☒	DW
☐	DB





CS401:Grand Quiz

Question # 26 of 30 (Start time: 04:51:07 PM, 21 June 2021)

Registers are storage cells _____.

Select the correct option

- | | |
|----------------------------------|---------------------------------------|
| ☒ | inside the processor |
| ☐ | both inside and outside the processor |
| ☐ | inside the memory |
| ☐ | outside the processor |

Click





CS401:Grand Quiz

Question # 27 of 30 (Start time: 04:51:51 PM, 21 June 2021)

Call Instruction takes a _____ as an argument.

Select the correct option

☐	destination register
☒	label
☐	memory address
☐	source register





CS401:Grand Quiz

Question # 28 of 30 (Start time: 04:52:24 PM, 21 June 2021)

The correlation process from the interrupt number to the interrupt handler uses a table called _____.

Select the correct option

☒	Interrupt Vector Table
☐	Interrupt Correlation Table
☐	Interrupt Calling Table
☐	Interrupt Handling Table





CS401:Grand Quiz

Question # 29 of 30 (Start time: 04:52:47 PM, 21 June 2021)

The extra bit produced as a result of an arithmetic operation that does not fit in the target register is stored in:

Select the correct option

☐	Zero Flag
☐	Interrupt Flag
☐	Overflow Flag
☒	Carry Flag





CS401:Grand Quiz

Question # 30 of 30 (Start time: 04:53:15 PM, 21 June 2021)

_____ can also be used as a masking operation to invert selective bits.

Select the correct option

☐	AND
☐	NOT
☒	XOR
☐	OR





CS401:Grand Quiz

Question # 1 of 30 (Start time: 09:40:34 AM, 21 June 2021)

Whenever we need access to a memory location whose address is not known until run-time we use _____.

Select the correct option

☐	Destination Register
☐	Counter Register
☒	Index Register
☐	Accumulator Register





CS401:Grand Quiz

Question # 2 of 30 (Start time: 09:41:22 AM, 21 June 2021)

In which of the following addressing, the memory address is fixed and is given in the instruction?

Select the correct option

☐	Base plus offset
☐	Indexed
☐	Indirect
☒	Direct





Question # 3 of 30 (Start time: 09:42:30 AM, 21 June 2021)

In the instruction "mov word [es:0], 0x1230", 12 refers to _____ color on _____ background.

Select the correct option

- | | |
|----------------------------------|--------------|
| ☐ | black, green |
| ☐ | blue, green |
| ☐ | green, black |
| ☒ | green, blue |

Click to Save A





CS401:Grand Quiz

Question # 4 of 30 (Start time: 09:43:25 AM, 21 June 2021)

As a result of base+offset addressing, the address that is generated is called a (an) _____ address.

Select the correct option

☐	elective
☐	additive
☐	net
☒	effective





CS401:Grand Quiz

Question # 5 of 30 (Start time: 09:44:01 AM, 21 June 2021)

Which of the following is the interrupt number for Non-Maskable Interrupt (NMI)?

Select the correct option

☐	INT 1
☐	INT 0
☒	INT 2
☐	INT 3





CS401:Grand Quiz

Question # 6 of 30 (Start time: 09:44:35 AM, 21 June 2021)

What does the given instruction do?

SHR DL, 1

Select the correct option

- | | |
|----------------------------------|---|
| ☐ | Store the 1 bit in DL |
| ☐ | Shift left and store the most significant bit in the carry flag |
| ☒ | Move the right most bit into the carry flag |
| ☐ | Move the left most bit into the carry flag |





CS401:Grand Quiz

Question # 7 of 30 (Start time: 09:45:18 AM, 21 June 2021)

When an element is pushed on the stack, SP is decremented by _____.

Select the correct option

☒	2
☐	4
☐	1
☐	3





CS401:Grand Quiz

Question # 8 of 30 (Start time: 09:45:50 AM, 21 June 2021)

_____ subtracts one from the operand.

Select the correct option

☐	Shift
☐	MEC
☒	DEC
☐	Modulus





CS401:Grand Quiz

Question # 9 of 30 (Start time: 09:46:34 AM, 21 June 2021)

SP is associated (by default) with _____.

Select the correct option

☐	CS
☐	DS
☒	SS
☐	ES





CS401:Grand Quiz

Question # 10 of 30 (Start time: 09:47:15 AM, 21 June 2021)

The operand of _____ is called the source operand because the data moves to the stack from the operand.

Select the correct option

☐	RET
☒	PUSH
☐	CALL
☐	POP





CS401:Grand Quiz

Question # 11 of 30 (Start time: 09:47:49 AM, 21 June 2021)

In extended multiplication, we store the multiplicand in _____ bits, and the result is stored in _____ bits.

Select the correct option

- | | |
|----------------------------------|--------|
| ☒ | 32, 32 |
| ☐ | 64, 64 |
| ☐ | 16, 32 |
| ☐ | 32, 64 |

Click to





CS401:Grand Quiz

Question # 12 of 30 (Start time: 09:48:12 AM, 21 June 2021)

VGA stands for _____.

Select the correct option

- | | |
|----------------------------------|----------------------------|
| ☐ | Visual Graphics Adapter |
| ☒ | Video Graphics Adapter |
| ☐ | Video Graphics Application |
| ☐ | Video Graphic Accumulator |





CS401:Grand Quiz

Question # 13 of 30 (Start time: 09:48:52 AM, 21 June 2021)

What is the purpose of the following two instructions?

1. MOV AX, 0XB800
2. MOV ES, AX

Select the correct option

☒	Points ES to video base
☐	Points ES to numeric base
☐	Points ES to audio base
☐	Points ES to graphics base





CS401:Grand Quiz

Question # 14 of 30 (Start time: 09:49:27 AM, 21 June 2021)

Which of the following bus is used to inform the memory that whether processor wants to read data or write data?

Select the correct option

☐	Function Bus
☐	Address Bus
☒	Control Bus
☐	Data Bus

cli





CS401:Grand Quiz

Question # 15 of 30 (Start time: 09:49:58 AM, 21 June 2021)

What the following piece of code does?

Shl word [multiplicand], 1

Rcl word [multiplicand+2], 1

Select the correct option

- | | |
|----------------------------------|--|
| ☐ | Multiply multiplicand and multiplicand+2 |
| ☐ | Rotate multiplicand and multiplicand+2 |
| ☐ | Extended Shift right |
| ☒ | Extended Shift left |





CS401:Grand Quiz

Question # 16 of 30 (Start time: 09:50:31 AM, 21 June 2021)

mov [1234], ax is an example of _____ addressing.

Select the correct option

☐	base + index
☐	register indirect
☐	base register indirect
☒	direct





CS401:Grand Quiz

Question # 17 of 30 (Start time: 09:51:07 AM, 21 June 2021)

In shift left operation, the left most bit will _____.

Select the correct option

- | | |
|----------------------------------|-------------------------------------|
| ☐ | always be 1 |
| ☒ | go into the Carry Flag |
| ☐ | be moved to the right most position |
| ☐ | be discarded |





CS401:Grand Quiz

Question # 18 of 30 (Start time: 09:51:42 AM, 21 June 2021)

iAPX88 Architecture consists of

Select the correct option

☐	10 Registers
☐	16 Registers
☒	14 Registers
☐	32 Registers





CS401:Grand Quiz

Question # 19 of 30 (Start time: 09:52:16 AM, 21 June 2021)

Which of the following are the two variants of STOS instruction?

Select the correct option

☐	STOS1 and STOS2
☒	STOSB and STOSW
☐	STOSA and STOSB
☐	STOS and STOSSES





CS401:Grand Quiz

Question # 20 of 30 (Start time: 09:52:51 AM, 21 June 2021)

The multiplier is stored in _____ bits in Extended Multiplication.

Select the correct option

☐	64
☐	8
☒	16
☐	32





CS401:Grand Quiz

Question # 21 of 30 (Start time: 09:53:43 AM, 21 June 2021)

There are _____ forms of MOVS instruction.

Select the correct option

☐	five
☒	two
☐	three
☐	four





CS401:Grand Quiz

Question # 22 of 30 (Start time: 09:54:13 AM, 21 June 2021)

Total number of reserved interrupts in Intel 8088 are _____.

Select the correct option

☐	3
☒	5
☐	8
☐	255





CS401:Grand Quiz

Question # 23 of 30 (Start time: 09:54:44 AM, 21 June 2021)

In case of DIV BX instruction, the quotient is stored in _____ register.

Select the correct option

☐	BL
☒	AX
☐	DX
☐	AL





CS401:Grand Quiz

Question # 24 of 30 (Start time: 09:55:20 AM, 21 June 2021)

Which of the following is the most illegal instruction?

Select the correct option

☐	Mov ax, [num1]
☐	Mov ax, al
☐	Mov al, [num1]
☒	Mov al, ax





CS401:Grand Quiz

Question # 25 of 30 (Start time: 09:55:50 AM, 21 June 2021)

The jump is taken if the last arithmetic operation has changed the sign unexpectedly.

Select the correct option

☐	JNO
☐	JNZ
☒	JO
☐	JZ





CS401:Grand Quiz

Question # 26 of 30 (Start time: 09:56:20 AM, 21 June 2021)

When the RET instruction is executed, it recovers the value of the _____ from the stack.

Select the correct option

☐	current instruction
☐	accumulator register
☒	instruction pointer
☐	previous instruction

Click t





CS401:Grand Quiz

Question # 27 of 30 (Start time: 09:56:58 AM, 21 June 2021)

The IDTR is a _____ bit register similar in structure to the GDTR.

Select the correct option

☐

64

☒

48

☐

32

☐

128





CS401:Grand Quiz

Question # 28 of 30 (Start time: 09:57:35 AM, 21 June 2021)

Shift logical Right (SHR) moves all the bits one position to the _____, and inserts a zero from the _____.

Select the correct option

- | | |
|----------------------------------|--------------|
| ☐ | left, left |
| ☐ | right, right |
| ☒ | right, left |
| ☐ | left, right |

Click to Save





CS401:Grand Quiz

Question # 29 of 30 (Start time: 09:58:22 AM, 21 June 2021)

The mechanism used to drop the carry for making the computed address valid is known as _____.

Select the correct option

- | | |
|----------------------------------|---------------------|
| ☒ | address wraparound |
| ☐ | overflow |
| ☐ | segment overlapping |
| ☐ | carry overload |





CS401:Grand Quiz

Question # 30 of 30 (Start time: 09:58:57 AM, 21 June 2021)

Which bit of attribute byte represents the green component of foreground color?

Select the correct option

☒	1
☐	0
☐	2
☐	3





CS401:Grand Quiz

Question # 1 of 30 (Start time: 10:09:21 AM, 21 June 2021)

Which of the following directive is used to reserve an eight bits space in memory?

Select the correct option

☐	dd
☒	db
☐	dw
☐	dq





CS401:Grand Quiz

Question # 2 of 30 (Start time: 10:10:10 AM, 21 June 2021)

Total number of reserved interrupts in Intel 8088 are _____.

Select the correct option

☐	8
☐	3
☐	255
☒	5

CS401:Grand Quiz

Quiz Start Time: 10:09

Question # 3 of 30 (Start time: 10:10:46 AM, 21 June 2021)

Shift Arithmetic Right (SAR) shifts every bit one place to the right and places a copy of the _____ significant bit at the _____ significant place.

Select the correct option

Reload

☐	least, most
☒	most, most
☐	most, least
☐	least, least





CS401:Grand Quiz

Question # 4 of 30 (Start time: 10:11:22 AM, 21 June 2021)

MOVS is used to _____ a block of memory.

Select the correct option

☐	Save
☐	Delete
☒	Move
☐	Push





CS401:Grand Quiz

Question # 5 of 30 (Start time: 10:11:56 AM, 21 June 2021)

_____ can also be used as a masking operation to invert selective bits.

Select the correct option

☐	AND
☒	XOR
☐	NOT
☐	OR





CS401:Grand Quiz

Question # 6 of 30 (Start time: 10:12:37 AM, 21 June 2021)

Which of following registers is used as a counter?

Select the correct option

☐	BX
☒	CX
☐	DX
☐	AX





CS401:Grand Quiz

Question # 7 of 30 (Start time: 10:13:07 AM, 21 June 2021)

SP is associated (by default) with _____.

Select the correct option

☐	CS
☒	SS
☐	DS
☐	ES





CS401:Grand Quiz

Question # 8 of 30 (Start time: 10:13:34 AM, 21 June 2021)

Push and pop operations always operate on_____.

Select the correct option

☐	Float
☐	Integer
☐	Byte
☒	Words





CS401:Grand Quiz

Question # 9 of 30 (Start time: 10:14:13 AM, 21 June 2021)

The convention to return a value from a subroutine is to use the _____ register.

Select the correct option

☐	BX
☐	DX
☒	AX
☐	CX

Click





CS401:Grand Quiz

Question # 10 of 30 (Start time: 10:14:49 AM, 21 June 2021)

MOV AX, 0X4C00

INT 0X21

Which of the following is the purpose of the given instructions?

Select the correct option

☐	Move ax into 0X4C00
☐	Variable definition
☐	Move number into ax
☒	Terminate a program





CS401:Grand Quiz

Question # 11 of 30 (Start time: 10:15:43 AM, 21 June 2021)

Which of the following cannot be pushed on the stack?

Select the correct option

☐	2 bytes
☐	4 bytes
☒	1 byte
☐	6 bytes





CS401:Grand Quiz

Question # 12 of 30 (Start time: 10:16:22 AM, 21 June 2021)

The stack of iAPX88 works on _____ sized elements.

Select the correct option

☐	nibble
☐	paragraph
☐	byte
☒	word





CS401:Grand Quiz

Question # 13 of 30 (Start time: 10:16:52 AM, 21 June 2021)

As a result of base+offset addressing, the address that is generated is called a (an) _____ address.

Select the correct option

☒	effective
☐	elective
☐	net
☐	additive

Click to S





CS401:Grand Quiz

Question # 14 of 30 (Start time: 10:17:24 AM, 21 June 2021)

In direct addressing, the memory address given in the instruction is _____.

Select the correct option

☐	variable
☐	empty
☒	fixed
☐	register





CS401:Grand Quiz

Question # 15 of 30 (Start time: 10:18:00 AM, 21 June 2021)

The operand of _____ is called the source operand because the data moves to the stack from the operand.

Select the correct option

☐	POP
☐	RET
☐	CALL
☒	PUSH





CS401:Grand Quiz

Question # 16 of 30 (Start time: 10:18:35 AM, 21 June 2021)

Registers are storage cells _____.

Select the correct option

- | | |
|----------------------------------|---------------------------------------|
| ☒ | inside the processor |
| ☐ | inside the memory |
| ☐ | outside the processor |
| ☐ | both inside and outside the processor |





CS401:Grand Quiz

Question # 17 of 30 (Start time: 10:19:00 AM, 21 June 2021)

When the control is transferred to a subroutine?

Select the correct option

- | | |
|----------------------------------|---|
| ☐ | When the return address is recovered |
| ☒ | After the arguments are pushed on the stack |
| ☐ | After the arguments are popped out from the stack |
| ☐ | When the instruction pointer is not available |





CS401:Grand Quiz

Question # 18 of 30 (Start time: 10:19:41 AM, 21 June 2021)

The interrupt call loads new values in CS, IP and _____.

Select the correct option

☐	SS
☐	DS
☐	BP
☒	FLAGS





CS401:Grand Quiz

Question # 19 of 30 (Start time: 10:20:14 AM, 21 June 2021)

How logical errors are different from syntax errors?

Select the correct option

- | | |
|----------------------------------|--|
| ☒ | Identifying syntax and logical errors is the responsibility of assembler and programmer respectively |
| ☐ | Identifying both types of errors is the responsibility of assembler |
| ☐ | Identifying syntax errors is the responsibility of programmer unlike logical errors |
| ☐ | Syntax errors are more important than the logical errors |

Click to





CS401:Grand Quiz

Question # 20 of 30 (Start time: 10:20:39 AM, 21 June 2021)

Which of the following is not a valid instruction in assembly language?

Select the correct option

☐	MOV AX, BX
☐	MOV AX, 55
☐	MOV BX, AX
☒	MOV CS, 0xb800





CS401:Grand Quiz

Quiz Start Time: 10:21:11 AM, 21 June 2021

Question # 21 of 30 (Start time: 10:21:11 AM, 21 June 2021)

In _____, a zero is inserted from the right, and every bit moves one position to its left. The most significant bit also drops into the carry flag.

Select the correct option

Rel

- | | |
|----------------------------------|------------------------------|
| ☒ | Shift Arithmetic Left (SAL) |
| ☐ | Shift Arithmetic Right (SAR) |
| ☐ | Shift logical Right (SHR) |
| ☐ | Rotate Right (ROR) |

Click to Save Answer & Move

CS401:Grand Quiz

Quiz Start Time: 10:21:11 AM, 21 June 2021

Question # 21 of 30 (Start time: 10:21:11 AM, 21 June 2021)

In _____, a zero is inserted from the right, and every bit moves one position to its left. The most significant bit also drops into the carry flag.

Select the correct option

Rel

- | | |
|----------------------------------|------------------------------|
| ☒ | Shift Arithmetic Left (SAL) |
| ☐ | Shift Arithmetic Right (SAR) |
| ☐ | Shift logical Right (SHR) |
| ☐ | Rotate Right (ROR) |

Click to Save Answer & Move





CS401:Grand Quiz

Question # 22 of 30 (Start time: 10:21:50 AM, 21 June 2021)

Which of the following instructions is used for non-destructive AND operation?

Select the correct option

☐	CMP
☐	NAND
☐	ADC
☒	TEST





CS401:Grand Quiz

Question # 23 of 30 (Start time: 10:22:41 AM, 21 June 2021)

The most convenient place to store local variables is _____.

Select the correct option

- | | |
|----------------------------------|--------------|
| ☐ | cache memory |
| ☒ | stack |
| ☐ | main memory |
| ☐ | registers |





CS401:Grand Quiz

Question # 24 of 30 (Start time: 10:23:11 AM, 21 June 2021)

Which of the following is the renamed version of conditional jump JZ?

Select the correct option

☐	JB
☒	JE
☐	JA
☐	JNC





CS401:Grand Quiz

Question # 25 of 30 (Start time: 10:23:45 AM, 21 June 2021)

Which of the following formulae calculates the desired location on the screen?

Select the correct option

- | | |
|----------------------------------|--|
| ☒ | $\text{location} = (\text{rowno} * 80 + \text{columnno}) * 2,$ |
| ☐ | $\text{location} = (\text{rowno} * 80 + \text{columnno}) * 6,$ |
| ☐ | $\text{location} = (\text{rowno} * 80 + \text{columnno}) * 8$ |
| ☐ | $\text{location} = (\text{colmunno} * 80 + \text{rowno}) * 2,$ |





Question # 26 of 30 (Start time: 10:24:13 AM, 21 June 2021)

Total

In _____ operation, the Most Significant Bit (MSB) is inserted from the right, causing every bit to move one position to the left. The MSB is also copied into the carry flag.

Select the correct option

- | | |
|----------------------------------|----------------------------------|
| ☐ | Rotate Right (ROR) |
| ☐ | Rotate through Carry Left (RCL) |
| ☒ | Rotate Left (ROL) |
| ☐ | Rotate through Carry Right (RCR) |

Click to Save Answer & Move to Next Question





CS401:Grand Quiz

Question # 27 of 30 (Start time: 10:24:53 AM, 21 June 2021)

After the execution of Shift Arithmetic Right instruction, the most significant bit _____.

Select the correct option

- | | |
|----------------------------------|--|
| ☒ | retains its original value |
| ☐ | is replaced by a 1 |
| ☐ | is replaced by the value of carry flag |
| ☐ | is replaced by a 0 |





CS401:Grand Quiz

Question # 28 of 30 (Start time: 10:25:29 AM, 21 June 2021)

_____ is a special prefix that is used to repeat the block instructions.

▶ Select the correct option

☐	RPT
☐	REPEAT
☒	REP
☐	REPB





CS401:Grand Quiz

Question # 29 of 30 (Start time: 10:27:09 AM, 21 June 2021)

Which of following is an illegal assembly instruction?

Select the correct option

☐	mov al, bl
☐	mov ax, bx
☒	mov [num1], [num2]
☐	mov [num1], al





CS401:Grand Quiz

Question # 30 of 30 (Start time: 10:28:06 AM, 21 June 2021)

Each entry of the interrupt vector table is of _____ byte(s).

Select the correct option

☒	4
☐	3
☐	2
☐	1





CS401:Grand Quiz

Question # 30 of 30 (Start time: 10:28:06 AM, 21 June 2021)

Each entry of the interrupt vector table is of _____ byte(s).

Select the correct option

☒	4
☐	3
☐	2
☐	1





CS401:Grand Quiz

Question # 1 of 30 (Start time: 10:53:43 AM, 21 June 2021)

Which of the following has magnitude and sign?

Select the correct option

☒	signed number
☐	hexadecimal number
☐	octal number
☐	unsigned number





CS401:Grand Quiz

Question # 1 of 30 (**Start time: 10:53:43 AM, 21 June 2021**)

Which of the following has magnitude and sign?

Select the correct option

☒	signed number
☐	hexadecimal number
☐	octal number
☐	unsigned number



Click to





CS401:Grand Quiz

Question # 2 of 30 (Start time: 10:54:45 AM, 21 June 2021)

How many characters are defined by the standard ASCII?

Select the correct option

☐	32
☒	128
☐	64
☐	132





CS401:Grand Quiz

Question # 3 of 30 (Start time: 10:55:24 AM, 21 June 2021)

The number of _____ in a cell is called the cell width.

Select the correct option

☐	Nibbles
☐	Words
☒	Bits
☐	Bytes





Question # 4 of 30 (Start time: 10:56:06 AM, 21 June 2021)

Keeping in view the downward compatability between the two systems, the codes written for the Intel 8088 are _____ on the Intel 386 processor.

Select the correct option

☐	invalid
☒	valid
☐	control
☐	altered

Click to Save Answer





CS401:Grand Quiz

Question # 5 of 30 (Start time: 10:56:47 AM, 21 June 2021)

During program execution, if any change occurs in AH or AL, is also reflected in _____.

Select the correct option

☐	AH
☐	AL
☐	DX
☒	AX





CS401:Grand Quiz

Question # 6 of 30 (Start time: 10:57:23 AM, 21 June 2021)

Which bit of the attribute byte represents the red component of foreground color?

Select the correct option

☒	2
☐	3
☐	4
☐	5





CS401:Grand Quiz

Question # 7 of 30 (Start time: 10:57:51 AM, 21 June 2021)

BP stands for _____ pointer.

Select the correct option

☒	Base
☐	Byte
☐	Basic
☐	Bit





CS401:Grand Quiz

Question # 8 of 30 (Start time: 10:58:22 AM, 21 June 2021)

MOV AX, 0X4C00

INT 0X21

Which of the following is the purpose of the given instructions?

Select the correct option

☐	Variable definition
☒	Terminate a program
☐	Move number into ax
☐	Move ax into 0X4C00





CS401:Grand Quiz

Question # 9 of 30 (Start time: 10:58:58 AM, 21 June 2021)

A symbol associated to a point in the program is called a _____.

Select the correct option

☐	Subroutines
☒	Label
☐	Stack
☐	JUMP





Question # 10 of 30 (Start time: 10:59:37 AM, 21 June 2021)

In "Base + Offset" addressing, the value contained in the base register is added with the offset to get the _____ address.

Select the correct option

- | | |
|----------------------------------|-----------|
| ☐ | physical |
| ☐ | linear |
| ☐ | virtual |
| ☒ | effective |

Click to Save A





CS401:Grand Quiz

Question # 11 of 30 (Start time: 11:00:11 AM, 21 June 2021)

MUL instruction performs an unsigned multiplication of the source operand and the _____.

Select the correct option

☐	carry
☐	word at ES:DI
☒	accumulator
☐	base





CS401:Grand Quiz

Question # 12 of 30 (Start time: 11:00:49 AM, 21 June 2021)

DW is used to reserve _____ bit value in memory.

Select the correct option

☒	16
☐	24
☐	8
☐	32





CS401:Grand Quiz

Question # 13 of 30 (Start time: 11:01:31 AM, 21 June 2021)

The correlation process from the interrupt number to the interrupt handler uses a table called _____.

Select the correct option

☐	Interrupt Handling Table
☒	Interrupt Vector Table
☐	Interrupt Correlation Table
☐	Interrupt Calling Table





CS401:Grand Quiz

Question # 14 of 30 (Start time: 11:02:06 AM, 21 June 2021)

Interrupts are _____ events.

Select the correct option

☐	Synchronous
☐	Predictable
☐	Dependent
☒	Asynchronous





CS401:Grand Quiz

Question # 15 of 30 (Start time: 11:02:50 AM, 21 June 2021)

What makes db different from dw?

Select the correct option

- | | |
|----------------------------------|---|
| ☒ | dw occupies 16 bits and db occupies 8 bits |
| ☐ | db occupies 4 bits while dw occupies 8 bits |
| ☐ | dw occupies 8 bits and db occupies 16 bits |
| ☐ | db occupies 1 bit while dw occupies 2 bits |





CS401:Grand Quiz

Question # 16 of 30 (Start time: 11:03:14 AM, 21 June 2021)

Verification of data integrity between sender and receiver is performed by the _____ flag.

Select the correct option

☐	zero
☐	auxiliary carry
☒	parity
☐	sign





CS401:Grand Quiz

Question # 17 of 30 (Start time: 11:03:47 AM, 21 June 2021)

Which of the following are the basic bitwise logical operations?

Select the correct option

- | | |
|----------------------------------|------------------------|
| ☐ | NOT, OR, AND, and NNOT |
| ☐ | AND, NOR, OR and NOT |
| ☐ | AND, OR, XOR, and NAND |
| ☒ | AND, OR, XOR, and NOT |





CS401:Grand Quiz

Question # 18 of 30 (Start time: 11:04:24 AM, 21 June 2021)

Which of the following string instructions is generally used in a loop instead of REP prefix?

Select the correct option

☐	CMPS
☒	LODS
☐	STOS
☐	SCAS

Click





CS401:Grand Quiz

Question # 19 of 30 (Start time: 11:04:57 AM, 21 June 2021)

BH register is a (an) _____ bit register.

Select the correct option

☐	32
☒	8
☐	24
☐	16





CS401:Grand Quiz

Question # 20 of 30 (Start time: 11:05:46 AM, 21 June 2021)

Stack is a data structure that behaves in _____ manner.

Select the correct option

☐	FIFO
☐	LILO
☒	LIFO
☐	Both LIFO and FIFO





CS401:Grand Quiz

Question # 21 of 30 (Start time: 11:06:14 AM, 21 June 2021)

Program consists of _____ logical parts.

Select the correct option

☐	Two
☒	Three
☐	One
☐	Four





CS401:Grand Quiz

Question # 22 of 30 (Start time: 11:06:47 AM, 21 June 2021)

Suppose, the current value of IP is 0x0129, and the relative address is 0x0012. What will be the new value of IP?

Select the correct option

☐	0x0012
☒	0x013B
☐	0x0135
☐	0x0129





CS401:Grand Quiz

Question # 23 of 30 (Start time: 11:07:17 AM, 21 June 2021)

With the execution of CALL instruction, the value of _____ is decremented by 2.

Select the correct option

☒	SP
☐	DS
☐	CX
☐	BX





CS401:Grand Quiz

Question # 25 of 30 (Start time: 11:08:13 AM, 21 June 2021)

Which of the following is the interrupt number for Non-Maskable Interrupt (NMI)?

Select the correct option

☐	INT 1
☐	INT 0
☒	INT 2
☐	INT 3





CS401:Grand Quiz

Question # 26 of 30 (Start time: 11:08:41 AM, 21 June 2021)

What makes CMP different from SUB?

Select the correct option

- | | |
|----------------------------------|--|
| ☐ | Both CMP and SUB are destructive subtraction |
| ☐ | CMP is a non destructive subtraction that does not affect any flag, however SUB is a destructive subtraction |
| ☒ | SUB is a destructive subtraction while CMP is a non-destructive subtraction. Both affect the flags |
| ☐ | SUB is a non destructive subtraction while CMP is a destructive one |





CS401:Grand Quiz

Question # 24 of 30 (Start time: 11:07:41 AM, 21 June 2021)

Which of the following formulae is used for calculating the segment address of an interrupt service routine "n"?

Select the correct option

☐	$n*4$
☐	$n*4-2$
☐	$n*2+4$
☒	$n*4+2$





CS401:Grand Quiz

Question # 27 of 30 (Start time: 11:09:35 AM, 21 June 2021)

Which of the following instructions is used for non-destructive AND operation?

Select the correct option

☒	TEST
☐	ADC
☐	NAND
☐	CMP





CS401:Grand Quiz

Question # 28 of 30 (Start time: 11:10:08 AM, 21 June 2021)

There are _____ registers in iAPX88 architecture that can hold address of data.

Select the correct option

☒	4
☐	2
☐	1
☐	3





CS401:Grand Quiz

Question # 29 of 30 (Start time: 11:10:49 AM, 21 June 2021)

Sending the appropriate signals on the control bus to the memory is the responsibility of _____.

Select the correct option

☐	Control Bus
☐	Data Bus
☐	Memory
☒	Processor





CS401:Grand Quiz

Question # 30 of 30 (Start time: 11:11:25 AM, 21 June 2021)

Call instruction changes the values of _____ and _____ registers.

Select the correct option

- | | |
|----------------------------------|--------|
| ☐ | IP, AL |
| ☒ | IP, SP |
| ☐ | IP, AX |
| ☐ | IP, DX |





CS401:Grand Quiz

Question # 1 of 30 (Start time: 11:26:04 AM, 21 June 2021)

The symbols in extended ASCII table by IBM fits in one _____.

Select the correct option

☐	bit
☒	byte
☐	word
☐	doubleword





CS401:Grand Quiz

Question # 2 of 30 (Start time: 11:26:37 AM, 21 June 2021)

Stack is a data structure that behaves in a First In Last _____ manner.

Select the correct option

☐	Add
☐	Push
☒	Out
☐	In





CS401:Grand Quiz

Question # 3 of 30 (Start time: 11:27:08 AM, 21 June 2021)

_____ is used for temporary diversion.

Select the correct option

☒	Call
☐	Return
☐	Jump
☐	Short jump





CS401:Grand Quiz

Question # 4 of 30 (Start time: 11:27:35 AM, 21 June 2021)

Execution of CALL instruction affects _____ number of bits in Flags register.

Select the correct option

☐	1
☐	5
☐	3
☒	0





CS401:Grand Quiz

Question # 5 of 30 (Start time: 11:28:20 AM, 21 June 2021)

Far jump is not position relative rather _____.

Select the correct option

☐	indirect
☒	absolute
☐	temporary
☐	memory dependent





Question # 6 of 30 (Start time: 11:28:54 AM, 21 June 2021)

In _____ operation, a carry flag is inserted from the left moving every bit one position to its right. The right most bit is dropped into the carry flag.

Select the correct option

- | | |
|----------------------------------|----------------------------------|
| ☐ | Rotate Left (ROL) |
| ☒ | Rotate through Carry Right (RCR) |
| ☐ | Rotate Right (ROR) |
| ☐ | Rotate through Carry Left (RCL) |

Click to Submit





CS401:Grand Quiz

Question # 7 of 30 (Start time: 11:29:31 AM, 21 June 2021)

Which of the following is the correct syntax for 'OR' operation in assembly language?

Select the correct option

☒	or ax, bx
☐	ax or, bx
☐	ax, or bx
☐	ax or bx





CS401:Grand Quiz

Question # 8 of 30 (Start time: 11:30:21 AM, 21 June 2021)

Shift logical Left (SHL) moves all the bits one position to the _____ and inserts a zero from the _____.

Select the correct option

- | | |
|----------------------------------|--------------|
| ☐ | right, right |
| ☐ | left, left |
| ☐ | right, left |
| ☒ | left, right |

Click





CS401:Grand Quiz

Question # 9 of 30 (Start time: 11:30:54 AM, 21 June 2021)

When an element is pushed on the stack, SP is decremented by _____.

Select the correct option

- | | |
|----------------------------------|---|
| ☒ | 2 |
| ☐ | 1 |
| ☐ | 4 |
| ☐ | 3 |





CS401:Grand Quiz

Question # 10 of 30 (Start time: 11:31:20 AM, 21 June 2021)

Stack is:

Select the correct option

☒	a data structure
☐	both algorithm and data structure
☐	an algorithm
☐	a procedure





CS401:Grand Quiz

Question # 11 of 30 (Start time: 11:31:46 AM, 21 June 2021)

cli stands for?

Select the correct option

- | | |
|----------------------------------|--------------------------|
| ☐ | control line instruction |
| ☐ | clock interrupt |
| ☐ | cross line instruction |
| ☒ | clear the interrupt flag |





Question # 12 of 30 (Start time: 11:32:23 AM, 21 June 2021)

In _____, a zero is inserted from the right, and every bit moves one position to its left. The most significant bit also drops into the carry flag.

Select the correct option

Reload

- | | |
|----------------------------------|------------------------------|
| ☐ | Rotate Right (ROR) |
| ☒ | Shift Arithmetic Left (SAL) |
| ☐ | Shift Arithmetic Right (SAR) |
| ☐ | Shift logical Right (SHR) |

Click to Save Answer & Move to





CS401:Grand Quiz

Question # 13 of 30 (Start time: 11:33:16 AM, 21 June 2021)

DW is used to reserve _____ bit value in memory.

Select the correct option

- | | |
|----------------------------------|----|
| ☐ | 24 |
| ☒ | 16 |
| ☐ | 32 |
| ☐ | 8 |





CS401:Grand Quiz

Question # 14 of 30 (Start time: 11:33:58 AM, 21 June 2021)

Verification of data integrity between sender and receiver is performed by the _____ flag.

Select the correct option

☐	zero
☐	sign
☐	auxiliary carry
☒	parity





CS401:Grand Quiz

Question # 15 of 30 (Start time: 11:34:36 AM, 21 June 2021)

In far jump, _____.

Select the correct option

- | | |
|----------------------------------|--|
| ☒ | both offset and segment are given |
| ☐ | only offset is given |
| ☐ | no need to mention any segment or offset |
| ☐ | only segment address is given |





CS401:Grand Quiz

Question # 16 of 30 (Start time: 11:34:59 AM, 21 June 2021)

Each entry of the interrupt vector table is of _____ byte(s).

Select the correct option

☒	4
☐	3
☐	2
☐	1





CS401:Grand Quiz

Question # 17 of 30 (Start time: 11:35:28 AM, 21 June 2021)

What would be the output of MOV AX, BL?

Select the correct option

- | | |
|----------------------------------|---|
| ☒ | Assembler will declare it as an illegal expression |
| ☐ | The contents of the AX register will be moved to the BL register |
| ☐ | The contents of the BL register will be moved to the AX register |
| ☐ | The contents of the BL register will be moved to the AX register, and the BL will be set as 0 |





CS401:Grand Quiz

Question # 18 of 30 (Start time: 11:35:52 AM, 21 June 2021)

The stack pointer marks the _____ of stack.

Select the correct option

☒	top
☐	bottom
☐	middle
☐	top and bottom





CS401:Grand Quiz

Question # 19 of 30 (Start time: 11:36:14 AM, 21 June 2021)

Which of the following is a program control instruction?

Select the correct option

☒	jne 0534
☐	add bx, 0534
☐	cli
☐	mov ax, bx





CS401:Grand Quiz

Question # 20 of 30 (Start time: 11:36:43 AM, 21 June 2021)

By default, BP is associated with _____.

Select the correct option

☐	CS
☐	SP
☐	IP
☒	SS





CS401:Grand Quiz

Question # 21 of 30 (Start time: 11:37:13 AM, 21 June 2021)

What makes db different from dw?

Select the correct option

- | | |
|----------------------------------|---|
| ☒ | dw occupies 16 bits and db occupies 8 bits |
| ☐ | db occupies 1 bit while dw occupies 2 bits |
| ☐ | db occupies 4 bits while dw occupies 8 bits |
| ☐ | dw occupies 8 bits and db occupies 16 bits |





CS401:Grand Quiz

Question # 22 of 30 (Start time: 11:37:41 AM, 21 June 2021)

EBCDIC, grey code, and ASCII are the standards for _____ representation of characters.

Select the correct option

☒	numeric
☐	generic
☐	alpha-numeric
☐	alphabetic





CS401:Grand Quiz

Question # 23 of 30 (Start time: 11:38:13 AM, 21 June 2021)

In 8051, there is an _____ stack.

Select the correct option

☐	semi-automatic
☒	incrementing
☐	decrementing
☐	stagnant





CS401:Grand Quiz

Question # 24 of 30 (Start time: 11:38:43 AM, 21 June 2021)

The interrupt call loads new values in CS, IP and _____.

Select the correct option

☐	SS
☐	DS
☒	FLAGS
☐	BP





CS401:Grand Quiz

Question # 25 of 30 (Start time: 11:39:19 AM, 21 June 2021)

The correct instruction to subtract with borrow is _____.

Select the correct option

☒	SBB
☐	SWB
☐	SB
☐	SUBB





CS401:Grand Quiz

Question # 26 of 30 (Start time: 11:39:43 AM, 21 June 2021)

The extra bit produced as a result of an arithmetic operation that does not fit in the target register is stored in:

Select the correct option

☐	Zero Flag
☐	Interrupt Flag
☐	Overflow Flag
☒	Carry Flag





CS401:Grand Quiz

Question # 27 of 30 (Start time: 11:40:20 AM, 21 June 2021)

The ordering of a program's instructions is ensured by the _____ register.

Select the correct option

☐	Flags
☐	Stack Pointer
☒	Program Counter
☐	Index





CS401:Grand Quiz

Question # 28 of 30 (Start time: 11:40:51 AM, 21 June 2021)

In assembly language, "DIV BL" instruction has an _____ bit operand.

Select the correct option

☐	16
☐	64
☐	32
☒	8





CS401:Grand Quiz

Question # 29 of 30 (Start time: 11:41:31 AM, 21 June 2021)

Which of the following jump is taken if the last arithmetic operation has produced a number in its destination that has even parity?

Select the correct option

☐	JNP
☐	JNS
☒	JPE
☐	JPO





CS401:Grand Quiz

Question # 30 of 30 (Start time: 11:42:02 AM, 21 June 2021)

Which of the following cannot be pushed on the stack?

Select the correct option

☐	6 bytes
☒	1 byte
☐	2 bytes
☐	4 bytes





CS401:Grand Quiz

Question # 1 of 30 (Start time: 11:45:20 AM, 21 June 2021)

When the operand of DIV instruction is in 16 bits, then the implied dividend will be in _____.

Select the correct option

☒	DX:AX
☐	AX
☐	ES:AX
☐	DS:BX





CS401:Grand Quiz

Question # 2 of 30 (Start time: 11:45:46 AM, 21 June 2021)

cli stands for?

Select the correct option

☐	clock interrupt
☐	control line instruction
☐	cross line instruction
☒	clear the interrupt flag





CS401:Grand Quiz

Question # 3 of 30 (Start time: 11:46:36 AM, 21 June 2021)

We can convert a digit into its ASCII representation by adding _____ to it.

Select the correct option

☒	0x30
☐	0x05
☐	0x20
☐	0x10





CS401:Grand Quiz

Question # 4 of 30 (Start time: 11:47:01 AM, 21 June 2021)

The instruction for permanent diversion in Intel 8088 is _____.

Select the correct option

☐	call
☐	ret
☒	jmp
☐	mov





CS401:Grand Quiz

Question # 5 of 30 (Start time: 11:47:39 AM, 21 June 2021)

In the instruction "mov word [es:0], 0x1230", 12 refers to _____ color on _____ background.

Select the correct option

- | | |
|----------------------------------|--------------|
| ☐ | green, black |
| ☐ | black, green |
| ☐ | blue, green |
| ☒ | green, blue |

Click





CS401:Grand Quiz

Quiz Start Time: 11:45 AM, 21

Question # 6 of 30 (Start time: 11:48:11 AM, 21 June 2021)

To

Shift Arithmetic Right (SAR) shifts every bit one place to the right and places a copy of the _____ significant bit at the _____ significant place.

Select the correct option

Reload Math E

- | | |
|----------------------------------|--------------|
| ☐ | most, least |
| ☐ | least, least |
| ☐ | least, most |
| ☒ | most, most |

Click to Save Answer & Move to Next Q





CS401:Grand Quiz

Question # 7 of 30 (Start time: 11:48:42 AM, 21 June 2021)

Total number of reserved interrupts in Intel 8088 are _____.

Select the correct option

☐	255
☒	5
☐	8
☐	3





CS401:Grand Quiz

Question # 8 of 30 (Start time: 11:49:09 AM, 21 June 2021)

In general, width of a memory cell cannot be greater than the width of _____.

Select the correct option

☒	Data Bus
☐	Control Bus
☐	Address Bus
☐	I/O Bus





CS401:Grand Quiz

Question # 9 of 30 (Start time: 11:49:35 AM, 21 June 2021)

Which of the following is a program control instruction?

Select the correct option

☐	cli
☐	add bx, 0534
☒	jne 0534
☐	mov ax, bx





Question # 10 of 30 (Start time: 11:50:07 AM, 21 June 2021)

When there is a 16 bit operand with the DIV instruction, then the implied dividend will be in _____ bits.

Select the correct option

- | | |
|----------------------------------|----|
| ☐ | 8 |
| ☐ | 16 |
| ☒ | 32 |
| ☐ | 64 |

Click to Save An





Question # 11 of 30 (Start time: 11:50:40 AM, 21 June 2021)

Which of the following formulae is used for calculating the segment address of an interrupt service routine "n"?

Select the correct option

- | | |
|----------------------------------|---------|
| ☒ | $n*4+2$ |
| ☐ | $n*4$ |
| ☐ | $n*4-2$ |
| ☐ | $n*2+4$ |

cli





CS401:Grand Quiz

Question # 12 of 30 (Start time: 11:51:11 AM, 21 June 2021)

IAPX88 stands for _____ .

Select the correct option

- | | |
|----------------------------------|--|
| ☐ | Intel Advanced Process Extensions 88 |
| ☒ | Intel Advanced Processor Extensions 88 |
| ☐ | Intel Active Processor Extensions 88 |
| ☐ | Intel Advanced Processor External 88 |





CS401:Grand Quiz

Question # 13 of 30 (Start time: 11:51:48 AM, 21 June 2021)

All mathematical and logical operations are performed on the _____.

Select the correct option

☐	Arithmetic Logic Unit
☐	Flags Register
☐	Program Counter
☒	Accumulator





CS401:Grand Quiz

Quiz Start Time: 11:45 AM, 21 Jun 2021

Question # 14 of 30 (Start time: 11:52:20 AM, 21 June 2021)

Keeping in view the downward compatability between the two systems, the codes written for the Intel 8088 are _____ on the Intel 386 processor.

Select the correct option

Rel

☐	altered
☒	valid
☐	invalid
☐	control

Click to Save Answer & Move to Next Question

CS401:Grand Quiz

Quiz Start Time: 11:45 AM, 21 Jun 2021

Question # 15 of 30 (Start time: 11:52:47 AM, 21 June 2021)

Total I

In _____ operation, the carry flag is inserted from the right causing every bit to move one location to its left. The dropped bit from the left goes into the carry flag.

Select the correct option

☒	Rotate through Carry Left (RCL)
☐	Rotate Right (ROR)
☐	Rotate through Carry Right (RCR)
☐	Rotate Left (ROL)

Click to Save Answer & Move to Next Question





CS401:Grand Quiz

Question # 16 of 30 (Start time: 11:53:13 AM, 21 June 2021)

Registers are normally part of _____.

Select the correct option

☒	CPU
☐	Hard disk
☐	RAM
☐	CPU and Cache





CS401:Grand Quiz

Question # 17 of 30 (Start time: 11:53:37 AM, 21 June 2021)

Stack Pointer is _____, when the ret instruction is executed.

Select the correct option

☒	incremented by 2
☐	decremented by 2
☐	divided by 2
☐	multiplied by 2





CS401:Grand Quiz

Question # 18 of 30 (Start time: 11:54:09 AM, 21 June 2021)

Which of the following jumps makes intra segment control possible?

Select the correct option

☒	Far
☐	Short
☐	Near
☐	Long





CS401:Grand Quiz

Question # 19 of 30 (Start time: 11:54:37 AM, 21 June 2021)

Which of the following is the correct instruction for NOT operation?

Select the correct option

- | | |
|----------------------------------|--------------------|
| ☒ | not bx |
| ☐ | not byte [var], ax |
| ☐ | not word [var], ax |
| ☐ | not ax, bx |





CS401:Grand Quiz

Question # 20 of 30 (**Start time: 11:55:02 AM, 21 June 2021**)

_____ instruction directs the flow of program.

Select the correct option

- | | |
|----------------------------------|-----------------|
| ☐ | Move |
| ☐ | Arithmetic |
| ☐ | Special |
| ☒ | Program Control |





CS401:Grand Quiz

Question # 21 of 30 (Start time: 11:55:37 AM, 21 June 2021)

The mechanism used to drop the carry for making the computed address valid is known as _____.

Select the correct option

- | | |
|----------------------------------|---------------------|
| ☐ | segment overlapping |
| ☐ | overflow |
| ☐ | carry overload |
| ☒ | address wraparound |

Click





CS401:Grand Quiz

Question # 22 of 30 (Start time: 11:56:07 AM, 21 June 2021)

In assembly language, integer division results in _____ quotient and _____ remainder.

Select the correct option

- | | |
|----------------------------------|--------------------------------|
| ☐ | integer, floating point |
| ☐ | floating point, floating point |
| ☒ | integer, integer |
| ☐ | floating point, integer |

Click to S





CS401:Grand Quiz

Question # 23 of 30 (Start time: 11:56:47 AM, 21 June 2021)

There are _____ forms of MOVS instruction.

Select the correct option

- | | |
|----------------------------------|-------|
| ☐ | four |
| ☐ | three |
| ☒ | two |
| ☐ | five |





CS401:Grand Quiz

Question # 24 of 30 (Start time: 11:57:17 AM, 21 June 2021)

The stack pointer marks the _____ of stack.

Select the correct option

☐	middle
☒	top
☐	top and bottom
☐	bottom





CS401:Grand Quiz

Question # 25 of 30 (Start time: 11:57:59 AM, 21 June 2021)

The physical memory address of 1234:5678 segment-offset pair is

Select the correct option

☐	174B8
☐	179F8
☒	179B8
☐	179C8





CS401:Grand Quiz

Question # 26 of 30 (Start time: 11:58:28 AM, 21 June 2021)

The maximum parameters a subroutine can receive are _____ when all the general purpose registers are used.

Select the correct option

☒	7
☐	4
☐	5
☐	6

Click





CS401:Grand Quiz

Question # 27 of 30 (Start time: 11:59:01 AM, 21 June 2021)

Which of the following is the extension of object file?

Select the correct option

☐	.exe
☐	.asm
☐	.lst
☒	.com





CS401:Grand Quiz

Question # 28 of 30 (Start time: 11:59:42 AM, 21 June 2021)

In shift left operation, the left most bit will _____.

Select the correct option

- | | |
|----------------------------------|-------------------------------------|
| ☐ | always be 1 |
| ☐ | be discarded |
| ☐ | be moved to the right most position |
| ☒ | go into the Carry Flag |





CS401:Grand Quiz

Question # 29 of 30 (Start time: 12:00:21 PM, 21 June 2021)

Which type of jump(s) cover the whole memory?

- I. Short
- II. Near
- III. Far

Select the correct option

☐	II
☐	I
☐	I and II
☒	III





CS401:Grand Quiz

Question # 30 of 30 (Start time: 12:00:52 PM, 21 June 2021)

iAPX88 Architecture consists of

Select the correct option

☐	16 Registers
☐	10 Registers
☐	32 Registers
☒	14 Registers

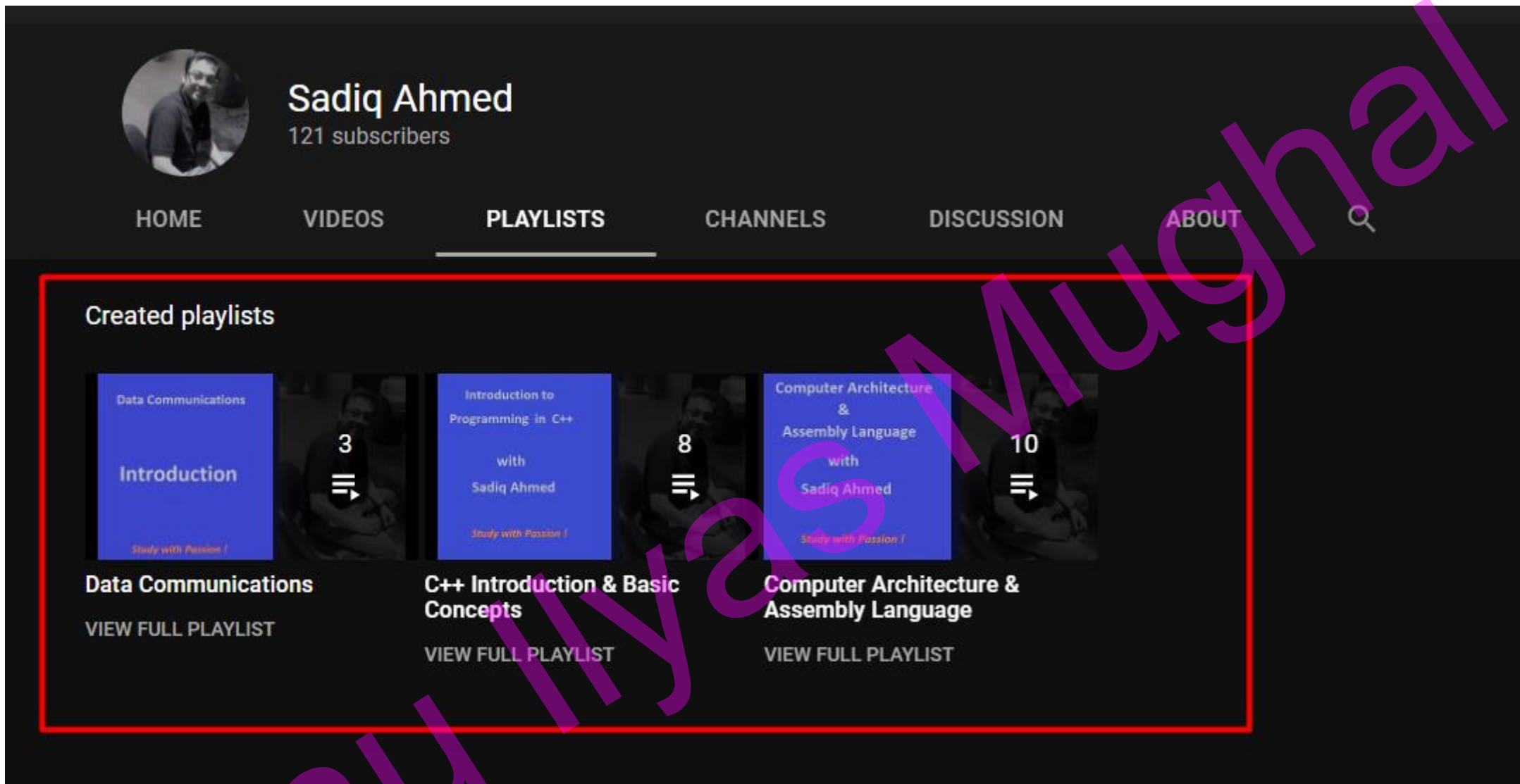
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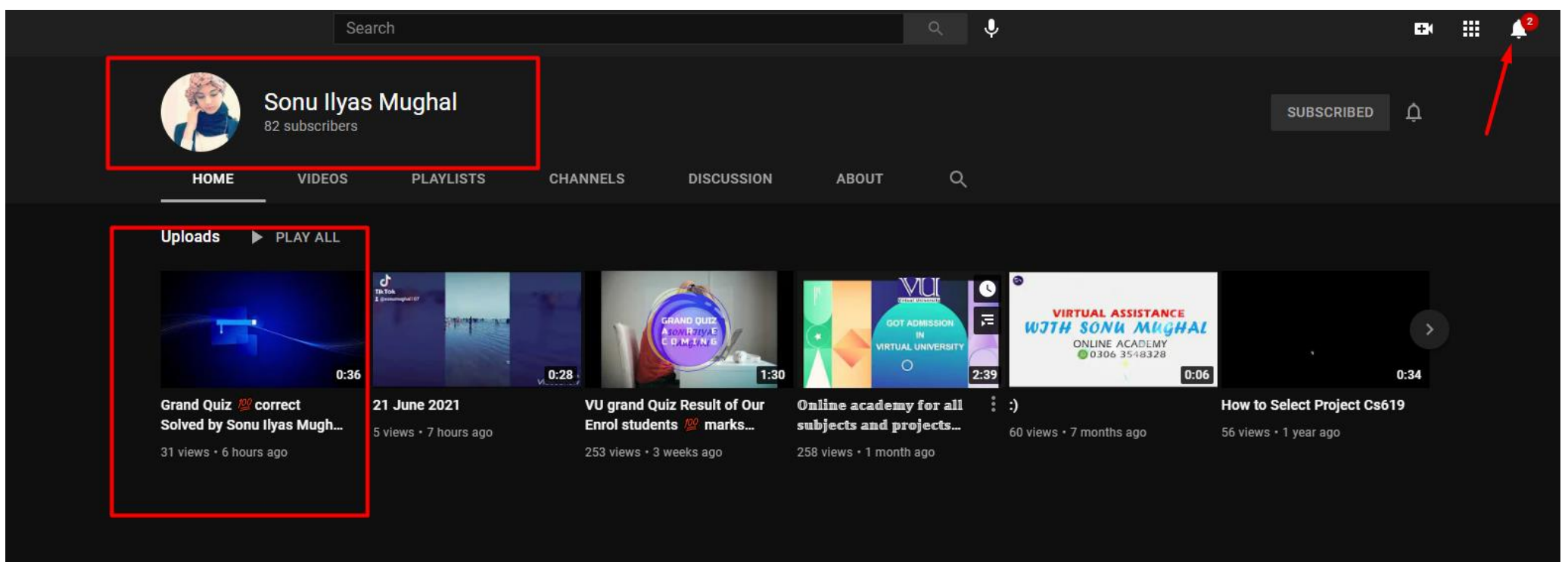


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