

## CS302P SOLVED QUIZ BY ASFANDYARR

- According to de morgan's law the expression  $(a+bc)'$  is equalent to?  **$(AB+AC)$**

EXPLANATION: Therefore,  $(a + bc)'$  is equivalent to  $a' * (b' + c')$ .

2) According to distributive law of boolean algebra, which of the following STATEMENT is true?

- $X+(YZ)=X+Y+Z$ 2)
- $X+(YZ)=XY+XZ$ 3)
- $X+(YZ)=XYZ$ 4)
- **$X+(YZ)=(X+Y)(X+Z)$**

3) In Electronics Workbench, the default value for the duty cycle of the clock source is

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- **50%**

4) In Electronics Workbench, which of the following is the best way to analyze/check each single output of our circuit?

- 7 SEGMENT DISPLAY
- RED PROBE
- AMMETER
- **BULB**

5) \_\_\_\_\_ gate will give high output (1) if any input is high (1). **OR GATE**

6) <p>Generally, for checking the result in a single bit, we use \_\_\_\_\_ indicator.</p>

- <p>Truth Table</p>
- <p>Logic Tester</p>
- <p>Logic Converter</p>
- **<p>Bulb</p>**