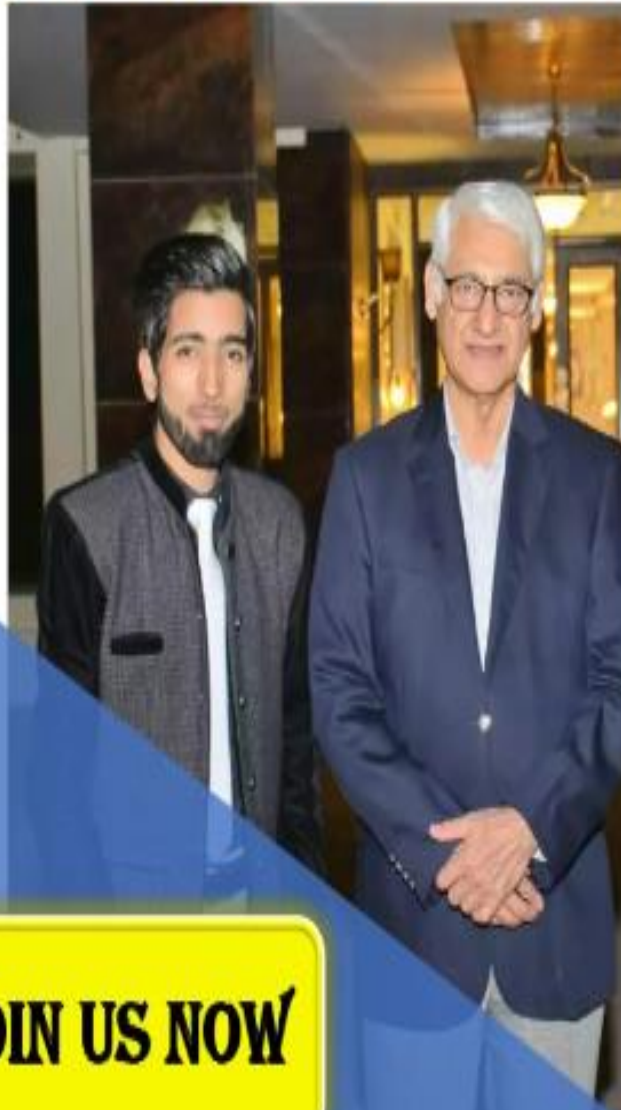


CS302 Important Topics Final Term By Vu Topper RM

وَعَزُّ مَنْ تَشَاءُ وَتَذَلُّ مَنْ تَشَاءُ

PROFESSIONAL ONLINE ACADEMY



**NOTHING IS
IMPOSSIBLE**

All Paid Services

- ❖ LMS Handling
- ❖ Important Notes
- ❖ Online Classes
- ❖ Projects
- ❖ Assignments
- ❖ Quiz
- ❖ GDB's

JOIN US NOW

For More Info
Contact us at:

Rizwan Manzoor

0322-4021365

- 1) ROM.
- 2) RAM.
- 3) Next state.
- 4) All latches.
- 5) State table.
- 6) A/D errors.
- 7) Skew clock.
- 8) D Flip Flop.
- 9) All counters.
- 10) All registers.
- 11) Present state.
- 12) J-K Flip Flop.
- 13) LUT stand for.
- 14) Flash Memory.

For More Help Contact Vu Topper RM

What's App 03224021365

- 15) Mealy Machine.
- 16) Moore Machine.
- 17) SOP expressions.
- 18) Circuit diagram.
- 19) Block bit diagram.
- 20) Up/Down Counter.
- 21) PROM vs EPROM.
- 22) Memory Read Write.
- 23) State diagram counter.
- 24) Master-Slave Flip Flop.
- 25) Memory cell two names.
- 26) Define sequential circuit?
- 27) 74HX163 a Mod 7 counter.
- 28) Application of multiplexer.

For More Help Contact Vu Topper RM

What's App 03224021365

- 29) Synchronous Burit SRAM.
- 30) Logic diagram of SR Latch.
- 31) Traffic signal control system.
- 32) What is DRAM and its types.
- 33) Make SOP from a give K-Map.
- 34) Define bi-stable multivibrators?
- 35) Serial in and parallel out 11_bit.
- 36) All Karnaugh Maps and Truth Tables.
- 37) Analogue and digital conversion logical.
- 38) 4-bit Bidirectional Shift register 74HC194.

For More Help Contact Vu Topper RM

What's App 03224021365

39) Reduce the num of state as much as positive?

40) Two different types of volatile memory names?

41) Synchronous Counter & Asynchronous Counter.

42) Memory Signal and Basic Operation on Memory.

43) Draw the circuit diagram of master slave flipflop.

44) Draw Circuit diagram of R/2R Ladder D/A conveter.

45) Circuit diagram Of Serial In/ Shift Right Out Register?

For More Help Contact Vu Topper RM

What's App 03224021365

- 46) Convert binary to decimal using Sum of Weighted Method.**
- 47) Counters, synchronous vs asynchronous. counter (concept).**
- 48) Difference b/w Shift Register and counter by logic diagrams.**
- 49) Different between DRAM and SRAM through circuit diagram?**
- 50) Complete diagram by putting appropriate gate in each red box.**
- 51) Write ISTYPE statement used to specify active low output of register mode?**

For More Help Contact Vu Topper RM

What's App 03224021365

52) Write the five element which will used in dual slop analogue to digital converter circuit?

**For More File You Can Visit
My Youtube Channel
(#VuTopperRM)**