

**CS302P - Digital Logic Design**

**(Practical) (Semester Quiz # 04)**

**Most Important Mcqs Final term**

# Ifraheem Hussain

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BC240440809: MALIKA ZAHRA

Time Left 78 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 04)

Quiz Start Time: 05:30 AM, 02 July 2025

Question # 10 of 10 ( Start time: 05:32:42 AM, 02 July 2025 )

Total Marks: 1

Three decade counter would have -----

Select the correct option

 Reload Math Equations

- 4 BCD counters
- 2 BCD counters
- 3 BCD counters
- 5 BCD counters

Click to Save Answer & Move to Next Question

BC240440809: MALIKA ZAHRA

Time Left 80 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 04)

Quiz Start Time: 05:30 AM, 02 July 2025

Question # 9 of 10 ( Start time: 05:32:25 AM, 02 July 2025 )

Total Marks: 1

Convert the Gray code 1011 to binary.

Select the correct option

 Reload Math Equations

- 1011
- 1101
- 1010
- 0111

Click to Save Answer & Move to Next Question

BC240440809: MALIKA ZAHRA

Time Left 80  
sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 04)

Quiz Start Time: 05:30 AM, 02 July 2025

Question # 8 of 10 ( Start time: 05:32:06 AM, 02 July 2025 )

Total Marks: 1

Convert the binary number 1100 to Gray code.

Select the correct option

 Reload Math Equations

<input type="radio"/>	1000
<input type="radio"/>	0101
<input checked="" type="radio"/>	1010
<input type="radio"/>	0111

Click to Save Answer & Move to Next Question

BC240440809: MALIKA ZAHRA

Time Left 86  
sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 04)

Quiz Start Time: 05:30 AM, 02 July 2025

Question # 7 of 10 ( Start time: 05:31:55 AM, 02 July 2025 )

Total Marks: 1

A circuit that compares two numbers and determines their magnitude is called \_\_\_\_\_

Select the correct option

 Reload Math Equations

<input checked="" type="radio"/>	Magnitude comparator
<input type="radio"/>	Size comparator
<input type="radio"/>	Inverter
<input type="radio"/>	Height comparator

Click to Save Answer & Move to Next Question

BC240440809: MALIKA ZAHRA

Time Left 86 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 04)

Quiz Start Time: 05:30 AM, 02 July 2025

Question # 6 of 10 ( Start time: 05:31:44 AM, 02 July 2025 )

Total Marks: 1

Ripple counters are also called \_\_\_\_\_

Select the correct option

Reload Math Equations

- SSI counters
- Synchronous counters
- VLSI counters
- Asynchronous counters

Click to Save Answer & Move to Next Question

BC240440809: MALIKA ZAHRA

Time Left 83 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 04)

Quiz Start Time: 05:30 AM, 02 July 2025

Question # 5 of 10 ( Start time: 05:31:30 AM, 02 July 2025 )

Total Marks: 1

A decimal counter has \_\_\_\_\_ states.

Select the correct option

Reload Math Equations

- 2
- 7
- 9
- 10

Click to Save Answer & Move to Next Question

BC240440809: MALIKA ZAHRA

Time Left 85 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 04)

Quiz Start Time: 05:30 AM, 02 July 2025

Question # 4 of 10 ( Start time: 05:31:16 AM, 02 July 2025 )

Total Marks: 1

To create a Johnson Counter by using D-flip flops, how flip flops should be connected together?

Select the correct option

Reload Math Equations

- Q output of previous flip flop should be connected to clock input of second flip flop
- Q' output of previous flip flop should be connected to clock input of second flip flop
- Q output of previous flip flop should be connected to D input of second flip flop
- Q' output of previous flip flop should be connected to D input of second flip flop

Click to Save Answer & Move to Next Question

BC240440809: MALIKA ZAHRA

Time Left 85 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 04)

Quiz Start Time: 05:30 AM, 02 July 2025

Question # 3 of 10 ( Start time: 05:31:03 AM, 02 July 2025 )

Total Marks: 1

An identify comparator is defined as a digital comparator which has -----

Select the correct option

Reload Math Equations

- Two output terminals
- Only one output terminal
- Three output terminals
- No output terminals

Click to Save Answer & Move to Next Question

BC240440809: MALIKA ZAHRA

Time Left 84 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 04)

Quiz Start Time: 05:30 AM, 02 July 2025

Question # 2 of 10 ( Start time: 05:30:49 AM, 02 July 2025 )

Total Marks: 1

A counter circuit is usually constructed of \_\_\_\_\_

Select the correct option

Reload Math Equations

- A number of NAND gates connected in cascade form
- A number of latches connected in cascade form
- A number of flip-flops connected in cascade
- A number of NOR gates connected in cascade form

Click to Save Answer & Move to Next Question

BC240440809: MALIKA ZAHRA

Time Left 81 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 04)

Quiz Start Time: 05:30 AM, 02 July 2025

Question # 1 of 10 ( Start time: 05:30:33 AM, 02 July 2025 )

Total Marks: 1

Which of the following is the modified code of Excess-3 code?

Select the correct option

Reload Math Equations

- ASCII
- EBDIC
- BCD
- Gray Code

Click to Save Answer & Move to Next Question

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