

VIRTUAL UNIVERSITY OF PAKISTAN

A Good Education is a Foundation For a Better Future

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EMAIL-hadirajputofficial@gmail.com
WHATSAPP& CALL-03087122922

TEAM HADI(VU LAHORE)

MID TERM PAST PAPER

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WARNING: Team HADI is not responsible for any mistake or wrong answer. All students reading and using this document may check and confirm the answers at their own.



WhatsApp:03087122922



FACEBOOK ID: <https://www.facebook.com/hadipastpapers/>

Best of luck!



Question # 1 of 30 (Start time: 10:43:48 PM, 27 December 2020)

Total Marks: 1

Which notation do we use to name different fields of a register in RTL?

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Select the correct option

- 0
- <
- +
- :=

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Question # 1 of 30 (Start time: 10:43:48 PM, 27 December 2020)

Total Marks: 1

Which notation do we use to name different fields of a register in RTL?

Select the correct option

- 0
- <
- +
- :=

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Question # 12 of 30 (Start time: 10:52:05 PM, 27 December 2020)

Total Marks: 1

Type checking allows the _____ to determine memory requirements for variables.

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Select the correct option

<input checked="" type="radio"/>	Compiler	//
<input type="radio"/>	Debugger	//
<input type="radio"/>	Linker	//
<input type="radio"/>	Loader	//

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Question # 30 of 30 (Start time: 11:01:20 PM, 27 December 2020)

Total Marks: 1

VLIW stands for _____.

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Select the correct option

- Variable Length Instruction Word
- Very Long Instruction Word
- Very Long Instruction Width
- Variable Length Instruction Width

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Question # 26 of 30 (Start time: 10:59:08 PM, 27 December 2020)

Total Marks: 1

The MAR is connected directly to the _____.

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Select the correct option

- | | | |
|----------------------------------|------------------|----|
| <input type="radio"/> | MBR | // |
| <input checked="" type="radio"/> | CPU internal bus | // |
| <input type="radio"/> | CPU external bus | |
| <input type="radio"/> | LIC | |

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Question # 15 of 30 (Start time: 10:53:46 PM, 27 December 2020)

Total Marks: 1

Type D instruction format of SRC includes _____ ALU registers.

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Select the correct option

- 3
- 4
- 1
- 2

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Question # 24 of 30 (Start time: 10:58:10 PM, 27 December 2020)

Total Marks: 1

The _____ instruction is completed once memory access has been made and the memory location has been written to.

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Select the correct option

<input type="radio"/>	Link	//
<input type="radio"/>	Branch	//
<input checked="" type="radio"/>	Store	
<input type="radio"/>	Control	

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Question # 30 of 30 (Start time: 11:01:20 PM, 27 December 2020)

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- Variable Length Instruction Width

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Question # 17 of 30 (Start time: 10:54:12 PM, 27 December 2020)

Total Marks: 1

The SRC uses a hazard detection unit. The hazard can be resolved using either pipeline stalls or by _____.

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Select the correct option

<input checked="" type="radio"/>	Data forwarding	//
<input type="radio"/>	Data compressing	//
<input type="radio"/>	Instruction forwarding	//
<input type="radio"/>	Instruction handling	//

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Question # 8 of 30 (Start time: 10:50:12 PM, 27 December 2020)

Total Marks: 1

Which of the following RTL descriptions is used for specifying the operation of an SRC instruction?

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Select the correct option

- | | | |
|----------------------------------|------------|----|
| <input type="radio"/> | IR<31..27> | // |
| <input type="radio"/> | IR<22..26> | // |
| <input checked="" type="radio"/> | IR<21..17> | |
| <input type="radio"/> | IR<21..0> | |

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Question # 22 of 30 (Start time: 10:57:32 PM, 27 December 2020)

Total Marks: 1

The Memory Buffer Register (MBR) has a _____ connection with both the memory sub-system and the registers/ALU.

Select the correct option

<input type="radio"/>	zero-directional	
<input type="radio"/>	uni-directional	
<input checked="" type="radio"/>	bi-directional	
<input type="radio"/>	tri-directional	

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Question # 20 of 30 (Start time: 10:56:26 PM, 27 December 2020)

Total Marks: 1

Motorola MC68000 is an example of _____ microprocessor.

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Select the correct option

<input checked="" type="radio"/>	CISC	/
<input type="radio"/>	RISC	/
<input type="radio"/>	SRC	
<input type="radio"/>	FALCON	

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Question # 28 of 30 (Start time: 11:00:19 PM, 27 December 2020)

Total Marks: 1

_____ control signal enables the input to the PC for receiving a value that is currently on the internal processor bus.

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Select the correct option

- | | | |
|----------------------------------|------|----|
| <input checked="" type="radio"/> | LPC | // |
| <input type="radio"/> | INC4 | // |
| <input type="radio"/> | LC | |
| <input type="radio"/> | Cout | |

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Question # 27 of 30 (Start time: 10:59:52 PM, 27 December 2020)

Total Marks: 1

Which of the following register(s) is/are programmer invisible and is/are required to hold an operand or result value while the bus is busy transmitting some other value?

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Select the correct option

- | | | |
|----------------------------------|-------------------------|----|
| <input type="radio"/> | Instruction Register | // |
| <input checked="" type="radio"/> | Memory address register | // |
| <input type="radio"/> | Memory Buffer Register | // |
| <input type="radio"/> | Registers A and C | // |

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Question # 25 of 30 (Start time: 10:58:36 PM, 27 December 2020)

Total Marks: 1

Almost every commercial computer has its own particular _____ language.

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Select the correct option

- | | | |
|----------------------------------|-----------------------|----|
| <input type="radio"/> | Scripting Language | // |
| <input type="radio"/> | Visual Language | // |
| <input type="radio"/> | Higher Level Language | |
| <input checked="" type="radio"/> | Assembly Language | |

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Question # 13 of 30 (Start time: 10:52:46 PM, 27 December 2020)

Total Marks: 1

In any instruction, the possible locations of the source operands can be _____.

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Select the correct option

- | | | |
|----------------------------------|------------------|----|
| <input type="radio"/> | CPU Registers | // |
| <input type="radio"/> | Memory Cells | // |
| <input type="radio"/> | I/O Locations | |
| <input checked="" type="radio"/> | All of the given | |

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Question # 4 of 30 (Start time: 10:46:56 PM, 27 December 2020)

Total Marks: 1

All registers of FALCON-A are of _____ while in case of SRC all registers are _____.

Select the correct option

- | | | |
|----------------------------------|------------------|----|
| <input checked="" type="radio"/> | 16-bits , 32 bit | // |
| <input type="radio"/> | 24-bits , 32 bit | // |
| <input type="radio"/> | 32-bits , 16 bit | |
| <input type="radio"/> | 32-bits , 64 bit | |

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Select the correct option

- | | | |
|----------------------------------|------------------|----|
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| <input type="radio"/> | 32-bits , 16 bit | |
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Select the correct option

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<input type="radio"/>	Loader	//

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Question # 14 of 30 (Start time: 10:53:24 PM, 27 December 2020)

Total Ma

Which type of Instruction is stored in instruction register (IR)?

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Select the correct option

- Current instruction being executed
- Next instruction to be executed
- Previously executed instruction
- Next branch instruction in the instruction sequence

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Question # 14 of 30 (Start time: 10:53:24 PM, 27 December 2020)

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Select the correct option

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<input type="radio"/>	Data compressing	//
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<input type="radio"/>	Instruction handling	//

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(PHP, ANDROID, PYTHON)

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- 2) QUIZZES
- 3) GDB
- 4) GRAND QUIZZES

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Quizzes, GDBs, Grand Quizzes
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