

# VIRTUAL UNIVERSITY OF PAKISTAN

A Good Education is a Foundation For a Better Future

CORRECT ANSWER SOLVED BY HADI  
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**TEAM HADI(VU LAHORE)**

## MID TERM PAST PAPER

We Deal All Kind of VU Project  
(PHP, .NET, ANDROID, PYTHON)

**MADE AND SOLVED BY HADI**

WARNING: Team HADI is not responsible for any mistake or wrong answer. All students reading and using this document may check and confirm the answers at their own.



**WhatsApp:03087122922**



**FACEBOOK ID: <https://www.facebook.com/hadipastpapers/>**

Best of luck!



Question # 2 of 30 ( Start time: 09:21:11 PM, 26 December 2020 )

Total Marks: 1

UART is a device used for \_\_\_\_\_ serial communication.

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Select the correct option

 Synchronous Asynchronous**MADE BY HADI**

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Question # 3 of 30 ( Start time: 09:22:13 PM, 26 December 2020 )

Total Marks: 1

BIOS supports \_\_\_\_\_ UARTS as COM Ports.

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Select the correct option

- 6
- 4
- 3
- 2

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Question # 9 of 30 ( Start time: 09:27:54 PM, 26 December 2020 )

Total Marks: 1

The \_\_\_\_ register of Real Time Clock is used to enable interrupt on various events like alarm time and time-up duration.

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Select the correct option

<input type="radio"/>	Status Register C	//
<input type="radio"/>	Status Register A	//
<input type="radio"/>	Status Register D	//
<input checked="" type="radio"/>	Status Register B	//

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Question # 10 of 30 ( Start time: 09:28:36 PM, 26 December 2020 )

Total Marks:

An I/O device cannot be directly connected to the buses so they generally require a \_\_\_\_\_ to be placed between the CPU and I/O device.

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Select the correct option

<input checked="" type="radio"/>	Controller	//
<input type="radio"/>	Pipeline	//
<input type="radio"/>	Data Bus	
<input type="radio"/>	I/O Bus	

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Question # 11 of 30 ( Start time: 09:29:17 PM, 26 December 2020 )

Total Marks: 1

If bit rate in asynchronous serial communication is 9600, then bits are sampled after every \_\_\_\_\_ seconds.

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Select the correct option

<input checked="" type="radio"/>	9600	//
<input type="radio"/>	1/9600	//
<input type="radio"/>	1/96	//
<input type="radio"/>	96	//

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Question # 12 of 30 ( Start time: 09:30:28 PM, 26 December 2020 )

Total Marks: 1

The PPI acts as an interface between the CPU and a parallel \_\_\_\_\_ .

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Select the correct option

<input checked="" type="radio"/>	I/O device	//
<input type="radio"/>	Controller	//
<input type="radio"/>	Control bus	
<input type="radio"/>	Data bus	

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Question # 13 of 30 ( Start time: 09:30:58 PM, 26 December 2020 )

Total Marks: 1

If the processor is 386 then the \_\_\_\_\_ bit of the EFLAGS register will not be set.

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Select the correct option

- |                                  |      |    |
|----------------------------------|------|----|
| <input type="radio"/>            | 12th | // |
| <input checked="" type="radio"/> | 18th | // |
| <input type="radio"/>            | 20th |    |
| <input type="radio"/>            | 24th |    |

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Question # 14 of 30 ( Start time: 09:31:36 PM, 26 December 2020 )

Total Marks:

\_\_\_\_\_ is used to identify the cause of interrupt.

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Select the correct option

Interrupt Enable Register

Interrupt ID Register

Interrupt Status Register

FLAGS Register

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Question # 16 of 30 ( Start time: 09:32:41 PM, 26 December 2020 )

Total Marks: 1

\_\_\_\_\_ interrupt is used to read time from Real Time Clock.

Select the correct option

- 1A/02H
- 1A/03H
- 1A/04H
- 1A/05H

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Question # 17 of 30 ( Start time: 09:33:13 PM, 26 December 2020 )

Total Marks: 1

Hardware Interrupts are \_\_\_\_\_.

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Select the correct option

<input type="radio"/>	Preemptive	//
<input checked="" type="radio"/>	Non-Preemptive	//
<input type="radio"/>	Both Preemptive and Non-Preemptive	
<input type="radio"/>	Neither Preemptive nor Non-Preemptive	

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Question # 18 of 30 ( Start time: 09:33:44 PM, 26 December 2020 )

Total Marks: 1

NMI Stand for \_\_\_\_\_

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Select the correct option

- |                                  |                           |    |
|----------------------------------|---------------------------|----|
| <input type="radio"/>            | Non Maximum Interrupt     | // |
| <input checked="" type="radio"/> | Non Maskable Interrupt    | // |
| <input type="radio"/>            | Non Multitude Interrupt   |    |
| <input type="radio"/>            | Non Multiplexed Interrupt |    |

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Question # 19 of 30 ( Start time: 09:34:09 PM, 26 December 2020 )

Total Marks: 1

If we want to produce the shrill voice from the speaker phone then we have to load the \_\_\_ divisor value at Port \_\_\_.

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Select the correct option

<input type="radio"/>	high, 0x42	//
<input type="radio"/>	low, 0x42	//
<input checked="" type="radio"/>	high, 0x22	
<input type="radio"/>	low, 0x22	

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Question # 21 of 30 ( Start time: 09:35:56 PM, 26 December 2020 )

Total Marks: 1

Timer interrupt approximately occurs \_\_\_\_\_ times every second.

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Select the correct option

- |                                  |      |    |
|----------------------------------|------|----|
| <input type="radio"/>            | 10.8 | // |
| <input type="radio"/>            | 12.8 | // |
| <input type="radio"/>            | 18.0 |    |
| <input checked="" type="radio"/> | 18.2 |    |

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Question # 27 of 30 ( Start time: 09:42:13 PM, 26 December 2020 )

Total Marks:

Interrupt \_\_\_\_\_ is used to get or set the time.

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Select the correct option

<input type="radio"/>	0AH	//
<input checked="" type="radio"/>	1AH	//
<input type="radio"/>	2AH	//
<input type="radio"/>	3AH	//

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Question # 28 of 30 ( Start time: 09:42:53 PM, 26 December 2020 )

Total Marks: 1

DMA is an abbreviation for \_\_\_\_\_.

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Select the correct option

- Distributed Memory Access
- Data Managed Access
- Direct Memory Access
- Dual Memory Access

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Question # 29 of 30 ( Start time: 09:43:18 PM, 26 December 2020 )

Total Marks:

Interrupt \_\_\_\_\_ is empty; we can use its vector as a flag.

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Select the correct option

- 9H
- 13H
- 15H
- 65H

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Question # 30 of 30 ( Start time: 09:43:44 PM, 26 December 2020 )

Total Marks: 1

By cascading two DMA devices, \_\_\_\_\_ bits can be transferred.

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Select the correct option

- |                                  |    |  |
|----------------------------------|----|--|
| <input type="radio"/>            | 4  |  |
| <input type="radio"/>            | 8  |  |
| <input checked="" type="radio"/> | 16 |  |
| <input type="radio"/>            | 32 |  |

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Question # 27 of 30 ( Start time: 09:42:13 PM, 26 December 2020 )

Total Marks:

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Select the correct option

<input type="radio"/>	0AH	//
<input checked="" type="radio"/>	1AH	//
<input type="radio"/>	2AH	//
<input type="radio"/>	3AH	//

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Select the correct option

<input type="radio"/>	Distributed Memory Access
<input type="radio"/>	Data Managed Access
<input checked="" type="radio"/>	Direct Memory Access
<input type="radio"/>	Dual Memory Access

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<input type="radio"/>	Distributed Memory Access
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<input checked="" type="radio"/>	Direct Memory Access
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Total Marks: 1

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Select the correct option

<input type="radio"/>	10.8	//
<input type="radio"/>	12.8	//
<input type="radio"/>	18.0	
<input checked="" type="radio"/>	18.2	

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Question # 3 of 30 ( Start time: 09:22:13 PM, 26 December 2020 )

Total Marks: 1

BIOS supports \_\_\_\_\_ UARTS as COM Ports.

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Select the correct option

- 6
- 4
- 3
- 2

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Select the correct option

<input checked="" type="radio"/>	Controller	//
<input type="radio"/>	Pipeline	//
<input type="radio"/>	Data Bus	
<input type="radio"/>	I/O Bus	

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Select the correct option

- 9H
- 13H
- 15H
- 65H

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Select the correct option

- |                                  |    |  |
|----------------------------------|----|--|
| <input type="radio"/>            | 4  |  |
| <input type="radio"/>            | 8  |  |
| <input checked="" type="radio"/> | 16 |  |
| <input type="radio"/>            | 32 |  |

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Select the correct option

<input type="radio"/>	10.8	//
<input type="radio"/>	12.8	//
<input type="radio"/>	18.0	
<input checked="" type="radio"/>	18.2	

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Total Marks: 1

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Select the correct option

- |                                  |                   |    |
|----------------------------------|-------------------|----|
| <input type="radio"/>            | Status Register C | // |
| <input type="radio"/>            | Status Register A | // |
| <input type="radio"/>            | Status Register D | // |
| <input checked="" type="radio"/> | Status Register B | // |

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# HADI VU PAST PAPER

---

WE DEAL ALL KIND OF VU PROJECT  
(PHP, ANDROID, PYTHON)

# DEAR STUDENTS

ARE YOU LOOKING  
FOR ASSISTANCE

- 1) ASSIGNMENTS
- 2) QUIZZES
- 3) GDB
- 4) GRAND QUIZZES

We Provide, You Solution  
of VU Academic Activities  
include Assignments,  
Quizzes, GDBs, Grand Quizzes  
If You Need Help in Services  
Kindly inform us.

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